

Silicon NPN Power Transistors

2SC1785

**DESCRIPTION**

- With TO-3 package
- High power dissipation

**APPLICATIONS**

- Power amplifier applications
- Power switching applications

**PINNING(see Fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

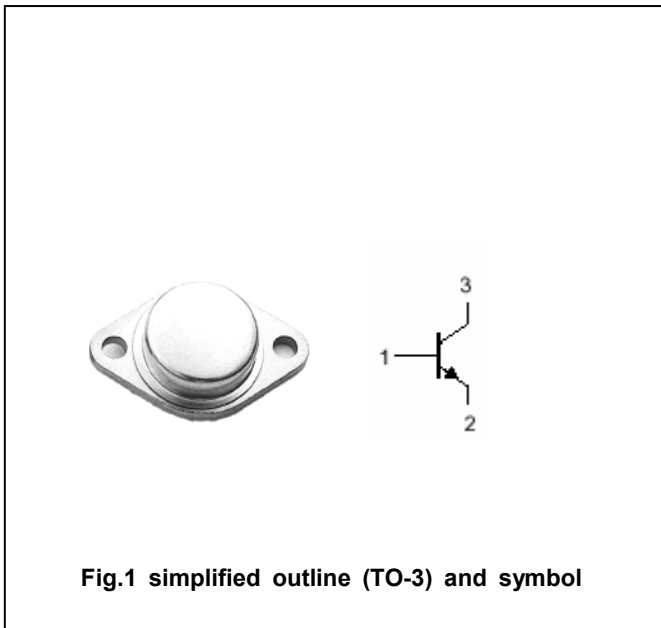


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	200	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	200	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	6	V
I <sub>C</sub>	Collector current		15	A
I <sub>B</sub>	Base current		4	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> =25□	100	W
T <sub>j</sub>	Junction temperature		150	□
T <sub>stg</sub>	Storage temperature		-55~150	□

## Silicon NPN Power Transistors

## 2SC1785

## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =50mA ; I <sub>B</sub> =0	200			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =1mA ; I <sub>C</sub> =0	6			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =10A; I <sub>B</sub> =1A			2.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =10A; I <sub>B</sub> =1A			2.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =200V; I <sub>E</sub> =0			100	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =6V; I <sub>C</sub> =0			100	μA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =5A ; V <sub>CE</sub> =4V	20			

PACKAGE OUTLINE

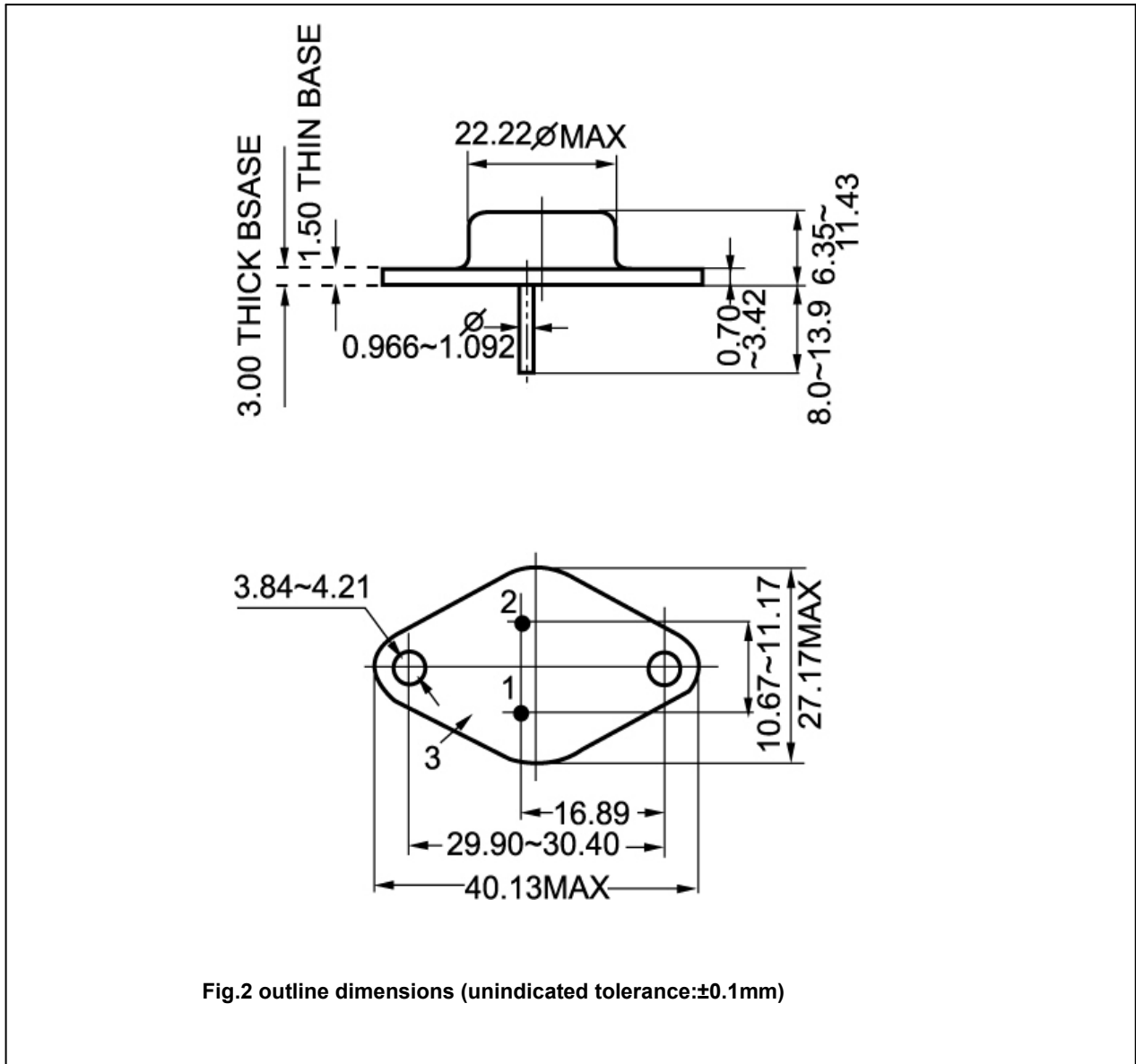


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)