# Transistors

# Digital transistors (built-in resistors) DTB123EK / DTB123ES

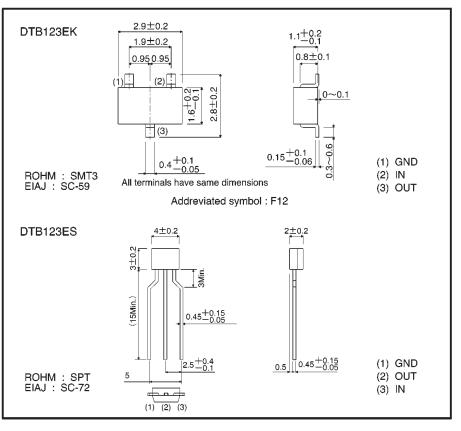
#### Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.

# Structure PNP digital transistor

(Built-in resistor type)

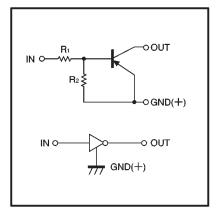




## • Absolute maximum ratings (Ta = $25^{\circ}$ C)

Parameter	Symbol	Limits(D	Unit		
	Symbol	К	S	Unit	
Supply voltage	Vcc	-50		V	
Input voltage	Max	—1	V		
	Vin	-1			
Output current	lc	-500		mA	
Power dissipation	Pd	200	300	mW	
Junction temperature	Tj	150		°C	
Storage temperature	Tstg	-55~+150		Ĉ	

#### Equivalent circuit





### •Electrical characteristics (Ta = $25^{\circ}$ C)

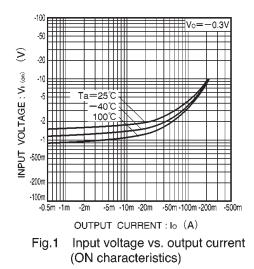
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	—	_	-0.5	v	Vcc=−5V, lo=−100 µ A
	VI(on)	-3	_	-		Vo=-0.3V, Io=-20mA
Output voltage	VO(on)		_	-0.3	V	lo/l=-50mA/-2.5mA
Input current	h	—	—	-3.8	mA	$V_1 = -5V$
Output current	IO(off)	—	_	-0.5	μA	$V_{CC} = -50V, V_I = 0V$
DC current gain	Gi	39	-	_	_	Vo=-5V, Io=-50mA
Input resistance	R1	1.54	2.2	2.86	kΩ	—
Resistance ratio	R2/R1	0.8	1	1.2	_	_
Transition frequency	f⊤		200		MHz	Vce=-10V, Ie=5mA, f=100MHz *

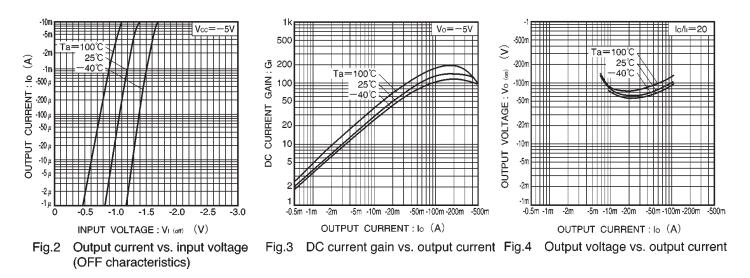
\* Transition frequency of the device

#### Packaging specifications

	Package	SMT3	SPT
	Packaging type	Taping	Taping
	Code	T146	TP
Part No.	Basic ordering unit (pieces)	3000	5000
DTB123EK		0	_
DTB123ES			0

Electrical characteristic curves





ROHM