UTC DTC114Y

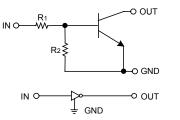
NPN DIGITAL TRANSISTOR

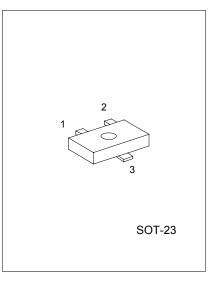
NPN DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

FEATURES

- * Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- * The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- * Only the on/off conditions need to be set for operation, making device design easy.

EQUIVALENT CIRCUIT





1: GND 2: IN 3: OUT

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply voltage	Vcc	50	V	
Input voltage	Vin	-6 ~ +40	V	
Output current	lo	70	mA	
	IC (Max.)	100	IIIA	
Power Dissipation	PD	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 ~ +150	°C	

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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input voltage	VI (off)	Vcc=5V, Io=100 μ A			0.3	V
	VI (on)	Vo=0.3V, Io=1mA	1.4			v
Output voltage	VO (on)	Io/II=5mA/0.25mA		0.1	0.3	V
Input current	lı	VI=5V			0.88	mA
Output current	IO (off)	Vcc=50V, VI=0V			0.5	μA
DC current gain	Gi	Vo=5V, Io=5mA	68			
Input resistance	R1		7	10	13	KΩ
Resistance ratio	R2/R1		3.7	4.7	5.7	
Transition frequency	f⊤	Vce=10V, Ie=-5mA, f=100MHz *		250		MHz

* Transition frequency of the device

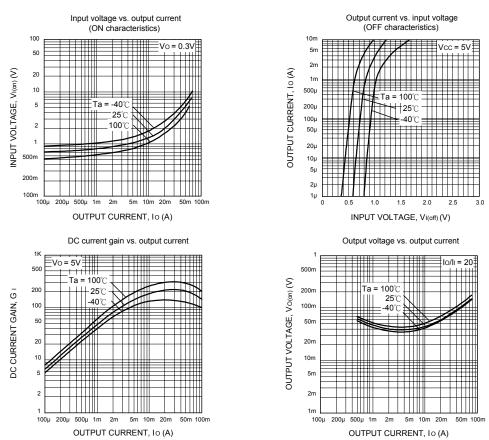
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NPN DIGITAL TRANSISTOR



ELECTRICAL CHARACTERISTIC CURVES

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