Emitter common (dual digital transistors) EMA2 / UMA2N / FMA2A

● Features

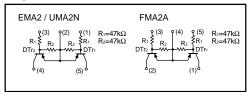
- 1) Two DTA144E transistors in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

Structure

Dual PNP silicon transistor (each with two built in resistors)

The following characteristics apply to both DTr1 and DTr2.

●Equivalent circuit

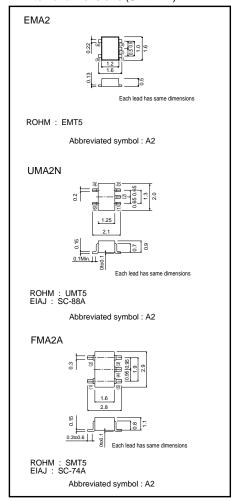


● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	-50	V	
Input voltage		Vin	-40	V	
		VIN	10		
Output current		lo	-30	mA	
		Ic (Max.)	-100		
Power dissipation	EMA2, UMA2N	Pd	150 (TOTAL)	*1 mW *2	
	FMA2A	Fu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded. *2 200mW per element must not be exceeded.

●External dimensions (Unit: mm)



●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	-	-	-0.5	V	Vcc=-5V, Io=-100μA	
	VI (on)	-3	-	-	V	Vo=-0.3V, Io=-2mA	
Output voltage	Vo (on)	-	-0.1	-0.3	V	lo=-10mA/l=-0.5mA	
Input current	lı	-	-	-0.18	mA	VI=-5V	
Output current	IO (off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V	
DC current gain	Gı	68	-	-	-	Vo=-5V, Io=-5mA	
Transition frequency	f⊤	-	250	-	MHz	Vc=-10V, Ie=5mA, f=100MHz *	
Input resistance	R ₁	32.9	47	61.1	kΩ	-	
Resistance ratio	R2/R1	0.8	1	1.2	-	_	

^{*} Transition frequency of the device

Packaging specifications

	Package	Taping						
	Code	T2R	TR	T148				
Туре	Basic ordering unit (pieces)	8000	3000	3000				
EMA2		0	_	_				
UMA2N		_	0	_				
FMA2A		_	_	0				

●Electrical characteristic curves

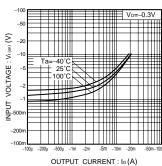


Fig.1 Input voltage vs. output current (ON characteristics)

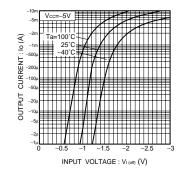


Fig.2 Output current vs. input voltage (OFF characteristics)

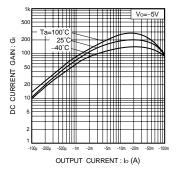


Fig.3 DC current gain vs. output current

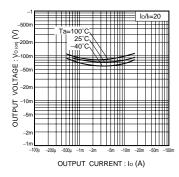


Fig.4 Output voltage vs. output current

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