# Product Preview

# Low-Voltage 1:10 Differential ECL/PECL Clock Driver

The MC100EP111 is a low skew 1–to–10 differential driver, designed with clock distribution in mind. It accepts two clock sources into an input multiplexer. The input signals can be either differential or single–ended if the VBB output is used. The selected signal is fanned out to 10 identical differential outputs.

- 100ps Part-to-Part Skew
- 35ps Output-to-Output Skew
- · Differential Design
- V<sub>BB</sub> Output
- Voltage and Temperature Compensated Outputs
- Low Voltage VEE Range of -2.375 to -3.8V
- 75kΩ Input Pulldown Resistors

The EP111 is specifically designed, modeled and produced with low skew as the key goal. Optimal design and layout serve to minimize gate—to—gate skew within a device, and empirical modeling is used to determine process control limits that ensure consistent tpd distributions from lot to lot. The net result is a dependable, guaranteed low skew device.

To ensure that the tight skew specification is met it is necessary that both sides of the differential output are terminated into  $50\Omega$ , even if only one side is being used. In most applications, all ten differential pairs will be used and therefore terminated. In the case where fewer than ten pairs are used, it is necessary to terminate at least the output pairs on the same package side as the pair(s) being used on that side, in order to maintain minimum skew. Failure to do this will result in small degradations of propagation delay (on the order of 10–20ps) of the output(s) being used which, while not being catastrophic to most designs, will mean a loss of skew margin.

# MC100EP111

LOW-VOLTAGE
1:10 DIFFERENTIAL
ECL/PECL CLOCK DRIVER



FA SUFFIX 32-LEAD TQFP PACKAGE CASE 873A-02

The MC100EP111, as with most other ECL devices, can be operated from a positive V<sub>CC</sub> supply in PECL mode. This allows the EP111 to be used for high performance clock distribution in +3.3V or +2.5V systems. Designers can take advantage of the EP111's performance to distribute low skew clocks across the backplane or the board. In a PECL environment, series or Thevenin line terminations are typically used as they require no additional power supplies. For more information on using PECL, designers should refer to Motorola Application Note AN1406/D.

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.

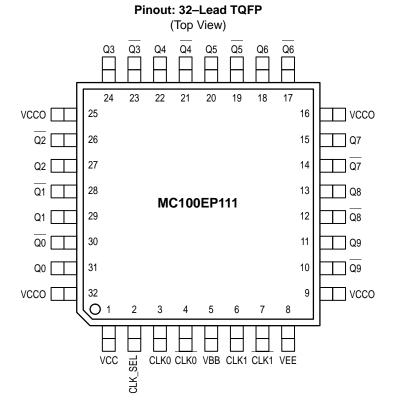


#### **PIN NAMES**

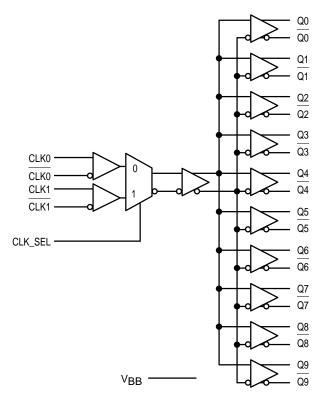
Pins	Function
CLKn, CLKn Q0:9, Q0:9 CLK_SEL VBB	Differential Input Pairs Differential Outputs Active Clock Select Input VBB Output

### **FUNCTION**

CLK_SEL	Active Input
0	CLK0, CLK0
1	CLK1, CLK1



#### **LOGIC SYMBOL**



#### **ECL DC CHARACTERISTICS**

			–40°C		0°C				25°C					
Symbol	Characteristic	Min	Тур	Max	Unit									
VOH	Output HIGH Voltage	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	-1.025	-0.955	-0.880	V
VOL	Output LOW Voltage	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	-1.810	-1.705	-1.620	V
VIH	Input HIGH Voltage	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	-1.165		-0.880	V
V <sub>IL</sub>	Input LOW Voltage	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	-1.810		-1.475	V
V <sub>BB</sub>	Output Reference Voltage	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	-1.38		-1.26	V
VEE	Power Supply Voltage	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	-2.375		-3.8	V
ΊΗ	Input HIGH Current			150			150			150			150	μΑ
IEE	Power Supply Current													mA

### **PECL DC CHARACTERISTICS**

		−40°C			0°C				25°C					
Symbol	Characteristic	Min	Тур	Max	Unit									
V <sub>OH</sub>	Output HIGH Voltage (1.)	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	2.275	2.345	2.420	V
V <sub>OL</sub>	Output LOW Voltage (1.)	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	1.490	1.595	1.680	V
VIH	Input HIGH Voltage (1.)	2.135		2.420	2.135		2.420	2.135		2.420	2.135		2.420	V
V <sub>IL</sub>	Input LOW Voltage (1.)	1.490		1.825	1.490		1.825	1.490		1.825	1.490		1.825	V
V <sub>BB</sub>	Output Reference Voltage (Note 1.)	1.92		2.04	1.92		2.04	1.92		2.04	1.92		2.04	V
VCC	Power Supply Voltage	2.375		3.8	2.375		3.8	2.375		3.8	2.375		3.8	٧
lн	Input HIGH Current			150			150			150			150	μΑ
IEE	Power Supply Current													mA

<sup>1.</sup> These values are for  $V_{CC}$  = 3.3V. Level Specifications will vary 1:1 with  $V_{CC}$ .

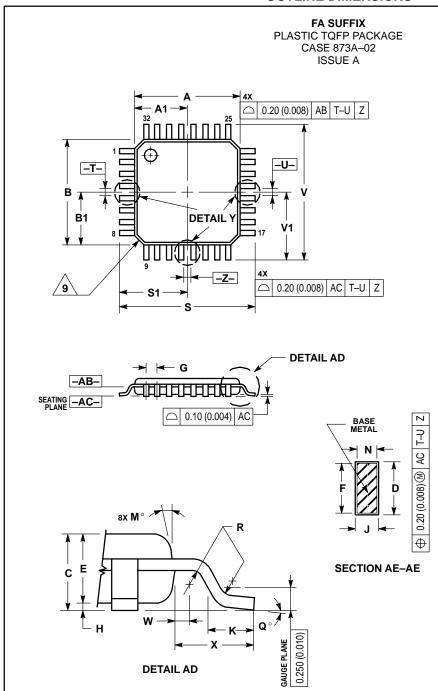
## **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}$ (min) to $V_{EE}$ (max); $V_{CC} = V_{CCO} = GND$ )

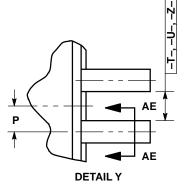
		-40°C		0°C			25°C			85°C					
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Condition
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output IN (differential) IN (single–ended)								400 400					ps	
<sup>t</sup> skew	Within–Device Skew Part–to–Part Skew (Diff)		35 100			35 100			35 100			35 100		ps	
f <sub>max</sub>	Maximum Input Frequency		1.5			1.5			1.5			1.5		GHz	
V <sub>PP</sub>	Minimum Input Swing	500			500			500			500			mV	
VCMR	Common Mode Range													V	
t <sub>r</sub> /t <sub>f</sub>	Output Rise/Fall Time		200			200			200			200		ps	20%-80%

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#### **OUTLINE DIMENSIONS**





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.

- 14.3M, 1932.
  2 CONTROLLING DIMENSION: MILLIMETER.
  3. DATUM PLANE -AB IS LOCATED AT BOTTOM OF LEAD AND IS COINCIDENT WITH THE LEAD WHERE THE LEAD EXITS THE PLASTIC BODY AT THE BOTTOM OF THE PARTING LINE.
  4. DATUMS -T-, -U-, AND -Z- TO BE DETERMINED AT DATUM PLANE -AB-.
  5. DIMENSIONS S AND V TO BE DETERMINED AT SEATING PLANE -AC-.
  6. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE PROTRUSION IS 0.250 (0.010) PER SIDE. DIMENSIONS A AND B DO INCLUDE MOLD MISMATCH AND ARE DETERMINED AT DATUM PLANE -AB-.
  7. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL NOT CAUSE THE D DIMENSION OF EXCEED 0.520 (0.020).
- 0.520 (0.020).

  8. MINIMUM SOLDER PLATE THICKNESS SHALL BE

- 0.0076 (0.0003).

  9. EXACT SHAPE OF EACH CORNER MAY VARY FROM DEPICTION.

	MILLIN	IETERS	INCHES					
DIM	MIN	MAX	MIN	MAX				
A		BSC	0.276 BSC					
A1		BSC		BSC				
В		BSC	0.276	BSC				
B1	3.500	BSC	0.138	BSC				
C	1.400	1.600	0.055	0.063				
D	0.300	0.450	0.012	0.018				
E	1.350	1.450	0.053	0.057				
F	0.300	0.400	0.012	0.016				
G	0.800	BSC	0.031 BSC					
Н	0.050	0.150	0.002	0.006				
J	0.090	0.200	0.004	0.008				
K	0.500	0.700	0.020 0.028					
M	12°	REF	12° REF					
N	0.090	0.160	0.004	0.006				
Р		BSC	0.016 BSC					
Q	1°	5°	1°	5°				
R	0.150	0.250	0.006	0.010				
S	9.000		0.354 BSC					
S1	4.500		0.177 BSC					
٧	9.000		0.354 BSC					
V1	4.500		0.177 BSC					
W		REF	0.008 REF					
Х	1.000	REF	0.039 REF					

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