## Differential ECL to TTL Translator

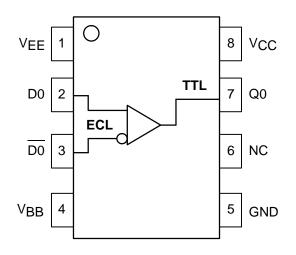
The MC10ELT/100ELT25 is a differential ECL to TTL translator. Because ECL levels are used a +5V, -5.2V (or -4.5V) and ground are required. The small outline 8-lead SOIC package and the single gate of the ELT25 makes it ideal for those applications where space, performance and low power are at a premium. Because the mature MOSAIC 1.5 process is used, low cost can be added to the list of features.

The V<sub>BB</sub> output allows the ELT25 to also be used in a single-ended input mode. In this mode the V<sub>BB</sub> output is tied to the IN input for a non-inverting buffer or the IN input for an inverting buffer. If used the V<sub>BB</sub> pin should be bypassed to ground via a 0.01µF capacitor.

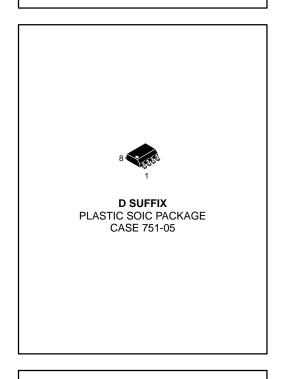
The ELT25 is available in both ECL standards: the 10ELT is compatible with MECL 10H logic levels while the 100ELT is compatible with ECL 100K logic levels.

- 2.6ns Typical Propagation Delay
- Differential ECL Inputs
- Small Outline SOIC Package
- 24mA TTL Outputs
- Flow Through Pinouts

### LOGIC DIAGRAM AND PINOUT ASSIGNMENT







PIN DESCRIPTION								
PIN	FUNCTION							
D Q VCC VEE VBB GND	Diff ECL Inputs TTL Output Positive Supply Negative Supply Reference Output Ground							

MOTOROLA

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### MC10ELT25 MC100ELT25

### **MAXIMUM RATINGS\***

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND, $V_{EE} = -5.2$ )	7.0	V
VEE	DC Supply Voltage (Referenced to GND, $V_{CC} = 5.0$ )	-8.0	V
VIN	Input Voltage	0 to V <sub>CC</sub>	V
IOUT	Current Applied to Output in Low Output State Continuous Surge		mA
Т <sub>А</sub>	Operating Temperature Range (In Free-Air)	-40 to 85	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

\* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

# TTL OUTPUT DC CHARACTERISTICS (V<sub>CC</sub> = 4.5V to 5.5V; V<sub>EE</sub> = -4.2V to -5.5V 100ELT, -4.94V to -5.5V 10ELT; $T_A = -40^{\circ}C$ to 85°C)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
VOH	Output HIGH Voltage	2.4			V	I <sub>OH</sub> = -3.0mA
V <sub>OL</sub>	Output LOW Voltage			0.5	V	I <sub>OL</sub> = 24mA
ІССН	Power Supply Current		11	16	mA	
ICCL	Power Supply Current		13	18	mA	
IEE	Power Supply Current		15	21	mA	
IOS	Output Short Circuit Current	-150		-60	mA	

### ECL INPUT DC CHARACTERISTICS

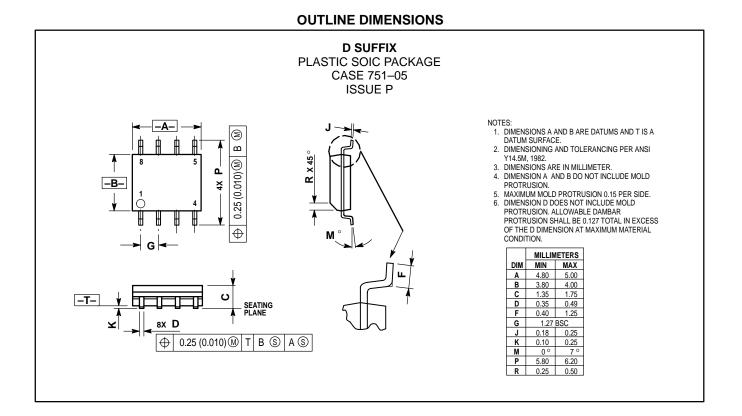
(V<sub>CC</sub> = 4.5V to 5.5V; V<sub>EE</sub> = –4.2V to –5.5V 100ELT, –4.94V to –5.5V 10ELT;  $T_A$  = –40°C to 85°C)

		-40	D∘C	°C 0°C 25°C			85°C					
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
IIН	Input HIGH Current		150		150			150		150	μΑ	
۱ <sub>IL</sub>	Input LOW Current	0.5		0.5		0.5			0.5		μΑ	
VCMR	Common Mode Range	V <sub>EE</sub> + 2.2	VCC	V <sub>EE</sub> + 2.2	VCC	V <sub>EE</sub> + 2.2		VCC	V <sub>EE</sub> + 2.2	VCC	V	
V <sub>PP</sub>	Minimum Peak-to-Peak Input <sup>1</sup>	200		200		200			200		mV	
VIH	Input HIGH Voltage 10ELT 100ELT	-1230 -1165	-890 -880	-1170 -1165	-840 -880	-1130 -1165		-810 -880	-1060 -1165	-720 -880	V	
V <sub>IL</sub>	Input LOW Voltage 10ELT 100ELT	-1950 -1810	-1500 -1475	-1950 -1810	-1480 -1475	-1950 -1810		-1480 -1475	-1950 -1810	-1445 -1475	V	
$V_{BB}$	Reference Output 10ELT 100ELT	-1.43 -1.38	-1.30 -1.26	-1.38 -1.38	-1.27 -1.26	-1.35 -1.38		-1.25 -1.26	-1.31 -1.38	-1.19 -1.26	V	

1. 200mV input guarantees full logic swing at the output.

### **AC CHARACTERISTICS** (V<sub>CC</sub> = 4.5V to 5.5V; V<sub>EE</sub> = -4.2V to -5.5V 100ELT, -4.94V to -5.5V 10ELT; T<sub>A</sub> = $-40^{\circ}$ C to $85^{\circ}$ C)

		_40°C		0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
<sup>t</sup> PLH	Propagation Delay	1.7	3.6	1.7	3.6	1.7		3.6	1.7	3.6	ns	$C_L = 20 pF$
<sup>t</sup> PHL	Propagation Delay	2.6	4.1	2.6	4.1	2.6		4.1	2.6	4.1	ns	$C_L = 20 pF$



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MC10ELT25/D

