

Silicon PNP Power Transistors

2SB995

DESCRIPTION

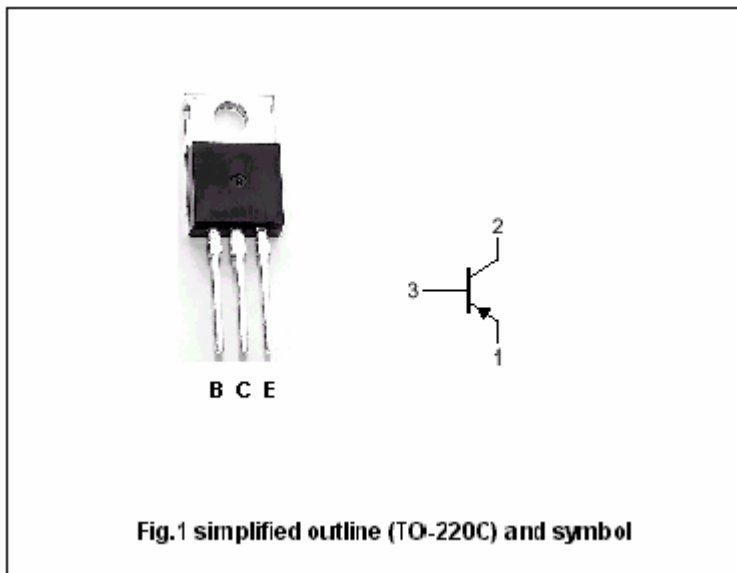
- With TO-220C package
- High current capacity
- Low collector saturation voltage

APPLICATIONS

- For audio frequency amplifier output stage applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Tc=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-100	V
V _{CEO}	Collector-emitter voltage	Open base	-100	V
V _{EBO}	Emitter-base voltage	Open collector	-6	V
I _C	Collector current		-5	A
P _C	Collector power dissipation	T _C =25°C	40	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-30mA; I _B =0	-100			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA; I _E =0	-100			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA; I _C =0	-6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-3A; I _B =-0.3 A			-1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-3A; I _B =-0.3 A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-100V; I _E =0			-0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-0.1	mA
h _{FE-1}	DC current gain	I _C =-1A ; V _{CE} =-5V	40		240	
h _{FE-2}	DC current gain	I _C =-3A ; V _{CE} =-5V	20			
f _T	Transition frequency	I _C =-1A ; V _{CE} =-5V	5			MHz

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance:±0.10 mm)