

Silicon PNP Power Transistors

2SB1096

DESCRIPTION

- With TO-220Fa package
- High breakdown voltage
- Complement to type 2SD1587

APPLICATIONS

- For TV vertical output applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector
3	Base

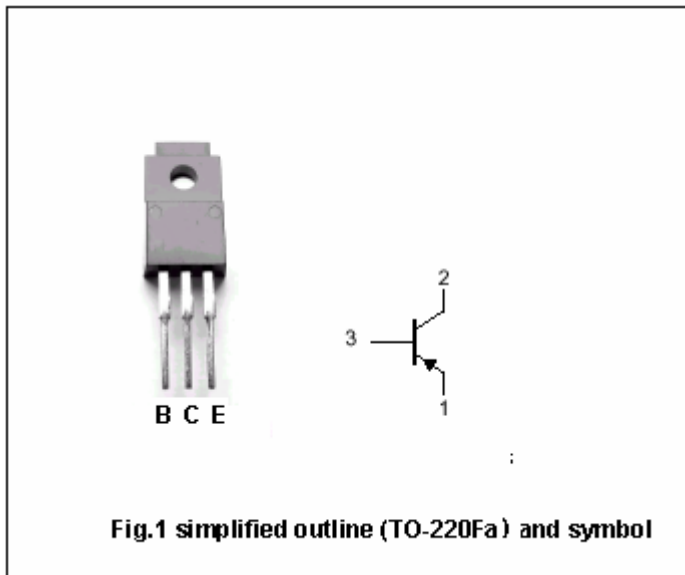


Fig.1 simplified outline (TO-220Fa) and symbol

Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-200	V
V _{CEO}	Collector -emitter voltage	Open base	-150	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-2	A
P _C	Collector power dissipation	T _a =25°C	2.0	W
		T _C =25°C	25	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-30mA; I _B =0	-150			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-500mA; I _B =-50mA			-1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-500mA; I _B =-50mA			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-150V; I _E =0			-50	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-50	μA
h _{FE}	DC current gain	I _C =-0.4A; V _{CE} =-10V	40		200	
f _T	Transition frequency	I _C =-0.4A; V _{CE} =-10V		5		MHz

◆ h_{FE} Classifications

M	L	K
40-80	60-120	100-200

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PACKAGE OUTLINE

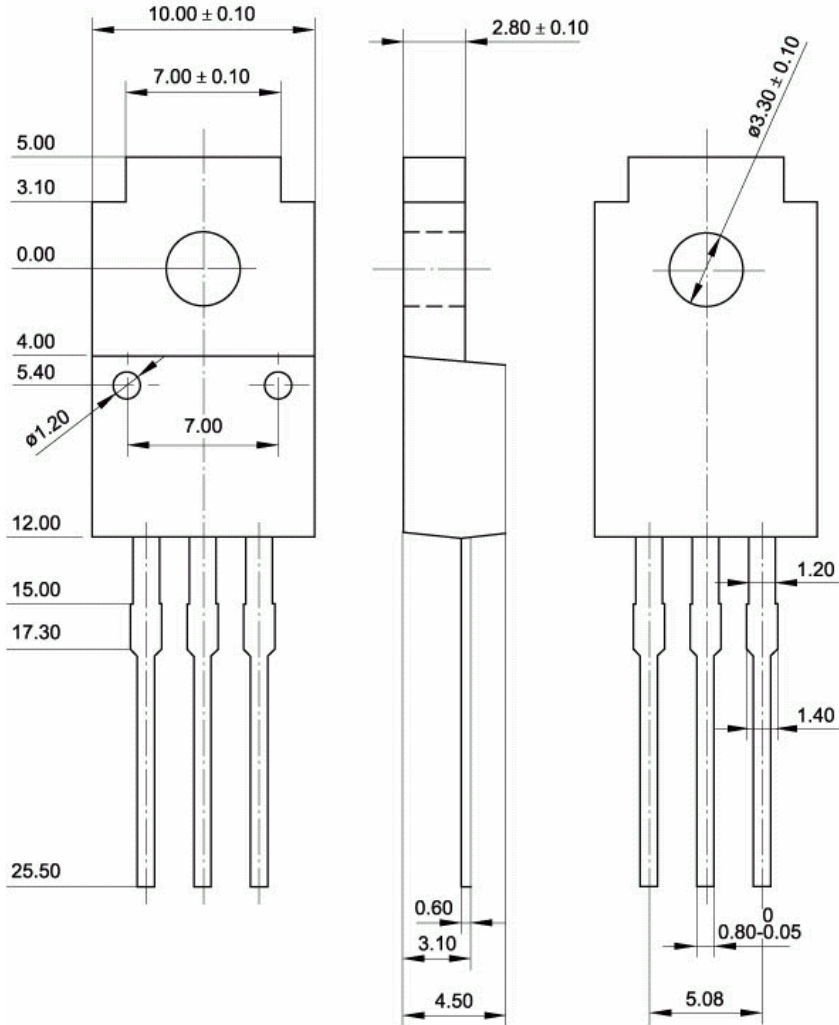


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)