## **DISCRETE SEMICONDUCTORS**

## DATA SHEET

# PMBT3640 PNP 1 GHz switching transistor

Product specification
File under Discrete Semiconductors, SC14

September 1995







## **PNP 1 GHz switching transistor**

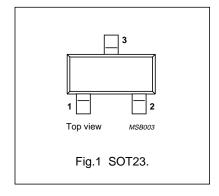
## **PMBT3640**

## **DESCRIPTION**

PNP general purpose switching transistor in a SOT23 package.

## **PINNING**

PIN	PIN DESCRIPTION	
	Code: V25	
1	base	
2	emitter	
3	collector	



## **LIMITING VALUES**

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
-V <sub>CBO</sub>	collector-base voltage	open emitter	_	12	V
-V <sub>CEO</sub>	collector-emitter voltage	open base	_	12	V
-V <sub>EBO</sub>	emitter-base voltage	open collector	_	4	V
-I <sub>C</sub>	DC collector current		_	80	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>s</sub> = 85 °C (note 1)	_	350	mW
T <sub>stg</sub>	storage temperature		-55	150	°C
Tj	junction temperature		_	175	°C

#### THERMAL RESISTANCE

SYMBOL	PARAMETER	THERMAL RESISTANCE
R <sub>th j-s</sub>	from junction to soldering point (note 1)	260 K/W

#### Note

1.  $T_s$  is the temperature at the soldering point of the collector tab.

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## **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Off charact	eristics		'	'	
-V <sub>(BR)CBO</sub>	collector-base breakdown voltage	$-I_C = 100 \mu\text{A}; I_E = 0$	12	_	٧
-V <sub>(BR)CES</sub>	collector-emitter breakdown voltage	$-I_C = 100 \mu\text{A};  V_{BE} = 0$	12	_	٧
-V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$-I_E = 100 \mu\text{A};  I_C = 0$	4	_	٧
-I <sub>CES</sub>	collector cut-off current	$-V_{CE} = 6 \text{ V}; V_{BE} = 0$	_	0.01	μΑ
		$-V_{CE} = 6 \text{ V}; V_{BE} = 0; T_{amb} = 65 ^{\circ}\text{C}$	_	1	μΑ
-l <sub>B</sub>	base current	$-V_{CE} = 6 \text{ V}; V_{BE} = 0$	_	10	nA
On charact	eristics; pulse test: pulse width ≤ 300	μs, duty cycle ≤ 2%.	•	•	•
h <sub>FE</sub>	DC current gain	$-I_C = 10 \text{ mA}; -V_{CE} = 0.3 \text{ V}$	30	120	
		$-I_C = 50 \text{ mA}; -V_{CE} = 1 \text{ V}$	20	_	
-V <sub>CEsat</sub>	collector-emitter saturation voltage	$-I_C = 10 \text{ mA}; -I_B = 1 \text{ mA}$	_	0.2	V
		$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	_	0.6	V
		$-I_C = 10 \text{ mA}; -I_B = 1 \text{ mA}; T_{amb} = 65 ^{\circ}C$	_	0.25	V
-V <sub>BEsat</sub>	base-emitter saturation voltage	$-I_C = 10 \text{ mA}; -I_B = 0.5 \text{ mA}$	0.75	0.95	V
		$-I_C = 10 \text{ mA}; -I_B = 1 \text{ mA}$	0.8	1	V
		$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	_	1.5	V
Small-signa	al characteristics		•	!	!
f <sub>T</sub>	transition frequency	$-I_C = 10 \text{ mA}; -V_{CE} = 5 \text{ V};$ f = 100 MHz	500	_	MHz
C <sub>c</sub>	output capacitance	I <sub>E</sub> = 0; -V <sub>CB</sub> = 5 V; f = 1 MHz	_	3.5	pF
C <sub>e</sub>	input capacitance	$I_C = 0$ ; $-V_{EB} = 0.5 \text{ V}$ ; $f = 1 \text{ MHz}$	_	3.5	pF
Switching t	times				
t <sub>d</sub>	delay time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-V_{BE(off)} = 1.9 \text{ V}; -I_{B1} = 5 \text{ mA}$	_	10	ns
t <sub>s</sub>	storage time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-I_{B1} = -I_{B2} = 5 \text{ mA}$	_	20	ns
t <sub>r</sub>	rise time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-V_{BE(off)} = 1.9 \text{ V}; -I_{B1} = 5 \text{ mA}$	_	30	ns
t <sub>f</sub>	fall time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-I_{B1} = -I_{B2} = 5 \text{ mA}$	-	12	ns
t <sub>on</sub>	turn-on time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-V_{BE(off)} = 1.9 \text{ V}; -I_{B1} = 5 \text{ mA}$	_	25	ns
		$-V_{CC} = 1.5 \text{ V}; -I_{C} = 10 \text{ mA};$ $-I_{B1} = 0.5 \text{ mA}$	_	60	ns
t <sub>off</sub>	turn-off time	$-V_{CC} = 6 \text{ V}; -I_{C} = 50 \text{ mA};$ $-V_{BE(off)} = 1.9 \text{ V}; -I_{B1} = I_{B2} = 5 \text{ mA}$	_	35	ns
		$-V_{CC} = 1.5 \text{ V}; -I_{C} = 10 \text{ mA};$ $-I_{B1} = I_{B2} = 0.5 \text{ mA}$	_	75	ns

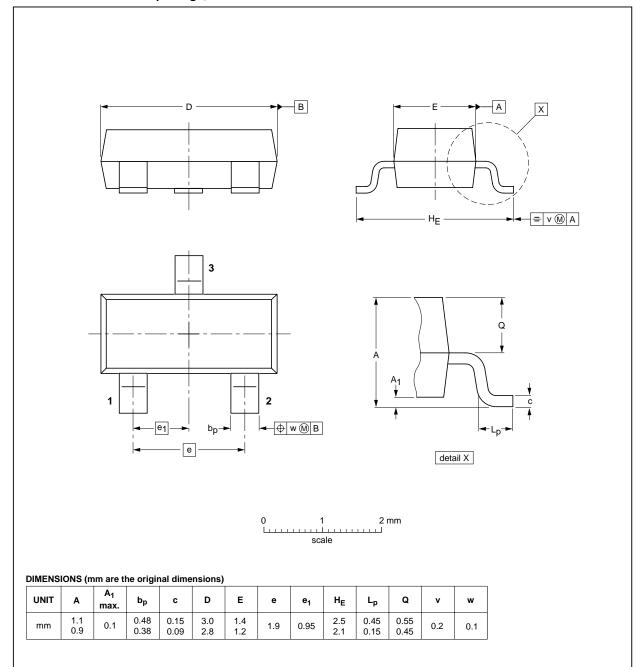
## PNP 1 GHz switching transistor

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## **PACKAGE OUTLINE**

## Plastic surface mounted package; 3 leads

SOT23



OUTLINE

VERSION

SOT23

IEC

EIAJ

**EUROPEAN** 

**PROJECTION** 

ISSUE DATE

97-02-28

REFERENCES

**JEDEC** 

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#### **DEFINITIONS**

Data Sheet Status		
Objective specification	This data sheet contains target or goal specifications for product development.	
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.	
Product specification	This data sheet contains final product specifications.	
Limiting values		
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation		

more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

## **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

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