

Silicon NPN Power Transistors

2SD1208

DESCRIPTION

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- With TO-3 package
- Wide area of safe operation
- High DC current gain
- Darlington

APPLICATIONS

- Power regulator for line operated TV

PINNING

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

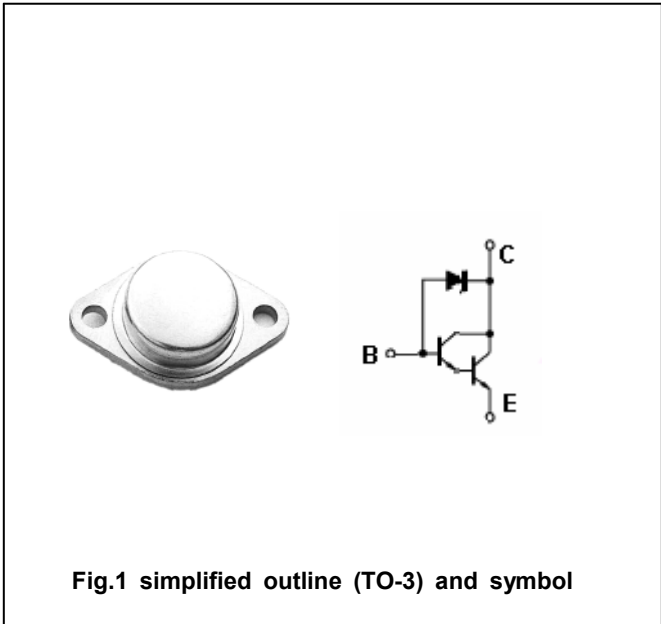


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	60±15	V
V _{CEO}	Collector-emitter voltage	Open base	60±15	V
V _{EBO}	Emitter-base voltage	Open collector	6	V
I _C	Collector current (DC)		5	A
I _{CP}	Collector current (Pulse)		20	A
P _C	Collector power dissipation	T _C =25°C	100	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-65~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO}	Collector-emitter breakdown voltage	I _C =100mA ; I _B =0	45		75	V
V _{CBO}	Collector-base breakdown voltage	I _C =100mA ; I _E =0	45		75	V
V _{CEsat-1}	Collector-emitter saturation voltage	I _C =0.5A ; I _B =1mA			1.5	V
V _{CEsat-2}	Collector-emitter saturation voltage	I _C =1A ; I _B =1mA			2.5	V
V _{BE}	Base-emitter on voltage	I _C =0.5A ; V _{CE} =5V			1.8	V
I _{EBO}	Emitter cut-off current	V _{EB} =6V ; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =0.5A ; V _{CE} =5V	2000		20000	

PACKAGE OUTLINE

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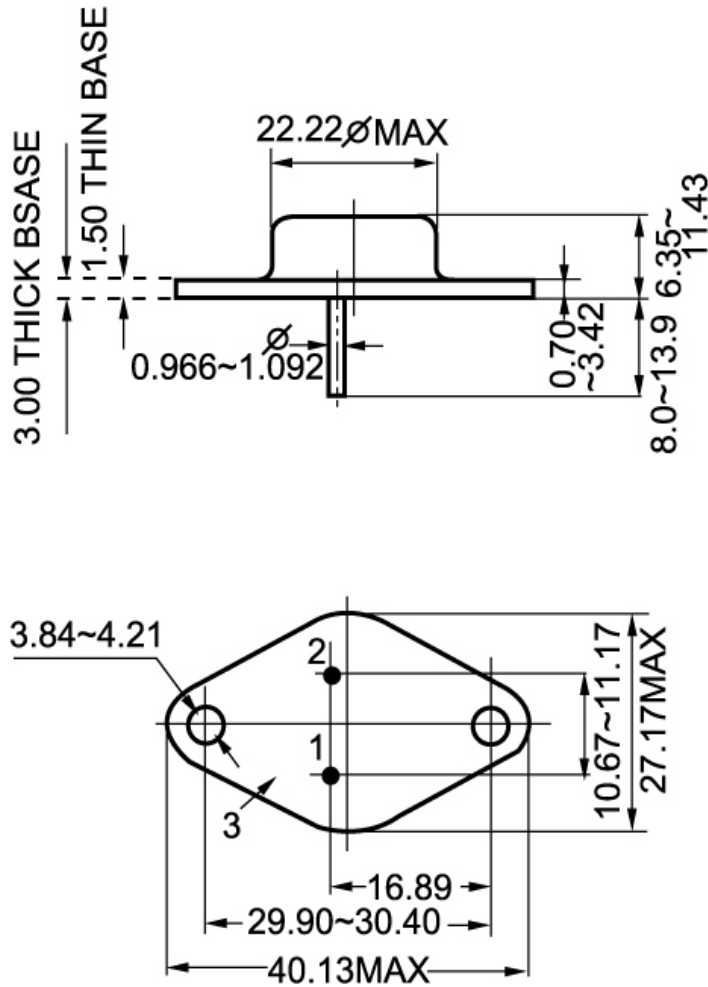


Fig.2 outline dimentions