

Silicon NPN Power Transistors

2SD1133 2SD1134

DESCRIPTION

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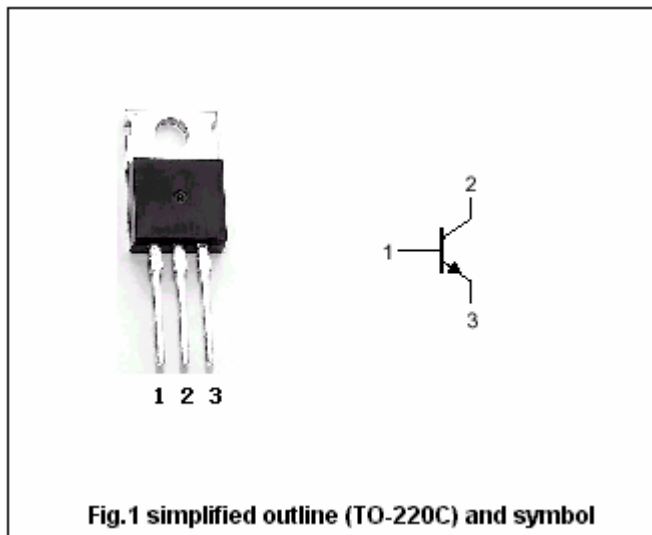
- With TO-220C package
- Complement to type 2SB857/858

APPLICATIONS

- For low frequency power amplifier applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings(Tc=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	70	V
V _{CEO}	Collector-emitter voltage	2SD1133	50	V
		2SD1134	60	
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		4	A
I _{CP}	Collector current-peak		8	A
P _C	Collector power dissipation	T _C =25°C	40	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-45~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	2SD1133	50			V
		2SD1134				
		I _C =50mA; R _{BE} =∞				
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =10μA; I _E =0	70			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =10μA; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =2A; I _B =0.2A			1.0	V
V _{BE}	Base-emitter voltage	I _C =1A; V _{CE} =4V			1.0	V
I _{CBO}	Collector cut-off current	V _{CB} =50V; I _E =0			1	μA
h _{FE-1}	DC current gain	I _C =1A; V _{CE} =4V	60		320	
h _{FE-2}	DC current gain	I _C =0.1A; V _{CE} =4V	35			
f _T	Transition frequency	I _C =0.5A; V _{CE} =4V		7		MHz

◆ h_{FE-1} classifications

B	C	D
60-120	100-200	160-320

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PACKAGE OUTLINE

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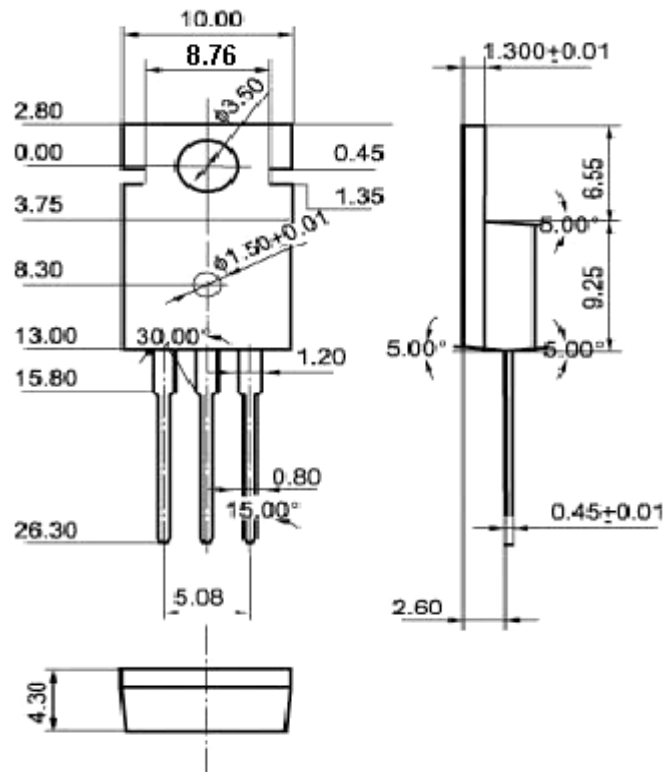


Fig.2 Outline dimensions (unindicated tolerance: ± 0.10 mm)

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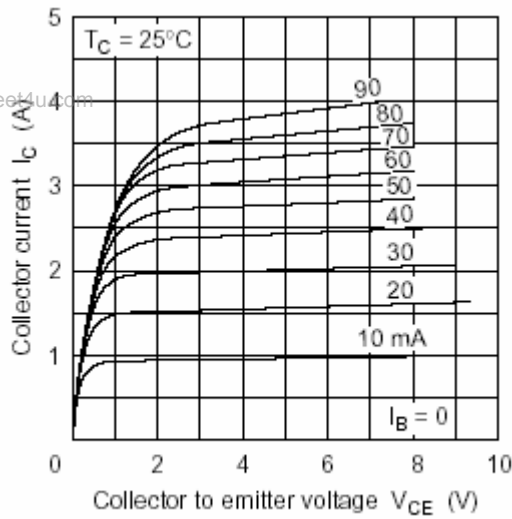


Fig.3 Static Characteristic

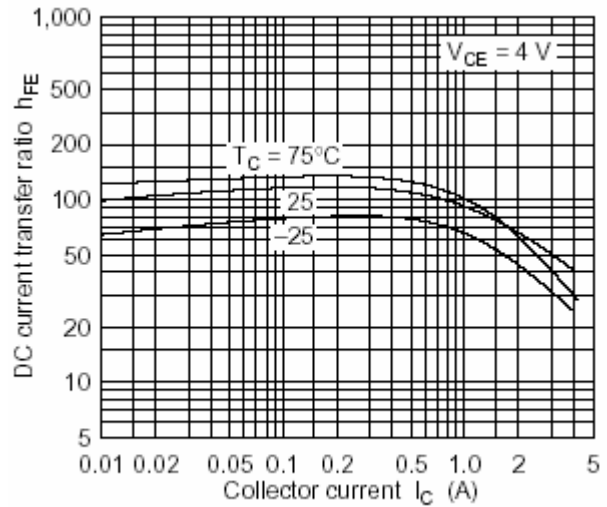


Fig.4 DC current Gain

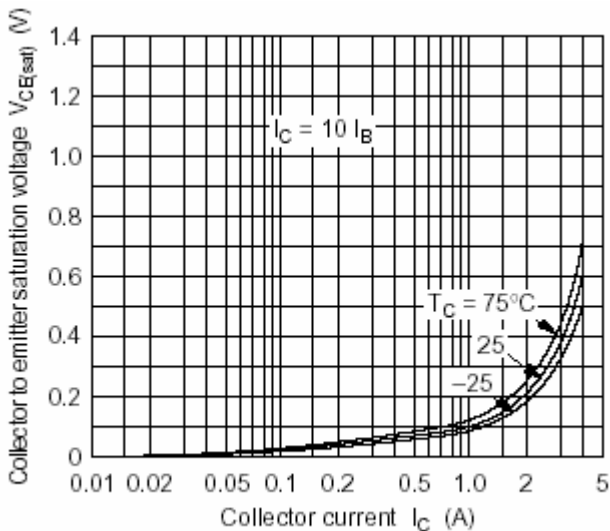


Fig.5 Collector-Emitter Saturation Voltage

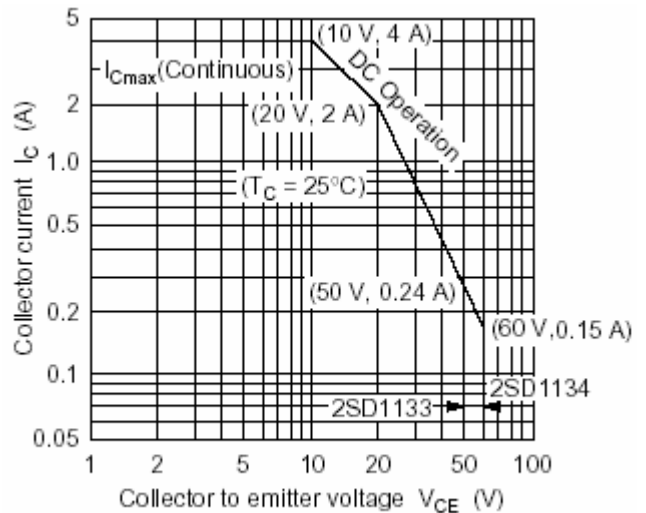


Fig.6 Safe Operating Area