

**M3 SERIES**
**P10LU-XXXXZ 3KV ISOLATED 2W UNREGULATED DUAL OUTPUT SIP7**
**Available Inputs:**
**5, 12, 24 and 48 VDC**
**Available Outputs:**
**(+/-) 5, 7.2, 12 and 15 VDC**

Other specifications please enquire

**Electrical Specifications**

(Typical at + 25° C, nominal input voltage, rated output current unless otherwise specified)

**Input Specifications**

Voltage range +/- 10 %

Filter Capacitors

**Isolation Specifications**

Rated voltage 3000 VDC

Leakage current 1 mA

 Resistance 10<sup>9</sup> Ohm

Capacitance 60 pF typ.

**Output Specifications**

Voltage accuracy +/- 5 %, max.

Ripple and noise (at 20 MHz BW) 75 mV p-p, max.

Short circuit protection Momentary

Line voltage regulation +/- 1,2 % / 1,0 % of Vin

Load voltage regulation +/- 8 %, load = 20 ~ 100 %

Temperature coefficient +/- 0,02 % / °C

**General Specifications**

Efficiency 70 % to 85 %

Switching frequency 85 KHz, typ.

**Environmental Specifications**

Operating temperature (ambient) - 40° C to + 85° C

Storage temperature - 55 °C to + 125 °C

Derating See graph

Humidity Up to 90 %, non condensing

Cooling Free air convection

**Physical Characteristics**

Dimensions SIP 19,50 x 6,00 x 9,50 mm

0,76 x 0,24 x 0,37 inches

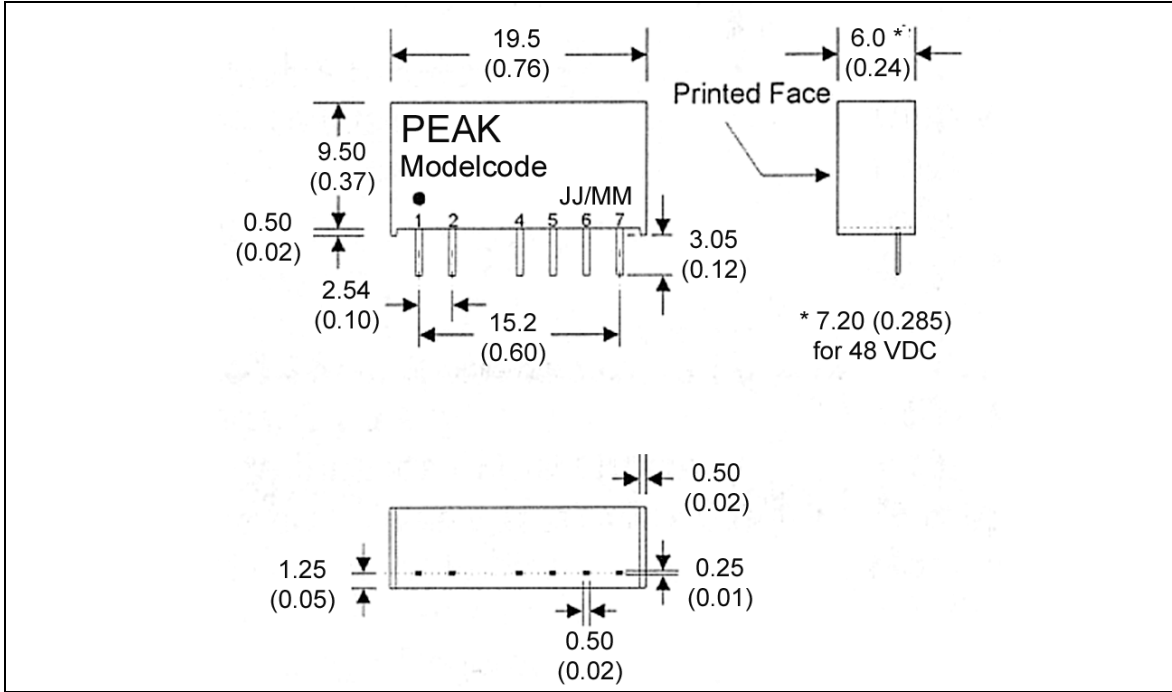
Weight 2,5 g 3,5 g for 48 VDC

Case material Non conductive black plastic

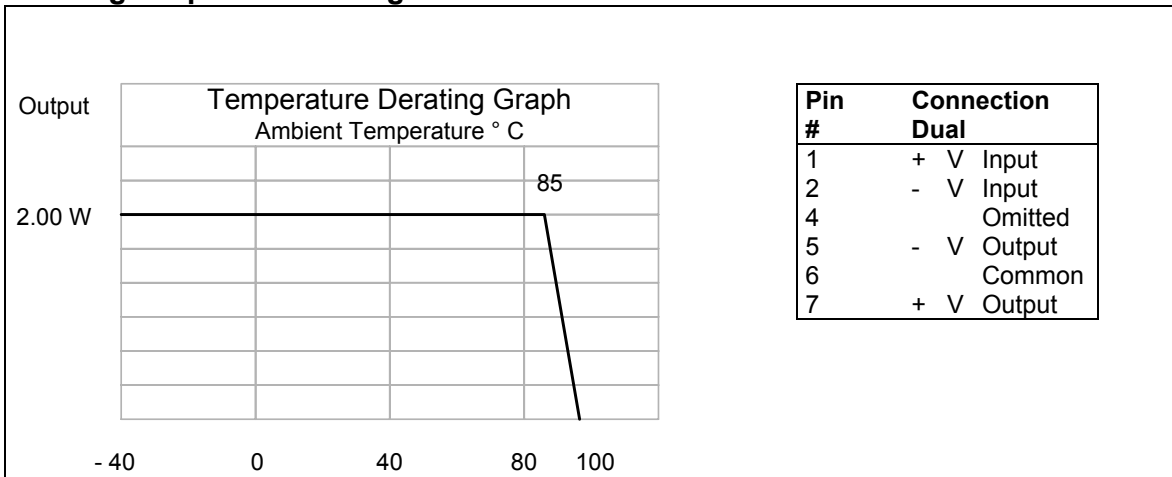
**Examples of Partnumbers/Modelcode**

PART NO.	INPUT VOLTAGE (VDC)	INPUT CURRENT NO LOAD	INPUT CURRENT FULL LOAD	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (max. mA)	EFFICIENCY FULL LOAD (% TYP.)
P10LU-0505Z	5	28	555	+/- 5	+/- 200	72
P10LU-0512Z	5	26	500	+/- 12	+/- 84	80
P10LU-0515Z	5	25	494	+/- 15	+/- 67	81
P10LU-1205Z	12	22	225	+/- 5	+/- 200	74
P10LU-1212Z	12	20	208	+/- 12	+/- 84	80
P10LU-1215Z	12	20	208	+/- 15	+/- 67	80
P10LU-2405Z	24	11	111	+/- 5	+/- 200	75
P10LU-247R2Z	24	10	107	+/- 7,2	+/- 139	78
P10LU-2412Z	24	8	102	+/- 12	+/- 84	81
P10LU-2415Z	24	8	102	+/-15	+/- 67	82

**Dimensions**



**Derating Graph and Pinning**



Specification can be changed without notice.

Copyright PEAK electronics GmbH

REV:2 / 09.2001