

EMP104-Q5

5.0 - 6.5 GHz Surface-Mounted PA

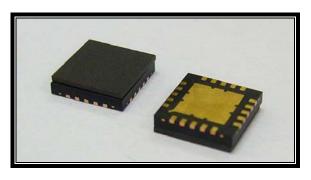
UPDATED: 04/24/2008

FEATURES

- 5.0 6.5 GHz Operating Frequency Range
- 33.0dBm Output Power at 1dB Compression
- 17.0 dB Typical Small Signal Gain
- -44dBc OIMD3 @Each Tone Pout 22.0dBm

APPLICATIONS

- Point-to-point and point-to-multipoint radio
- Military Radar Systems





Caution! ESD sensitive device.

ELECTRICAL CHARACTERISTICS (T_a = 25 °C, 50 ohm, VDD=10V, IDQ=1000mA)

SYMBOL	PARAMETER/TEST CONDITIONS	MIN	TYP	MAX	UNITS
F	Operating Frequency Range	5.0		6.5	GHz
P1dB	Output Power at 1dB Gain Compression	32.0	33.0		dBm
Gss	Small Signal Gain	15.0	17.0		dB
OIMD3	Output 3 rd Order Intermodulation Distortion @∆f=10MHz, Each Tone Pout 22.0dBm		-44	-40	dBc
Input RL	Input Return Loss		-6		dB
Output RL	Output Return Loss		-6		dB
ldss	Saturate Drain Current V _{DS} =3V, V _{GS} =0V		1680		mA
V _{DD}	Power Supply Voltage		10		V
Rth	Thermal Resistance ¹		7.5		°C/W
Tb	Operating Base Plate Temperature	-35		+75	°C

ABSOLUTE MAXIMUM RATINGS FOR CONTINUOUS OPERATION^{2,3}

SYMBOL	CHARACTERISTIC	CONTINUOUS	
V_{DS}	Drain to Source Voltage	10 V	
V_{GS}	Gate to Source Voltage	-4 V	
I_{DD}	Drain Current	ldss	
I_{GSF}	Forward Gate Current	35mA	
P_{IN}	Input Power	@ 3dB compression	
T_CH	Channel Temperature	150°C	
T_{STG}	Storage Temperature	-65/150°C	
P_T	Total Power Dissipation	17W	

^{1.} R_{th} is mounting dependent. Measured result when used with Excelics recommended evaluation board. Adequate heat sinking recommended.

Specifications are subject to change without notice.

^{2.} Operating the device beyond any of the above rating may result in permanent damage.

^{3.} Bias conditions must also satisfy the following equation $V_{DS}*I_{DS} < (T_{CH} - T_{HS})/R_{TH}$; where T_{HS} = ambient temperature

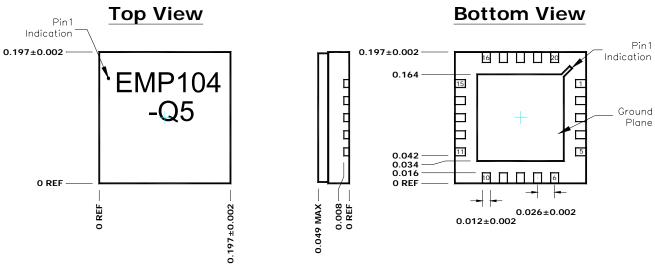




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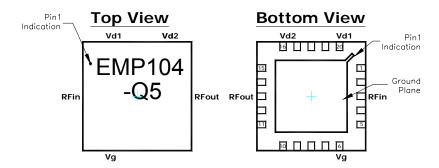
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CHIP OUTLINE AND PIN ASSIGNMENT



Additional Notes:

- 1) Ground Plane must be soldered to PCB RF ground
- 2) All dimensions are in inches
- 3) Refer to Excelics application notes on QFNs for further guidelines
- 4) Pin Assignment:



Pin	Assignment
1, 2, 4, 5	NC
3	RF _{in}
6	V_{q}
7, 8, 9, 10, 11, 12, 14, 15	NČ
13	RF _{out}
16	V_{d2}
17, 18, 19	NC
20	V _{d1}



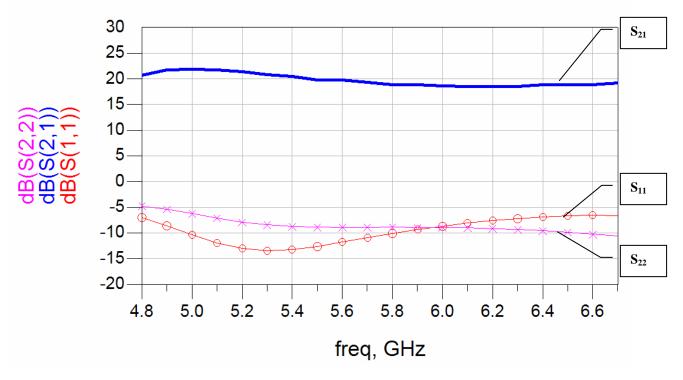


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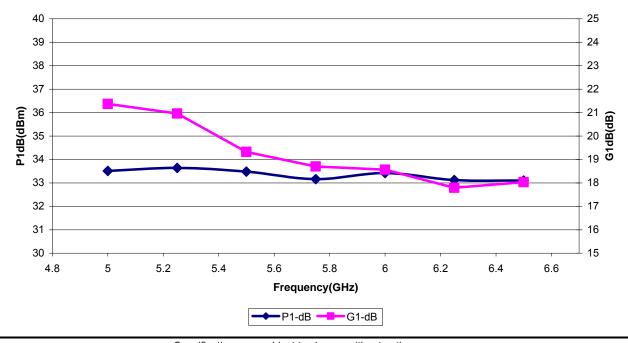
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Typical Performance:

1. Small-Signal Parameters(@Vds = 10V, lds = 1000mA)



2. P1-dB & G1-dB (@Vds = 10V, Ids = 1000mA)



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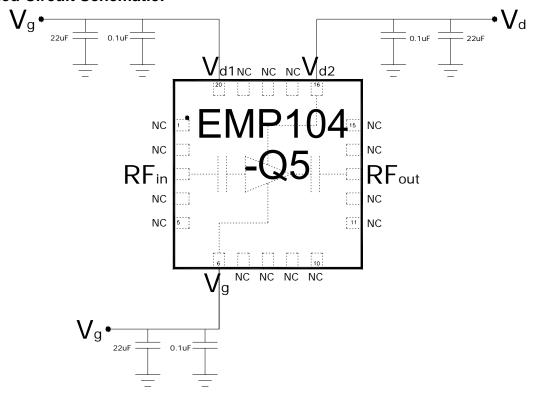




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Recommended Circuit Schematic:



Notes:

- External bypass capacitors should be placed as close to the package as possible.
- Dual biasing sequence required: 2)
 - a. Turn-on Sequence: Apply V_g = -2.5V, followed by V_d = 10V, lastly increase V_g until required I_{dq} b. Turn-off Sequence: Turn off V_d , followed by V_g
- 3) Demonstration board available upon request.

