

www.DataSheet4U.com

STEPPER MOTOR DRIVER

■ GENERAL DESCRIPTION

■ PACKAGE OUTLINE

The NJM2673 is a stepper motor driver, which consists of a LS-TTL compartible logic input stage, off time control circuits and a pair of high power H-bridges and protection diodes.

The output current is up to 1000mA.

The NJM2673 with small number of external components conforms a complete control and drive unit for stepper motor systems.



■ FEATURES

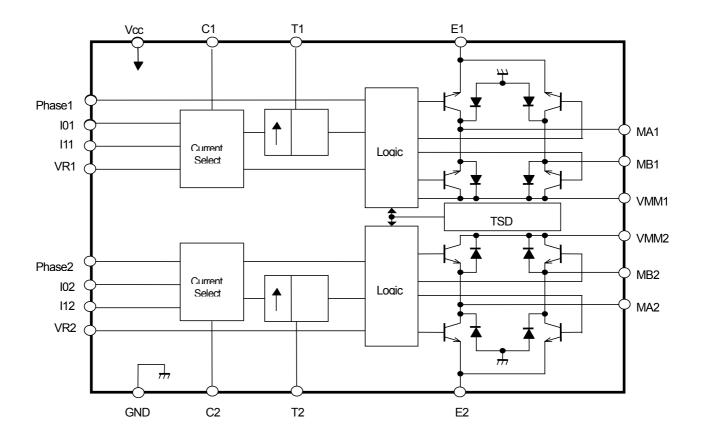
- Switched mode bipolar constant current drive
- Wide voltage range 4 to 45V
- Wide range of current control 5 to 1000mA
- Half- step and full-step operation
- Thermal overload protection
- Package EMP24

■ PIN CONNECTION

VCC	1	0	24	T2
T1	2		23	VR2
VR1	3		22	PHASE2
PHASE1	4		21	I ₁₂
I ₁₁	5		20	I ₀₂
GND	6	NJM	19	GND
GND	7	2673E3	18	GND
I ₀₁	8		17	MB2
MB1	9		16	C2
C1	10		15	E2
E2	11		14	MA2
VMM	12		13	MA1

www.DataSheet4U.com

■ BLOCK DIAGRAM



■ PIN DESCRIPTION

EMP	Symbol	Description
1	V_{CC}	Logic voltage supply normally +5V.
2	T ₁	Clock Oscillator, channel 1. Timing pin connect a 56kΩ resistance and a 820pF in
		parallel between T and ground.
3	V_{R1}	Reference voltage, channel 1. Controls the threshold voltage for the comparator and
		hence the output current.
4	Phase1	Controls the direction of the motor current of M _{A1} and M _{B1} outputs. Motor current flows
		from M_{A1} to M_{B1} when Phase1 is HIGH.
5	I ₁₁	Logic input, channel 1. It controls, together with the I ₀₁ input, the current level in the
		output stage.
6,7,18,19	GND	Ground and negative supply. These pins are used for heat sinking. Make sure that all
		ground pins are soldered onto a suitable large copper ground plane for efficient heat
		sinking.
8	I ₀₁	Logic input, channel 1. It controls, together with the I ₁₁ input, the current level in the
•		output stage.
9	M _{B1}	Motor output B, channel 2. Motor current flows from M _{A1} to M _{B1} when Phase1 is high.
10	C ₁	Comparator input, channel 1. This input senses the instantaneous voltage across the
11	_	sensing resistor, filtered through a RC network.
11	E ₁	Common emitter, channel 1. Connect the Sense resistor between this pin and
12	V_{MM}	ground.
13	M _{A1}	Motor supply voltage, 4 to 40V. Motor output A, channel 1. Motor current flows from M_{A1} to M_{B1} when Phase1 is high.
14	M _{A2}	Motor output A, channel 2. Motor current flows from M_{A2} to M_{B2} when Phase2 is high.
15	E ₂	Common emitter, channel 2. Connect the Sense resistor between this pin and
10	L 2	ground.
16	C ₂	Comparator input, channel 2. This input senses the instantaneous voltage across the
.0	02	sensing resistor, filtered through a RC network.
17	M _{B2}	Motor output B, channel 2. Motor current flows from M_{A2} to M_{B2} when Phase2 is high.
20	I ₀₂	Logic input, channel 2. It controls, together with the I_{12} input, the current level in the
	-	output stage.
21	I ₁₂	Logic input, channel 2. It controls, together with the I_{02} input, the current level in the
		output stage.
22	Phase2	Controls the direction of the motor current of M _{A2} and M _{B2} outputs. Motor current flows
		from M _{A2} to M _{B2} when Phase2 is HIGH.
23	V_{R2}	Reference voltage, channel 2. Controls the threshold voltage for the comparator and
		hence the output current.
24	T ₂	Clock Oscillator, channel 2. Timing pin connect a $56k\Omega$ resistance and a $820pF$ in
		parallel between T and ground.

www.DataSheet4H.com

■ ABSOLUTE MAXIMUM RETINGS

(Ta=25°C)

PARAMETER	SYMBOL	MIN.	MAX.	UNIT		
Voltage						
Logic Supply	V_{CC}	0	7	V		
Motor Supply	V_{MM}	0	45	V		
Logic Input Voltage	VI	-0.3	V_{CC}	V		
Comparator Input Voltage	V _C	-0.3	V_{CC}	V		
Reference Input Voltage	V _C	-0.3	V_{CC}	V		
Current						
Motor Output Current	I _M	-1000	+1000	mA		
Logic Input Current	I _I	-10	-	mA		
Analog Input Current	l _A	-10	-	mA		
Temperature						
Operating Temperature	T _{opr}	-40	85	°C		
Storage Temperature	T _{stg}	-55	150	°C		

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Logic Supply	V_{CC}	4.75	5.00	5.25	V
Motor Supply	V_{MM}	4	-	40	V
Motor Output Current	I _M	-800	-	800	mA
Operating Junction Temperature	Tj	-20	-	+125	°C
Rise time Logic Inputs	t _r	-	-	2	μS
Fall Time Logic Inputs	t _f	-	-	2	μS

ERECTRICAL CHARACTERISTICS (T_i =+25°C, V_{CC} =5V, V_{MM} =40V, C_T =820pF, R_T =56k Ω)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
General			•		•	
Supply Current	I _{CC}		-	-	60	mA
Turn Off	td	dVc/dt≥50mV/μs	-	0.9	-	μS
Thermal Shutdown Temperature	TSD		-	170	-	°C
Logic Input						
H Level Input Voltage	V _{IH}		2.0	-	-	V
L Level Input Voltage	$V_{\rm IL}$		-	-	0.8	V
H Level Input Current	I _{IH}	V _I =2.4V	-	-	20	μΑ
L Level Input Current	I _{IL}	V _I =0.4V	-250	-	-	μΑ
Input Resistance			<u>'</u>			
Input Resistance	R_R		-	8.8	-	kΩ
Analog Input			<u>'</u>			
Threshold Voltgae	V_{CH}	V _R =5.0V,I ₀ =I ₁ =L	405	450	495	mV
	V_{CM}	$V_R = 5.0 V, I_0 = H, I_1 = L$	284	315	347	mV
	V _{CL}	V _R =5.0V,I ₀ =L,I ₁ =H	134	150	163	mV
Input Current	I _C		-20	-	-	μΑ
Motor Output						
Lower Transistor Saturation Voltage	V_{OL}	I _M =500mA	-	1.1	1.4	V
Lewer Translator Catalation Voltage	V OL	I _M =800mA	-	1.3	1.7	V
Upper Transistor Saturation Voltage	V _{OU}	I _M =500mA	-	1.1	1.4	V
		I _M =800mA I _M =500mA	-	1.3 1.0	1.7 1.3	V
Lower Diode Forward Voltage Drop	V_{fL}	I _M =800mA	-	1.0	1.6	V
Linnar Diada Farriard Voltaga Dran	\/	I _M =500mA	-	1.1	1.4	V
Upper Diode Forward Voltage Drop	V _{fU}	I _M =800mA	-	1.3	1.7	V
Output Leak Current	I _{IL}	I ₀ =I ₁ =H	-	-	100	μΑ
Monostable						
Cut Off Time	t_{off}		-	31	-	μS

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
The word Decistors	Rth _{J-GND}		-	13	-	°C
Thermal Resistance	Rth _{J-A}	Note2	-	42	-	°C

Notes

- 1. All voltages are with respect to ground. Currents are positive into, negative out of specified terminal.
- 2. All ground pins soldered onto 20cm^2 PCB copper area with free air condition, T_A =25°C.

[CAUTION]

The specifications on this databook are only given for information, without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.