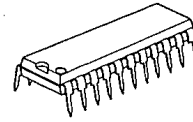


VIDEO EQUALIZER

■ GENERAL DESCRIPTION

NJM2258 is the IC functioning the gain high pass correction, as well as for equalizing function of wave distortion correction, generated by bright signal of group delay feature like low band filter. It has internalizing REC line, one circuit, and then the playback line 2 circuit.

■ PACKAGE OUTLINE



NJM2258L

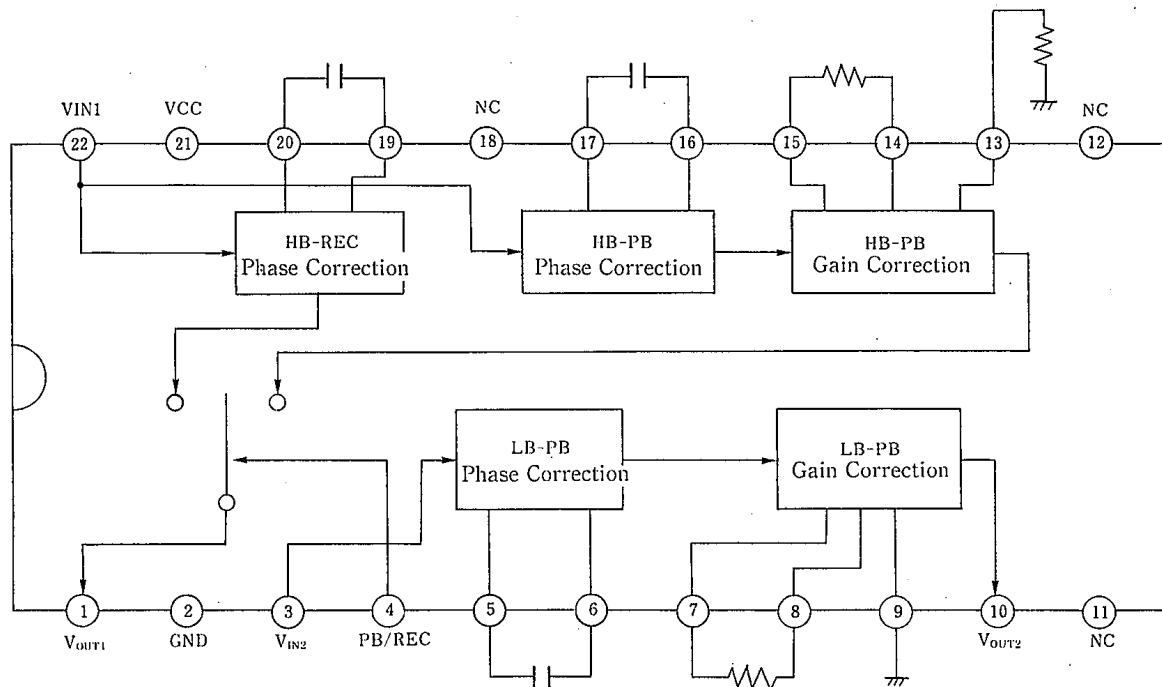
■ FEATURES

- 5V Spec, (Recommended Operating Voltage Range)
- Wide Band Width, 10MHz
- REC/PLAYBCK Change over function attached
- Package Outline SDIP22
- Bipolar Technology

■ Application

- VCR (S-VHS compatible)
- Video Camera
- Laser Disc

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	7	V
Power Dissipation	P _D	700	mW
Operating Temperature Range	T _{opr}	-20~+75	°C
Storage Temperature Range	T _{stg}	-40~+125	°C

■ ELECTRICAL CHARACTERISTICS

(V⁺=5V, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I _p	No Signal		26	34	mA

HB-REC

Phase Positive Gain	G _{af}	f _{IN} =100KHz 19PINOUT	-1	0	+1	dB
Phase Negative Gain	G _{ar}	f _{IN} =100KHz 20PINOUT	-6.4	-5.4	-4.4	dB
19pin Impedance	AT19	f _{IN} =100KHz	-7.0	-6.0	-5.0	dB
Output Gain LOW	G _{al}	f _{IN} =100k Hz	-1	0	+1	dB
Output Gain HIGH	G _{ah}	f _{IN} =5MEGHZ	-1	0	+1	dB
Output Gain f Feature	ΔG _a	G _{ah} -G _{al}	-1	0	+1	dB
Output Secondary Distortion	DA2	f _{IN} =5MEG v _{IN} =1.0V _{pp}		-40	-30	dB
Output the third Distortion	DA3	f _{IN} 5MEG v _{IN} =1.0V _{pp}		-36	-30	dB

HB-PB

Positiv Phase Gain	G _{bf}	f _{IN} =100KHz 16PINOUT	-1	0	+1	dB
Negative Phase Gain	G _{br}	f _{IN} =100KHz 17PINOUT	-0.2	0.8	1.8	dB
16 Impedance	AT16	f _{IN} =100KHz	-7.0	-6.0	-5.0	dB
Output Gain	G _{ca}	f _{IN} =100KHz	-6.1	-5.0	-4.0	dB
15-14 Impedance	ΔG _c	15PIN-14PIN=1.7KΩ	4.5	5.5	6.5	dB
Output Gain LOW	G _{cbl}	f _{IN} =100KHz	-1	0	+1	dB
Output Gain HIGH	G _{cbh}	f _{IN} =5MEGHZ	2	3	4	dB
Output Gain f Feature	ΔG _b	G _{cbh} -G _{cbl}	2	3	4	dB
Output Secondary Distortion	DC2	f _{IN} =5MEG v _{IN} =1.0V _{pp}		-30	-25	dB
Output the third Distortion	DC3	f _{IN} =5MEG v _{IN} =1.0V _{pp}		-27	-22	dB

LB-PB

Positive Phase Gain	D _{af}	f _{IN} =100KHz 6PINOUT	-1	0	+1	dB
Negative Phase Gain	G _{dr}	f _{IN} =100KHz 5PINOUT	-0.2	0.8	1.8	Db
6 Impedance	AT6	f _{IN} =100KHz	-7.0	-6.0	-5.0	dB
Output Gain	G _{da}	f _{IN} =100KHz	-6.1	-5.1	-4.1	dB
7-8 Impedance	ΔG _d	7PIN-8PIN=1.7KΩ	4.5	5.5	6.5	dB
Output Gain LOW	G _{dbl}	f _{IN} =100KHz		+1	0	dB
Output Gain HIGH	G _{dbh}	f _{IN} =5MEGHZ	-1	0	+1	dB
Output Gain f Feature	ΔG _e	G _{dbh} -G _{dbl}	-1	0	+1	dB
Output Secondary Distortion	DE2	f _{IN} =5MEG v _{IN} =1.0V _{pp}		-35	-28	dB
Output the third Distortion	DE3	f _{IN} =5MEG v _{IN} =1.0V _{pp}		-36	-30	dB

■ TERMINAL FUNCTION

PIN	PIN NAME	SYMBOL	FUNCTION
1	HB-REC/PB OUT	VOUT1	HB type (S-VHS) Correction Output Pin
2	GND	GND	Ground
3	LB-PB IN	VIN2	LB type, (VHS) play-back signal Input Pin
4	HB-REC/PB Change over	PB REC	HB type, Change-over output of REC signal or Play-back signal High makes play-back signal output, and low makes REC signal output.
5	LB-PB Phase Correction Pin 1	LPC1	Connecting Capacitor between Pin 5~6, which helps to give feature of correcting the group delay.
6	LB-PB Phase Correction Pin 2	LPC2	Connecting Capacitor between Pin 5~6, which helps to give feature of correcting the group delay.
7	LB-PB Gain Correction Pin 1	LGC1	Setting up Gain by connecting resistor between Pin 7~8.
8	LB-PB Gain Correction Pin 2	LGC2	Setting up Gain by connecting resistor between Pin 7~8.
9	LB-PB Gain Correction Pin 3	LP	Connecting L-C parallel resonance between pin 9~GND, helps to give High band keeping, and if not required of keeping connect to GND.
10	LB-PB OUT	VOUT2	LB type Output pin
11	N.C		N.C pin
12	N.C		N.C pin
13	LB-PB Gain Correction Pin 3	HP	Connecting L-C parallel resonance between pin 13~GND, helps to give High band keeping, and if not required of keeping connect to GND.
14	LB-PB Gain Correction Pin 2	HPG1	Setting up Gain by connecting resistor between Pin 14~15.
15	LB-PB Gain Correction Pin 1	HPG2	Setting up Gain by connecting resistor between Pin 14~15.
16	LB-PB Phase Correction Pin 2	HPC1	Connecting Capacitor between Pin 16~17, which helps to give feature of correcting the group delay.
17	LB-PB Phase Correction Pin 1	HPC2	Connecting Capacitor between Pin 16~17, which helps give feature of correcting the group delay.
18	N.C		N.C pin
19	LB-PB Phase Correction Pin 2	HRC1	Connecting Capacitor between Pin 19~20, which helps to give feature of correcting the group delay.
20	LB-PB Phase Correction Pin 1	HRC2	Connecting Capacitor between Pin 19~20, which helps to give feature of correcting the group delay.
21	V+	Vcc	Voltage Source.
22	HB-REC/PB IN		HB type Input pin.

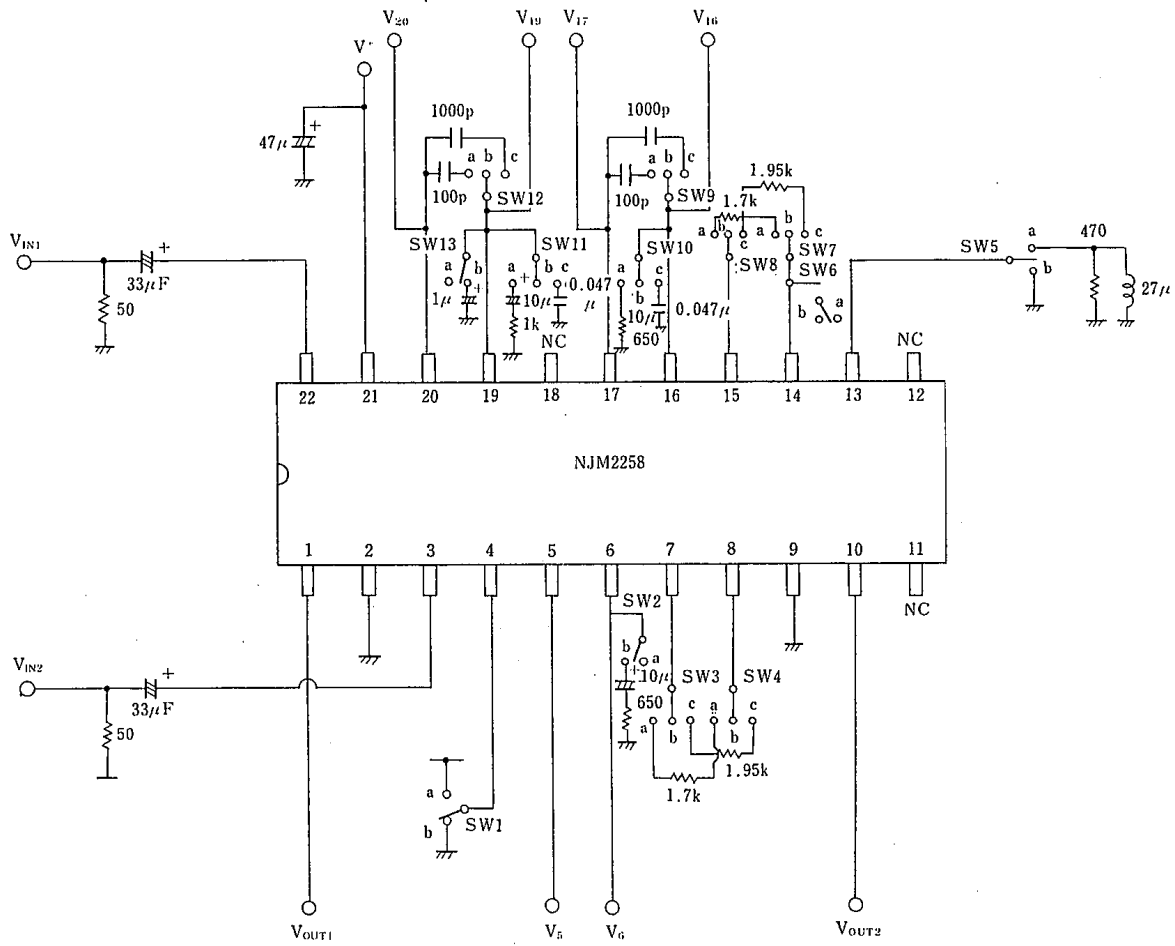
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■ TEST CONDITION

PARAMETER	SW-CONDITION													TEST PIN	TEST CONDITION		
	1	2	3	4	5	6	7	8	9	10	11	12	13				
I _p	a	a	b	b	b	a	b	b	b	b	b	b	a				
G _{Af}	b																V19 f=100kHz, V=0.5V _{IPP}
G _{Ar}															b		V20 f=100kHz, V=0.5V _{IPP}
AT19												a		a			V19 f=100kHz, V=0.5V _{IPP}
G _{al}												b	a				VOUT1 f=100kHz, V=0.5V _{IPP}
G _{ah}												b	a				VOUT1 f=5MHz, V=0.5V _{IPP}
DA2														c			VOUT1 f=5MHz, V=1.0V _{IPP}
DA3														c			VOUT1 f=5MHz, V=1.0V _{IPP}
G _{bf}																	V16 f=100kHz, V=0.5V _{IPP}
G _{br}																	V17 f=100kHz, V=0.5V _{IPP}
AT16											a						V16 f=100kHz, V=0.5V _{IPP}
G _{ea}	a								a	b							VOUT1 f=100kHz, V=0.5V _{IPP}
△G _{ea}							a	a									VOUT1 f=100kHz, V=0.5V _{IPP}
G _{eb1}					a		c	c									VOUT1 f=100kHz, V=0.5V _{IPP}
G _{ebh}					a		c	c									VOUT1 f=5MHz, V=0.5V _{IPP}
DC2					b		c	c	e								VOUT1 f=5MHz, V=1.0V _{IPP}
DC3					b		c	c	e								VOUT1 f=5MHz, V=1.0V _{IPP}
G _{df}	a											b	a				V6 f=100kHz, V=0.5V _{IPP}
G _{dr}																	V5 f=100kHz, V=0.5V _{IPP}
AT6		b															V6 f=100kHz, V=0.5V _{IPP}
G _{da}		a															VOUT2 f=100kHz, V=0.5V _{IPP}
△G _d			a	a													VOUT2 f=100kHz, V=0.5V _{IPP}
G _{eb1}			c	c													VOUT2 f=100kHz, V=0.5V _{IPP}
G _{ebh}			c	c													VOUT2 f=5MHz, V=0.5V _{IPP}
DE2			c	c													VOUT2 f=5MHz, V=1.0V _{IPP}
DE3			c	c													VOUT2 f=5MHz, V=1.0V _{IPP}

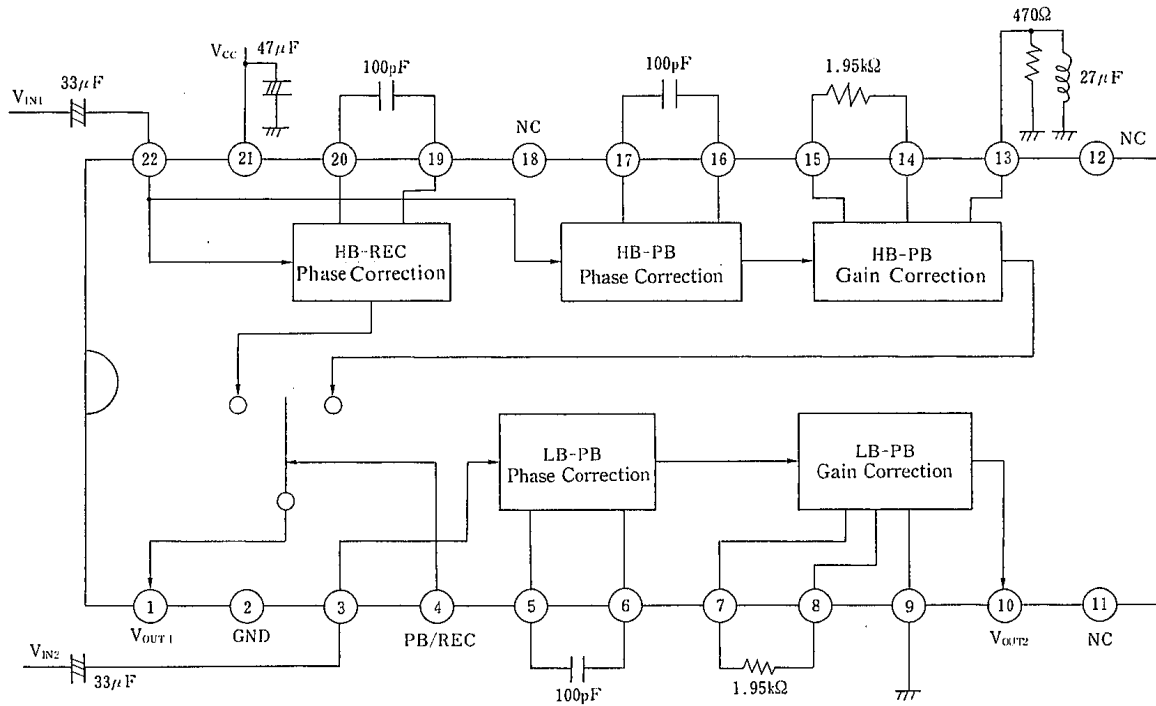
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■ TEST CIRCUIT



5

APPLICATION CIRCUIT



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MEMO

[CAUTION]

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