NJNZZ

3-INPUT VIDEO SWITCH WITH 6dB AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2245 is a three input integrated video switch witch selects one video or audio signal from three input signals.

It contains 6dB amplifier and its operating supply voltage range is 8.5 to 13V and bandwidth is 5MHz. Crosstalk is 65dB (at 4.43MHz).

■ FEATURES

- Operating Voltage 8.5~13V
- 3 Input-1 Output
- Internal 6dB Amplifier
- Muting Function available
- Cross-talk 65dB(at 4.43MHz)
- Wide Frequency Range 5MHz(1VP-P input)
- Package Outline DIP8, DMP8, SIP8
- Bipolar Technology

■ APPLICATION

VCR AV-TV Video Disc Player

■ PACKAGE OUTLINE





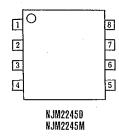
NJM2245D

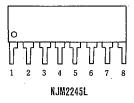
NJM2245M



NJM2245L

■ PIN CONFIGURATION

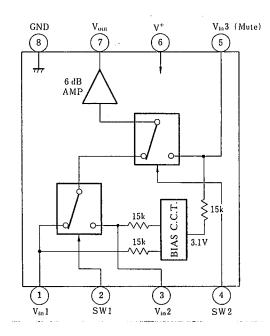




PIN FUNCTION 1 . V_{In}1 2 . SW1 3 . V_{In}2

- 4 . SW2 5 . V_{In}3 6 . V
- 7. V_{out} 8. GND

■ BLOCK DIAGRAM



■ INPUT CONTROL SIGNAL-OUTPUT SIGNAL

SW 1	SW 2	OUTPUT SIGNAL
L	L	V _{IN} 1
Н	L	V _{IN} 2
L/H	Н	V _{IN} 3

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V ⁺	15		
Power Dissipation	Po	(DIP8) 500	mW	
		(DMP8) 300	mW	
		(SIP8) 800	mW	
Operating Temperature Range	Торг	-40~+85	°C	
Storage Temperature Range	Tstg	-40~+125	°C	

■ ELECTRICAL CHARACTERISTICS

 $(V^+=9V, Ta=25^{\circ}C)$

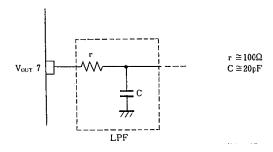
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Recommended Supply Voltage	V*			<u></u>	13.0	V
Operating Current	I _{CC}	S1=S2=S3=S4=S5=2	10.0	16.5	23.0	mA
Voltage Gain	Gv	Vin=1.0V _{P.P} , 100kHz, Vo/Vi, R _L =1kΩ		6.2	6.7	dB
Frequency Characteristic	G _f	$Vin = 1.0_{P-P}, V_0(10MHz)/V_0(100kHz) R_L = 1k\Omega$	-1.0		+1.0	dB
Differential Gain	DG	Vin=1.0V _{P.P} , staircase, $R_L = I k \Omega$		0.3		, %
Differential Phase	DP	Vin=1.0V _{P-P} , staircase, $R_L = 1 k\Omega$	_	0.3	_	deg.
Output Offset Voltage	V _{off}	$S1=S2=S3=2$, $S5=1\rightarrow 2$ V_{O} :voltage change			±60	mV
Crosstalk	СТ	Vin=1V _{P.P} , 4.43MHz, V _O /Vi	_	-65	_	dB
Switch Change Voltage	V _{CH}	All inside SW:ON	2.4	_	_	V
Switch Change Voltage	V _{CL}	All inside SW:OFF	_		0.8	V
Input Impedance	R ₁		_	15	_	kΩ

(note) Unless specified, tested with three mode below.

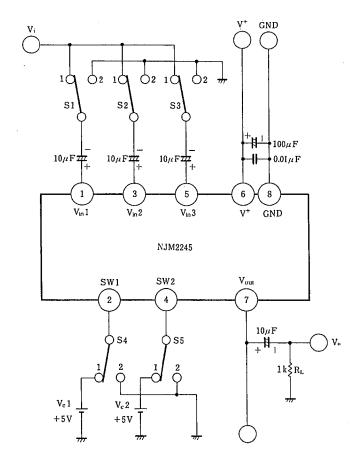
a) S1=1, S2=S3=S4=S5=2 b) S2=S4=1, S1=S3=S5=2 c) S1=S2=2, S3=S5=1, S4=1 or 2

■ APPLICATION

Oscillation Prevention on light loading conditions Recommended under circuit.



■ TEST CIRCUIT



DC Voltage Each Terminal

Typ. on Test Circuit Ta =25℃

Terminal Name	V _{IN} I	SWI	V _{IN} 2	SW2	V _{IN} 3	V+	Vout	GND
DC Voltage	½ V+	<u> </u>	$\frac{2}{5}V^{+}$	_	$\frac{2}{5}V^{+}$		$\frac{2}{5}$ V+-2.1	

■ EQUIVALENT CIRCUIT

PIN NO. PIN	FUNCTION	INSIDE EQUIVALENT CIRCUIT	PIN NO. PIN	FUNCTION	INSIDE EQUIVALENT CIRCUIT
1	V _{IN} 1	V _{1N} 1 ≥ 200Ω V _{1N} 1 ≥ 200Ω 200Ω	5	V _{IN} 3 (Mute)	V+ V _{IN} 3 ₹ 200Ω
. 2	SW 1	2kΩ 2kΩ 2l3kΩ 1.1mA 39kΩ	6	V+	·
3	Vin 2	V ₁ ,χ ₂ ≥ 200 Ω 15k 200 Ω	7	V _{OUT}	200Ω · Vout
4	SW 2	SW2 2kΩ 3l3kΩ 1.1 mA 9kΩ	8	GND	<u> </u>

5

NJM2	245
------	-----

MEMO

[CAUTION]
The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.