

# 256K

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Only

# MICRON

TECHNOLOGY, INC.

2805 East Columbia Road  
Boise, Idaho 83706  
TEL. (208) 386-3900  
TWX 910-970-5973

Quality • Performance • Service

## (Micron 1 MEG PIN COMPATIBLE DRAM Family)

**MT1259-P** 262,144 x 1 BIT DYNAMIC RAM  
**MT4067-P** 65,536 x 4 BIT DYNAMIC RAM

### FEATURES

- 256K x 1 and 64K x 4 DRAM's in Industry Standard 1 MEG pin out.
- Available in DIP, ZIP and SOJ packages.
- Compatible with 1 MEG DRAM refresh modes.
- Allows up-grade to 1 MEG DRAM's with no PC Board changes.

### OPTIONS

- Timing
  - 100ns access
  - 120ns access
  - 150ns access

### MARKING

- Timing
  - x1, x4
  - 10
  - 12
  - 15

- Packages
  - Plastic DIP
  - Ceramic DIP
  - Plastic ZIP
  - Plastic SOJ

- Packages
  - P
  - CP
  - ZP
  - DJP

### Order Example:

256K x 1, 120ns access in plastic ZIP = MT1259ZP-12

The MT1259P/MT4067P are randomly accessed solid-state memories containing 262,144 bits organized in a x1 or x4 configuration. These devices are assembled in 1 MEG DRAM packages and conform to 1 MEG pin outs with the exception of address A9 which is a no connect (A8 on 256K x 4). This allows PC Board layout that is compatible with both 256K and 1 MEG DRAM's.

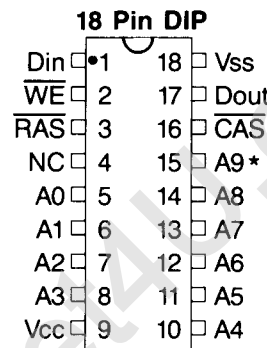
Care must be taken when specifying  $\overline{\text{RAS}}$  only refresh. 256K DRAM's use 256 cycle refresh distributed across 4ms while 1 MEG DRAM's use 512 cycle refresh distributed across 8 ms. Because of chip layout constraints some address scrambling was necessary to achieve the 1MEG x 1 pin out compatibility. It is necessary therefore, for the 512 cycle/8ms  $\overline{\text{RAS}}$  only refresh address (A0-A8) specification be satisfied for proper operation of the 256K X 1 part. This also insures proper operation of 1MEG DRAMs used in the same PC Board. No special considerations are required if  $\overline{\text{CAS}}$  before  $\overline{\text{RAS}}$  or Hidden Refresh cycles are employed.

The MT1259P/MT4067P is manufactured and quality controlled in Micron's modern Boise, Idaho USA facility. Refer to the MT1259P/MT4067P data sheet for all electrical specifications. Each unit receives accelerated burn-in and several hours of AMBYX™ system level-testing prior to final test and shipment.

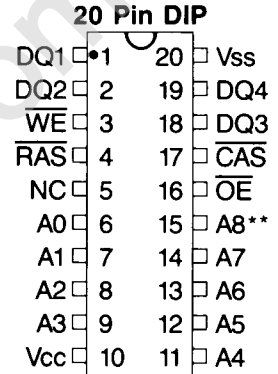
Please contact the factory for technical, test and application assistance. Micron can also furnish the representative and distributor nearest you. Micron's QUALITY ASSURED policy is to offer prompt, accurate and courteous service while assuring quality and reliability. The success of our memory business depends on the success of your application.

### PIN ASSIGNMENT (TOP VIEW)

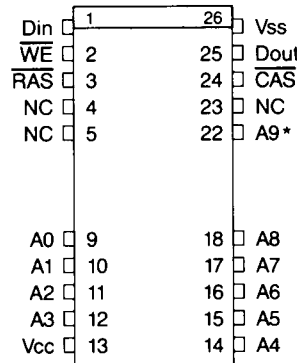
MT 1259P



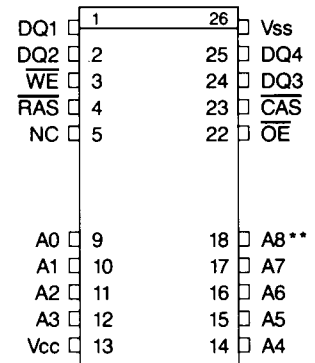
MT 4067P



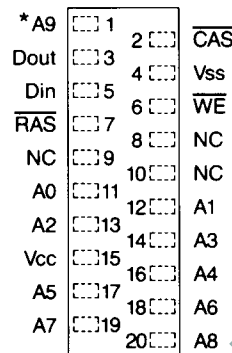
20 Pin SOJ



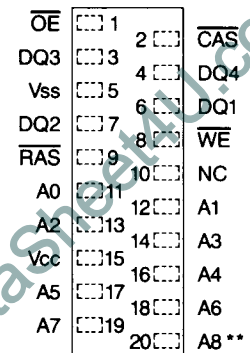
20 Pin SOJ



20 Pin ZIP



20 Pin ZIP



\*No connect-used as A9 on 1 MEG x 1.

\*\*No connect-used as A8 on 256K x 4.