

M54577P

7-UNIT 30mA TRANSISTOR ARRAY

DESCRIPTION

M54577P is seven-circuit transistor arrays. The circuits are made of NPN transistors. The semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

- Medium breakdown voltage ($BV_{CEO} \geq 30V$)
- Output sink current ($I_{c(max)} = 30mA$)
- Driving available with MOS (PMOS, CMOS) IC output
- Low output saturation voltage ($V_{CE(sat)} = 0.35V$ at $I_c = 20mA$)
- Wide operating temperature range ($T_a = -20$ to $+75^\circ C$)

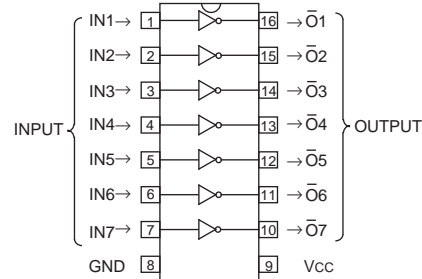
APPLICATION

Driving of digit drives of indication elements (LEDs and lamps)

FUNCTION

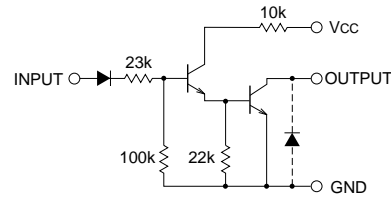
The M54577P has seven circuits consisting of NPN transistor. This I_c uses a predriver stage with a diode and $23k\Omega$ resistor in series to input. The output transistor emitters are all connected to the GND pin (pin 8), and V_{cc} is connected to pin 9. The collector current are capable of sinking 30mA maximum. Collector-emitter supply voltage is 30V maximum. Collector-emitter saturation voltage is below 0.35V ($I_c = 20mA$) Drives active "H" input.

PIN CONFIGURATION



Package type 16P4(P)

CIRCUIT DIAGRAM



The seven circuits share the V_{cc} and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit : Ω

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, $T_a = -20 \sim +75^\circ C$)

Symbol	Parameter	Conditions	Ratings	Unit
V_{cc}	Supply voltage		13	V
V_{CEO}	Collector-emitter voltage	Output, H	-0.5 ~ +30	V
I_c	Collector current	Current per circuit output, L	30	mA
V_i	Input voltage		-20 ~ V_{cc}	V
P_d	Power dissipation	$T_a = 25^\circ C$, when mounted on board	1.47	W
T_{opr}	Operating temperature		-20 ~ +75	$^\circ C$
T_{stg}	Storage temperature		-55 ~ +125	$^\circ C$

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RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted, $T_a = -20 \sim +75^\circ\text{C}$)

Symbol	Parameter	Limits			Unit
		min	typ	max	
VCC	Supply voltage	4.5	5	13	V
Ic	Collector current (Current per 1 circuit)	0	10	20	mA
V _{IH}	"H" input voltage	3	—	V _{CC}	V
V _{IL}	"L" input voltage	0	—	1	V

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

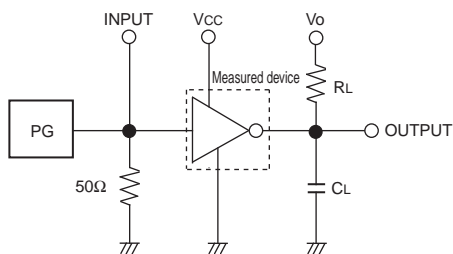
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
V (BR) CEO	Collector-emitter breakdown voltage	ICEO = 100μA	30	—	—	V
VCE(sat)	Collector-emitter saturation voltage	VCC = 4.5V, VI = 3V, IC = 10mA VCC = 6V, VI = 3V, IC = 20mA	—	—	0.25 0.35	V
I _I	Input current	VCC = 4.5V, VI = 3V	30	—	90	μA
I _{CC}	Supply current (Only one time operation)	VCC = 4.5V, VI = 3V VCC = 13V, VI = 3V	—	0.4 1.3	0.9 2.3	mA
hFE	DC amplification factor	VCE = 4V, VCC = 4.5V, IC = 20mA, Ta = 25°C	500	1200	—	—

* : The typical values are those measured under ambient temperature (T_a) of 25°C . There is no guarantee that these values are obtained under any conditions.

SWITCHING CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

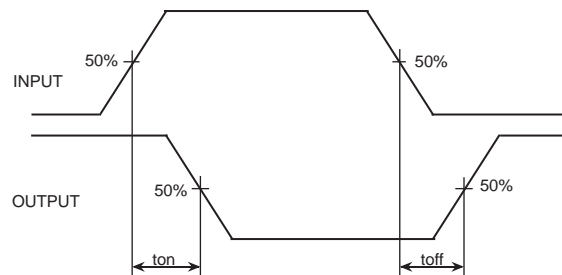
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
ton	Turn-on time	CL = 15pF (note 1)	—	210	—	ns
toff	Turn-off time		—	3200	—	ns

NOTE 1 TEST CIRCUIT



- (1) Pulse generator (PG) characteristics : PRR=1kHz,
tw = 10μs, tr = 6ns, tf = 6ns, Zo = 50Ω, VP = 3VP-P
- (2) Input-output conditions : RL = 500Ω, Vo = 10V, VCC = 6V
- (3) Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes

TIMING DIAGRAM



TYPICAL CHARACTERISTICS

