

FSA641 – 2:1 MIPI Switch, Featuring 2-Data and 1-Data Lane Configuration

Features

Switch Type	2:1
Signal Types	MIPI, DPHY
V _{CC}	2.65 to 4.3V
Input Signals	0 to V _{CC}
R _{ON}	7Ω Typical HS MIPI 10Ω Typical LS MIPI
ΔR _{ON}	0.75Ω Typical HS & LS MIPI
I _{CC}	1μA Maximum
O _{IRR}	-50dB Typical
X _{TALK}	-40dB Typical
Bandwidth	720MHz Typical
Channel-to-Channel Skew	15ps Typical
C _{ON}	8pF Typical
Package	20-Lead UMLP 3.0x3.0x 0.55mm, 0.4mm Pitch
Ordering Information	FSA641UMX

Applications

- Cellular Phones, Smartphones
- Displays

Typical Application

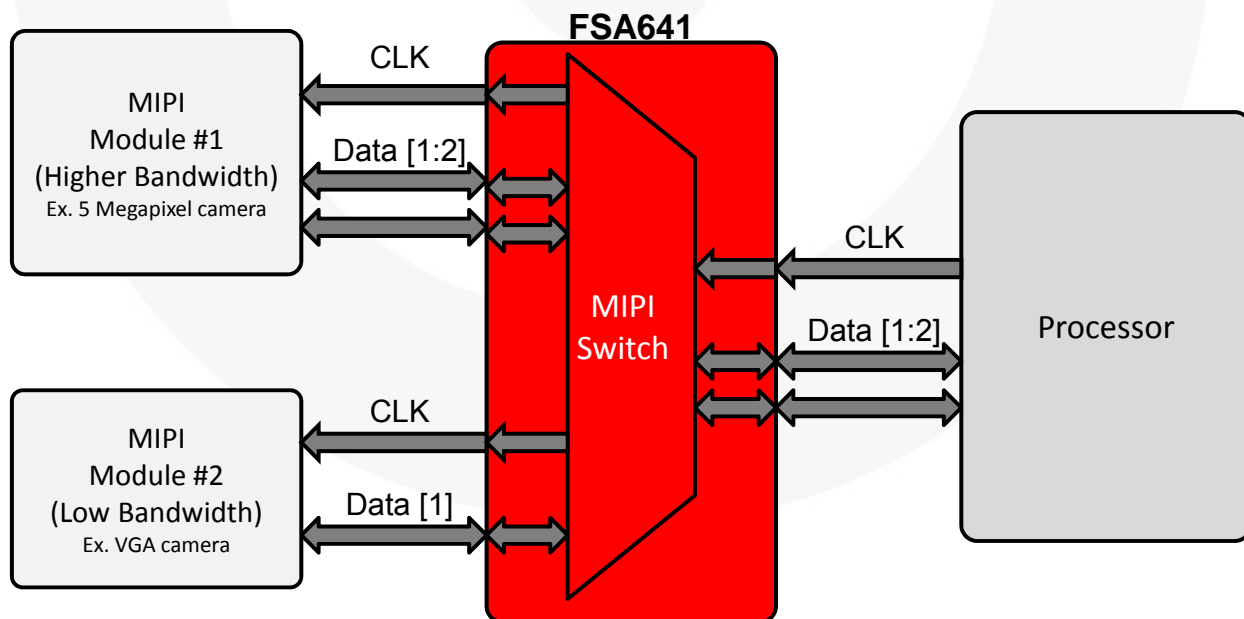


Figure 1. Mobile Phone Example

Description

The FSA641 is a 2:1 MIPI switch made for 2-data lane and 1-data lane modules. This part is configured as a single-pole, double-throw switch (SPDT) and is optimized for switching between two high-speed or low-power MIPI sources. The FSA641 has specially been designed for the MIPI specification and allows connection to either a CSI or DSI module. The FSA641 features an extremely low on capacitance (C_{ON}) of 8pF. The wide bandwidth (>720MHz) exceeds the bandwidth needed to pass the third harmonic, resulting in signals with minimum edge and phase distortion. Superior channel-to-channel crosstalk minimizes interference.

Related Resources

- For samples and questions, please contact: Analog.Switch@fairchildsemi.com.
- FSA641 Demonstration Board

Physical Dimensions

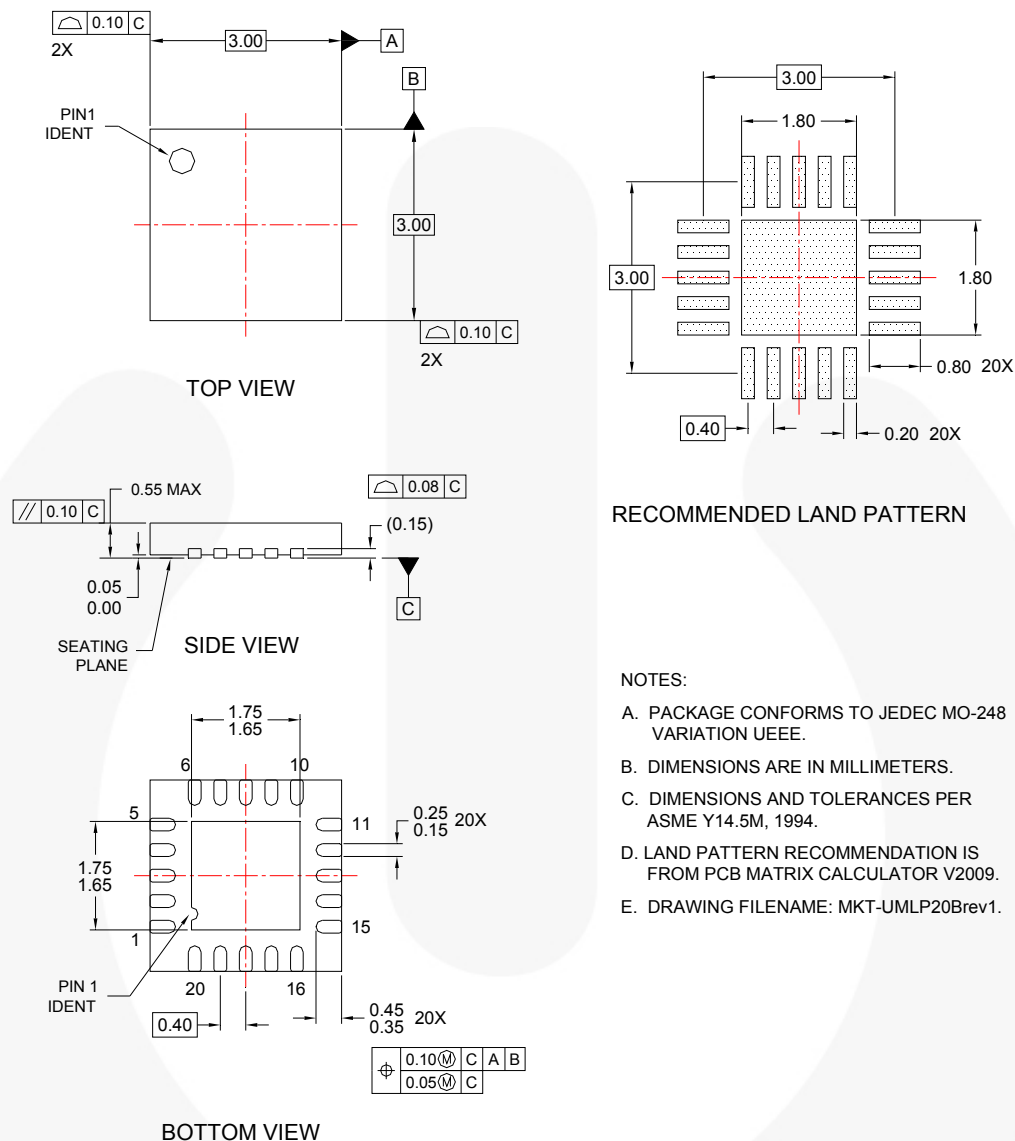


Figure 16. 20-Lead, Quad, Ultrathin Molded Leadless Package (UMLP), 3.0 x 3.0mm

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Ordering Information

Part Number	Top Mark	Operating Temperature Range	Package
FSA641UMX	F641	-40 to +85°C	20-Lead, Quad, Ultrathin Molded Leadless Package (UMLP), 3.0 x 3.0mm



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