FUDAN MICROELECTRONICS



# FM2141 (GFCI) Low Power Ground Fault Interrupter

**Specification** 

Oct. 2007

上海复旦微电子股份有限公司

FM2141 Low Power Ground Fault Interrupter

Ver 1.0

Specification

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Ver 1.0

Specification 2

## **Product Overview**

### Introduction

FM2141 is a low power controller for AC receptacle ground fault circuit interrupters. The devices detect hazardous current paths to ground and ground to neutral faults. The circuit interrupter then disconnects the load from the line before a harmful or lethal shock occurs.

FM2141 contains a diode rectifier, shunt regulator, precision sense amplifier, current reference, time delay circuit and SCR driver internally. Two sense transformers, SCR, solenoid, three resistors and four capacitors complete the design of the basic circuit interrupter.

The simple layout and minimum components insure ease of application and long term reliability. Features not found in other GFCI controllers include a low offset voltage sense amplifier eliminating the need for a coupling capacitor between the sense transformer and sense amplifier, and an internal rectifier to eliminate high voltage rectifying diodes.

FM2141 is powered only during the positive half period of the line voltage, but can sense current faults independent of its phase relative to the line voltage. The gate of the SCR is driven only during the positive half cycle of the line voltage.

### Features

- Powered from the AC line
- Built-in rectifier
- Direct interface to SCR
- 500 µA quiescent current
- Precision sense amplifier
- Adjustable time delay
- Minimum external components
- Meets UL 943 requirements
- For use with 110V or 220V systems
- Available in 8 pin SOP or DIP package

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### **Pin Assignment**



## **Pin Description**

PIN	SYM	PIN	SYM	PIN	SYM	PIN	SYM
1	Amp Out	3	V <sub>REF</sub>	5	LINE	7	SCR Trigger
2	V <sub>FB</sub>	4	GND	6	+V <sub>S</sub>	9	Delay Cap

## **Block Diagram**



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# **Electrical Characteristics**

### **Absolute Maximum Ratings**

Parameters	Min	Тур	Max	Units	
Supply Current	-	-	-	10	mA
Internal Power Dissipation	-	-	-	500	mW
Storage Temperature Range	-	-65	-	+150	°C
Operating Temperature Range	-	-35	-	+80	°C
Junction Temperature	-	-	-	125	
Looding Coldering Temperature	60 Sec DIP	-	-	300	°C
Leading Soldering Temperature	10 Sec SOP	-	-	260	°C

## **DC Characteristics**

 $(I_{LINE} = 1.5 \text{mA}, T_A = +25 \degree \text{C}, R_{SET} = 650 \text{K}\Omega)$ 

Parameters	Sym	Test Conditions	Min	Тур	Max	Units		
Shunt Regulator (Pins 5 to 4)								
Regulated Voltage	V <sub>reg</sub> 1	Ι <sub>2-3</sub> = 11μΑ	25.0	27.0	29.0	V		
Regulated Voltage	V <sub>reg</sub> 2	I <sub>LINE</sub> = 740μΑ,I <sub>2-3</sub> = 9μΑ	25.0	27.0	29.0	V		
Quiescent	I <sub>LINE</sub>	V <sub>5-4</sub> = 24V	-	500	-	μA		
Sensibility (Pin 2 to 7)								
Offset voltage	V <sub>offset</sub>		-200	0	200	μV		
Gain bandwidth	GB	(设计值)	_	1.5	_	MHz		
Input bias current	I <sub>Bias</sub>	(设计值)		30	100	nA		
Sensibility	I <sub>SEN</sub>	Pin 7↑	3.49	4.5	5.9	μA		
SCR Trigger								
Output R <sub>0</sub> V <sub>7-4</sub>		$V_{7-4} = Open, V_{5-4} = Open$	3.8	4.7	5.6	kΩ		
Output	VOL	I <sub>2-3</sub> = 9μA	0	0.1	10	mV		
Output	VOH	I <sub>2-3</sub> = 11μA	2.4	3.0	4.0	V		
Reference Voltage (Pins 3 to 4)								
Reference Voltage	eference $V_{\text{Ref}}$ $I_{\text{LINE}} = 740 \mu \text{A}$		12.0	13.0	14.0	V		

## **AC Characteristics**

 $(I_{LINE} = 1.5 \text{mA}, T_A = +25 \degree \text{C}, R_{SET} = 650 \text{K}\Omega)$ 

Parameters	Sym	Test Conditions	Min	Тур	Max	Units
Delay Timer						
Delay Time TD C <sub>8-4</sub> = 1.2 nF		C <sub>8-4</sub> = 1.2 nF	-	0.2	-	ms

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## **GFCI** Application

The GFCI detects a ground fault by sensing a difference current in the line and neutral wires. The difference current is assumed to be a fault current creating a potentially hazardous path from line to ground. Since the line and neutral wires pass through the center of the sense transformer, only the differential primary current is transferred to the secondary. Assuming the turns ratio is 1:1000, the secondary current is 1/1000<sup>th</sup> the fault current. FM2141's sense amplifier converts the secondary current to a voltage which is compared with either of the two window detector reference voltages. If the fault current exceeds the design value for the duration of the programmed time delay, FM2141 will send a current pulse to the gate of the SCR.

Detecting ground to neutral faults is more difficult. RB represents a normal ground fault resistance, RN is the wire resistance of the electrical circuit between load/neutral and earth ground. RG represents the ground to neutral fault condition. According to UL 943, the GFCI must trip when RN= $0.4\Omega$ , RG= $1.6\Omega$ and the normal ground fault is 6 mA.

Assuming the ground fault to be 5mA, 1mA and 4mA will go through RG and RN, respectively, causing an effective 1mA fault current. This current is detected by the sense amplifier and amplifier by the sense amplifier. Now the ground/neutral and sense transformers are mutually coupled by RG, RN and neutral wire ground loop, producing a positive feedback loop through the sense amplifier. The newly created feedback loop causes the sense amplifier to oscillate at a frequency determined by ground/neutral transformer secondary inductance and C4. Typically it occurs at 8 KHz.

C2 is used to program the time required for the fault to be present before the SCR is triggered. Refer to the equation below for calculating the value of C2.

 $C2 = 6 \times T$ Unit of C2 is nF, Unit of T is ms.

RSET is used to set the fault current at which the GFCI trips.

$$R_{SET} = \frac{4.6 \times N}{I_{EAULT} \times COS180 (T/P)}$$

When used with a 1:1000 sense transformer, its typical value is  $1M\Omega$  for a GFCI designed to trip at 5mA.

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# **Application Circuit**



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# **Revision History**

Version	Publication date	Pages	Paragraph or Illustration	Revise Description
1.0	Oct. 2007	9		Initial Release.



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