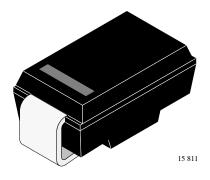


# **Schottky Barrier Rectifier**

### Features

- High efficiency
- Low power losses
- Very low switching losses
- Low reverse current
- High surge capability



### Applications

Polarity protection Low voltage, high frequency rectifiers

### **Absolute Maximum Ratings**

T	=	25°C
• •		20 0

1 - 20 0						
Parameter	Test Conditions	Туре	Symbol	Value	Unit	
Deverage vielte an		BYS10-25	V	25	V	
Reverse voltage= Repetitive peak reverse voltage		BYS10-35	V <sub>R</sub> = V <sub>RRM</sub>	35		
Repetitive peak reverse voltage		BYS10-45	VRRM	45		
Peak forward surge current	t <sub>p</sub> =10ms, half sinewave		I <sub>FSM</sub>	30	А	
Average forward current	•		I <sub>FAV</sub>	1.5	А	
Junction and storage temperature range			T <sub>j</sub> =T <sub>stg</sub>	-55+150	°C	

# **Maximum Thermal Resistance**

 $T_i = 25^{\circ}C$ 

Parameter	Test Conditions		Value	Unit
Junction lead	T <sub>L</sub> =constant	R <sub>thJL</sub> 25		
Junction ambient	mounted on epoxy–glass hard tissue		150	K/W
	mounted on epoxy–glass hard tissue, 50mm <sup>2</sup> 35µm Cu	R <sub>thJA</sub>	125	
	mounted on Al-oxid-ceramic (Al <sub>2</sub> O <sub>3</sub> ), 50mm <sup>2</sup> 35µm Cu		100	

# **Electrical Characteristics**

#### T<sub>i</sub> = 25°C

Parameter	Test Conditions	Туре	Symbol	Min	Тур	Max	Unit
Forward voltage	I <sub>F</sub> =1A		V <sub>F</sub>			500	mV
Reverse current	V <sub>R</sub> =V <sub>RRM</sub>		1			500	μΑ
	V <sub>R</sub> =V <sub>RRM</sub> , T <sub>i</sub> =100°C		I R			10	mA



### **Vishay Semiconductors**

# **Characteristics** ( $T_j = 25^{\circ}C$ unless otherwise specified)

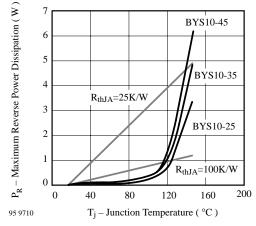


Figure 1. Max. Reverse Power Dissipation vs. Junction Temperature

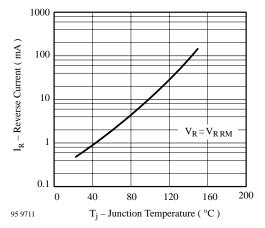
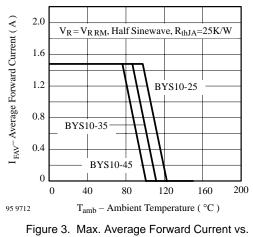


Figure 2. Max. Reverse Current vs. Junction Temperature



Ambient Temperature



 $I_{\rm FAV}-$  Average Forward Current ( A ) 2.0  $V_R = 0 V$ , Half Sinewave 1.6 R<sub>thJA</sub>=25K/W 1.2 100K/W 0.8 125K/W 0.4 150K/W 0 200 40 80 120 160 0 95 9713 Tamb - Ambient Temperature ( °C )

Figure 4. Max. Average Forward Current vs. Ambient Temperature

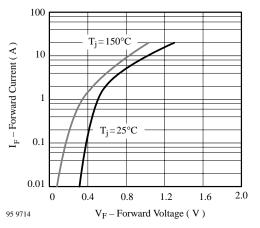


Figure 5. Max. Forward Current vs. Forward Voltage

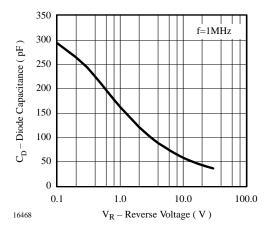
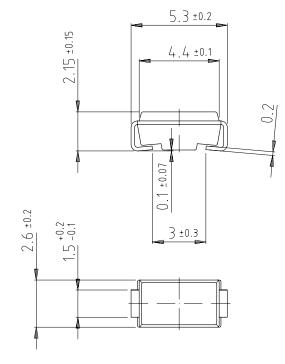


Figure 6. Diode Capacitance vs. Reverse Voltage



### **Dimensions in mm**





Plastic case JEDEC DO 214 similar to SMA Cathode indicated by a band



technical drawings according to DIN 14275 specifications

### **Vishay Semiconductors**



### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.

2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay-Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay-Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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