

HA17558B Series

Dual Operational Amplifier

REA03D0003-0100 Rev.1.00 Dec 25, 2006

Description

HA17558B is dual bipolar op-amp with improved characteristics compared to HA17558A. It has wide bandwidth, low noise, high slew rate; wide operating voltage range and high gain characteristics.

This product has a wide range of applications that is appropriate for audio application, as well as AC/DC converter. taSheet4U.com

Features

- Wide bandwidth: 7 MHz
- High speed: 3 V/µs
- Low input noise voltage: $1 \ \mu Vrms$
- Large DC voltage gain: 110 dB
- Operating voltage: ± 2 V to ± 18 V
- Package outline available in Pb free lead frame: DP-8
 SOP-8 (JEITA)
 SOP-8 (JEDEC)

Applications

- Audio AC-3 decoder system
- Audio amplifier
- AC/DC converter

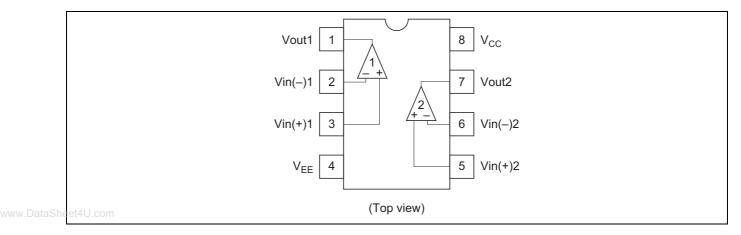
Ordering Information

Type No.	Application	Package Code (Package Name)
HA17558B	Commercial use	PRDP0008AF-B (DP-8FV)
HA17558BF		PRSP0008DE-B (FP-8DGV)
HA17558BRP		PRSP0008DD-C (FP-8DCV)

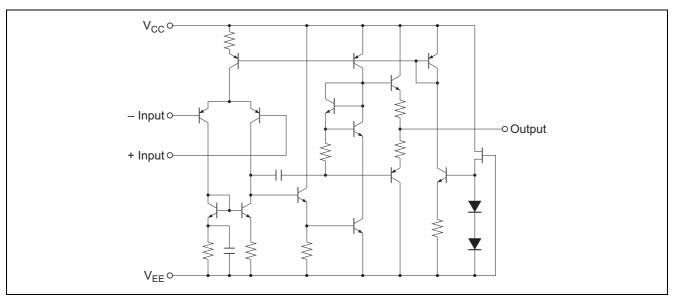
Note: This product is designed for consumer use and not for automotive.



Pin Arrangement



Circuit Schematic (1/2)





Absolute Maximum Ratings

$(Ta = 25^{\circ}C)$

		Ratings			
Item	Symbol	HA17558B	HA17558BF	HA17558BRP	Unit
Supply Voltage	Vcc	18	18	18	V
	VEE	-18	-18	-18	V
Differential input voltage	V _{IN} (diff)	±30	±30	±30	V
Common mode input voltage	V _{CM} * ³	±15	±15	±15	V
Power dissipation	PT	670 * ¹	385 * ²	385 * ²	mW
Operating temperature	Topr	-40 to +85	-40 to +85	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	-55 to +125	-55 to +125	°C

Notes: 1. This is the allowable value up to $Ta = 45^{\circ}C$. Derate by 8.3 mW/°C above that temperature.

www.DataSheet4U.com 2. These are the allowable values up to Ta = 60°C mounting on 40mm × 40mm × 1.6mm (t) 10% wiring density glass epoxy board. Derate by 5.9 mW/°C above that temperature.

3. If the supply voltage is less than \pm 15 V, input voltage should be less than supply voltage.

Electrical Characteristics

		(Ta =	25°C, V _{CC}	=+15 V, Y	$V_{\rm EE} = -2$	15 V, unless otherwise specified)
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	VIO	_	0.5	3	mV	$R_S \le 10 \ k\Omega$
Input offset current	I _{IO}	_	5	50	nA	
Input bias current	I _{IB}	_	65	250	nA	
Supply current	I _{CC}	_	2.5	4	mA	
Power supply rejection ratio	PSRR	80	100	_	dB	$R_{S} \le 10 \ k\Omega$
Voltage gain	Av	85	110	—	dB	$R_L \ge 2 \ k\Omega, \ V_O = \pm 10 \ V$
Common mode rejection ratio	CMR	80	100	_	dB	$R_{S} \le 10 \ k\Omega$
Output swing voltage	V _{OS}	±10	±13	_	V	$R_L \ge 2 \ k\Omega$
		±12	±14	_	V	$R_L \ge 10 \ k\Omega$
Output sink current	I _{OSINK}	_	70	_	mA	$V_{IN(-)} = 1 V, V_{IN(+)} = 0 V,$
						$V_{O} = 2 V$
Output source current	IOSOURCE	—	45	—	mA	$V_{IN(-)} = 0 V, V_{IN(+)} = 1 V,$
						$V_0 = 2 V$
Slew rate	SR	_	3	—	V/µs	
Equivalent input noise voltage	V _{NI}	_	1		μVrms	RIAA, $R_S = 1 \text{ k}\Omega$, 30 kHz LPF
Gain bandwidth product	fu	_	7	_	MHz	f = 10 kHz
Total harmonic distortion	THD	_	0.0045	_	%	$f = 1 \text{ kHz}, V_0 = 1 \text{ Vrms}$

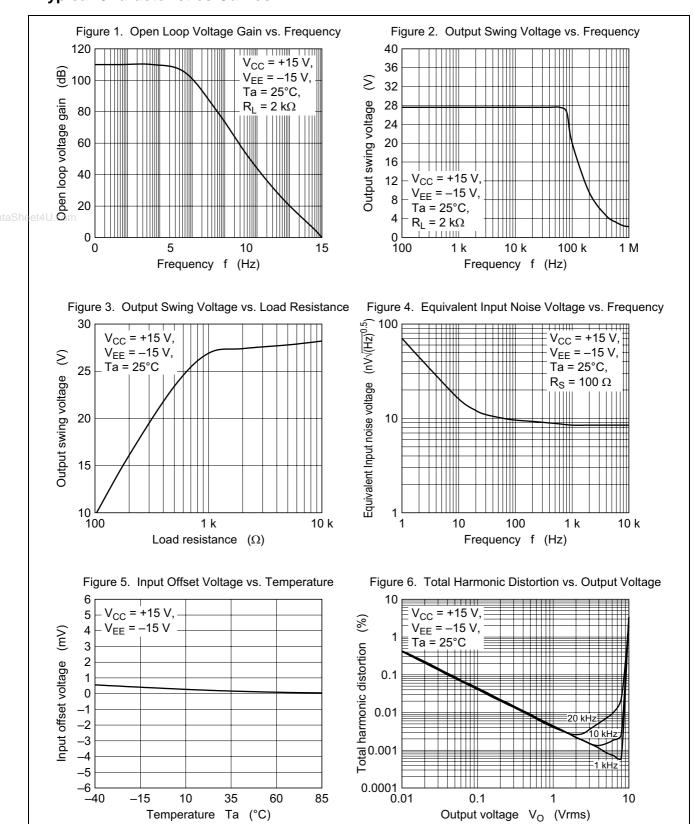


Table of Graphs

Elec	Figure	
Open loop voltage gain	vs. Frequency f	1
Output swing voltage	vs. Frequency f	2
Output swing voltage	vs. Load resistance R _L	3
Equivalent input noise voltage	vs. Frequency f	4
Input offset voltage	vs. Temperature Ta	5
Total harmonic distortion	vs. Output Voltage Vo	6

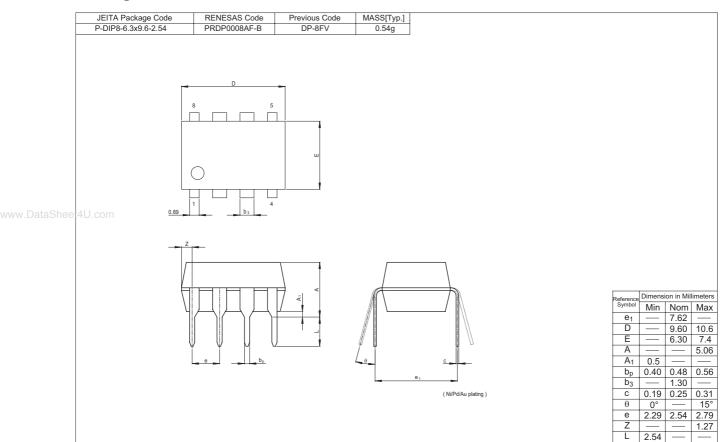
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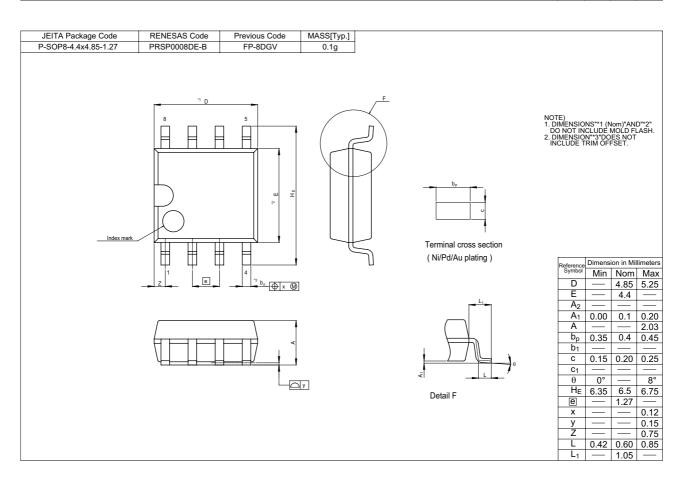




Typical Characteristics Curves

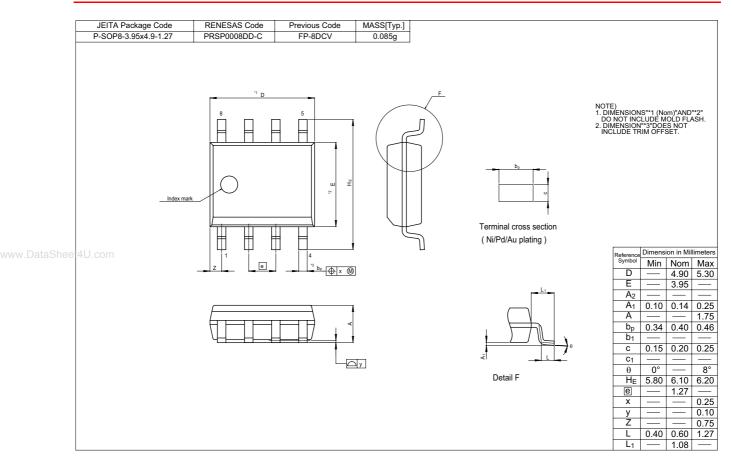
Package Dimensions







HA17558B Series





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