

# **HA17393B Series**

## **Dual Comparators**

REA03D0002-0100 Rev.1.00 Dec 25, 2006

### **Description**

HA17393B is dual comparators designed for general purpose, especially for power control systems. This IC operates from a single power-supply voltage over a wide range of voltages. Operation from split power supply current is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators have the merit which ground is included in the common-mode input voltage range at a single-voltage power supply operation.

#### **Features**

• Wide range of supply voltage

Single supply: 2 V to 36 V, Dual supplies:  $\pm 1$  V to  $\pm 18$  V

Very low supply current: 0.6 mA

• Small input bias current: 25 nA

• Small input offset voltage: 2 mV

• Common mode input voltage range includes ground.

• Low output saturation voltage: 200 mV at 4 mA

• Open collector output

• Package outline available in Pb free lead frame:

DP-8 SOP-8 (JEITA) SOP-8 (JEDEC)

### **Applications**

- Battery charger
- Cordless telephone
- Switching power supply
- DC/DC module
- · PC motherboard
- Communication equipment

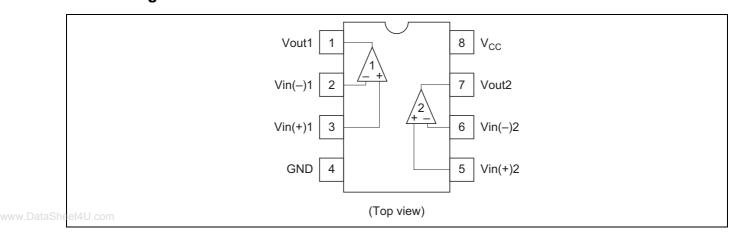
#### **Ordering Information**

Type No.	Application	Package Code (Package Name)	
HA17393B	Commercial use	PRDP0008AF-B (DP-8FV)	
HA17393BF		PRSP0008DE-B (FP-8DGV)	
HA17393BRP		PRSP0008DD-C (FP-8DCV)	

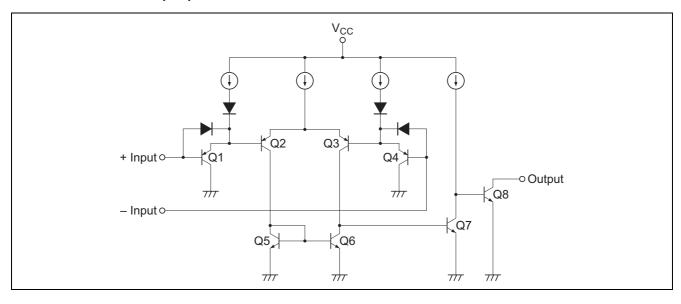
Note: This product is designed for consumer use and not for automotive.



### **Pin Arrangement**



### Circuit Schematic (1/2)



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

		Ratings			
Item	Symbol	HA17393B	HA17393BF	HA17393BRP	Unit
Supply Voltage	Vcc	36	36	36	V
Differential input voltage	V <sub>IN</sub> (diff)	Vcc	Vcc	Vcc	V
Input voltage	V <sub>IN</sub>	-0.3 to V <sub>CC</sub>	-0.3 to V <sub>CC</sub>	-0.3 to V <sub>CC</sub>	V
Input current (V <sub>IN</sub> < -0.3 V)	I <sub>IN</sub>	50	50	50	mA
Power dissipation	P <sub>T</sub>	570 * <sup>1</sup>	385 * <sup>2</sup>	385 * <sup>2</sup>	mW
Output short-circuit to ground		Continuous	Continuous	Continuous	
Operating temperature	Topr	-40 to +85	-40 to +85	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	-55 to +125	-55 to +125	°C

- www.DataShee Notes: 1. This is the allowable value up to Ta = 55°C. Derate by 8.3 mW/°C above that temperature.
  - 2. These are the allowable values up to Ta =  $25^{\circ}$ C mounting in air. When it is mounted on glass epoxy board of 40 mm  $\times$  40 mm  $\times$  1.5 mm (t) with 30% wiring density, the allowable value is 570 mW up to Ta =  $45^{\circ}$ C. If Ta >  $45^{\circ}$ C, derate by 7.14 mW/°C.

#### **Electrical Characteristics**

 $(Ta = 25^{\circ}C, V_{CC} = +5 V, unless otherwise specified)$ 

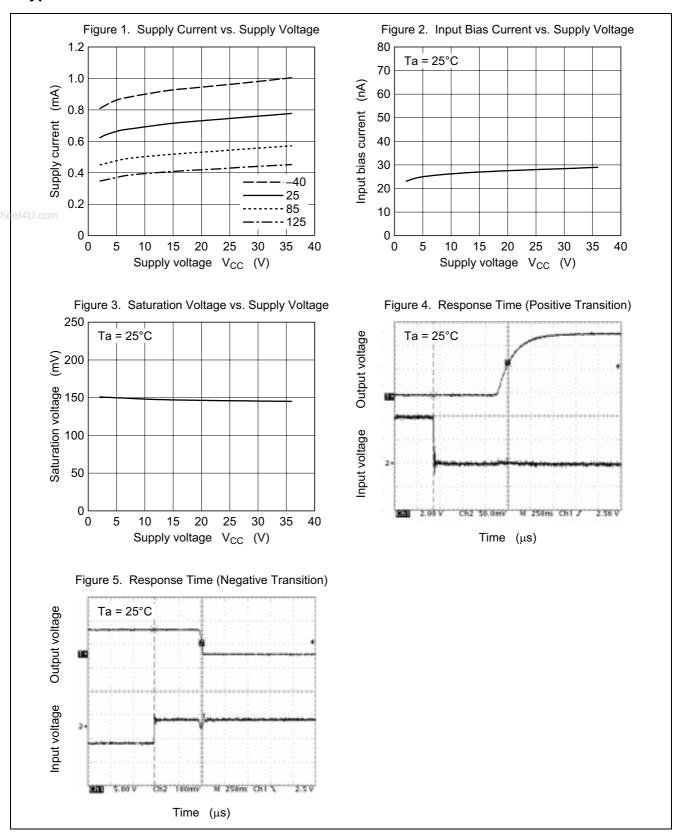
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	V <sub>IO</sub>	_	2	5	mV	$V_{O} = 1.4 \text{ V}, R_{S} = 0 \Omega$
						V <sub>CC</sub> = 5 V to 30 V
Input offset current	I <sub>IO</sub>		5	50	nA	V <sub>CM</sub> = 0 V
Input bias current	$I_{IB}$	_	25	250	nA	$V_{CM} = 0 \text{ V}, I_{IN(+)} \text{ or } I_{IN(-)} \text{ with}$
						output in linear range
Voltage gain	A <sub>V</sub>	_	200	_	dB	$V_{CC}$ = 15 V, $R_L \ge$ 15 k $\Omega$ ,
						V <sub>O</sub> = 1 V to 11 V
Common mode input voltage range	$V_{IR}$	0	_	V <sub>CC</sub> -1.5	V	V <sub>CC</sub> = 30 V
Supply current	Icc	_	0.6	1.0	mA	$V_{CC} = 5 \text{ V}, R_L = \infty$
		_	0.8	1.7	mA	$V_{CC} = 36 \text{ V}, R_L = \infty$
Response time	t <sub>R</sub>	_	1.3	_	μS	$V_{RL} = 5 \text{ V}, R_{L} = 5.1 \text{ k}\Omega$
Large signal response time	t <sub>RL</sub>	_	200	_	ns	V <sub>IN</sub> = TTL logic swing, V <sub>REF</sub> =
						1.4 V, $V_{RL} = 5 \text{ V}$ , $R_L = 5.1 \text{ k}\Omega$
Output sink current	I <sub>OSINK</sub>	6	16	_	mA	$V_{IN(-)} = 1 \text{ V}, V_{IN(+)} = 0 \text{ V},$
						V <sub>O</sub> = 1.5 V
Output saturation voltage	$V_{O(SAT)}$	_	200	400	mV	$V_{IN(-)} = 1 \text{ V}, V_{IN(+)} = 0 \text{ V},$
						I <sub>OSINK</sub> ≤ 4 mA
Output leakage current	I <sub>LO</sub>	_	0.1	_	nA	$V_{IN(-)} = 0 \text{ V}, V_{IN(+)} = 1 \text{ V},$
						V <sub>O</sub> = 5 V

# **Table of Graphs**

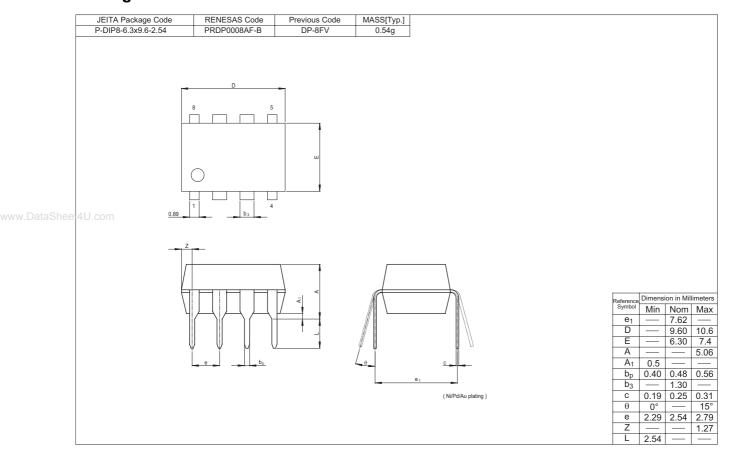
Electri	Figure	
Supply current	vs. Supply voltage ±V <sub>CC</sub>	1
Input bias current	vs. Supply voltage ±V <sub>CC</sub>	2
Saturation voltage	vs. Supply voltage ±V <sub>CC</sub>	3
Response time (Positive transition)	vs. Time s	4
Response time (Negative transition)	vs. Time s	5

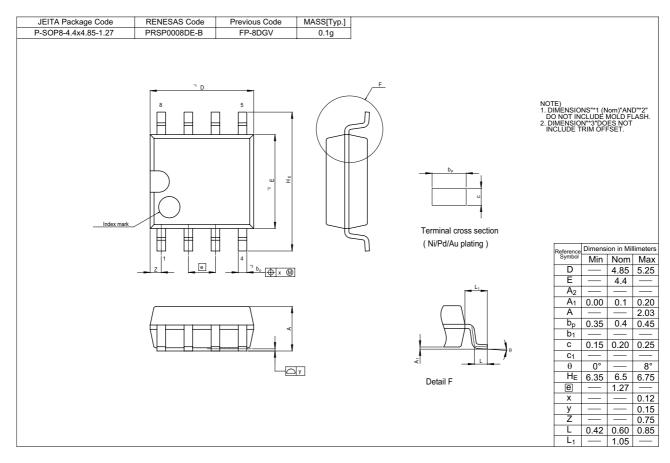
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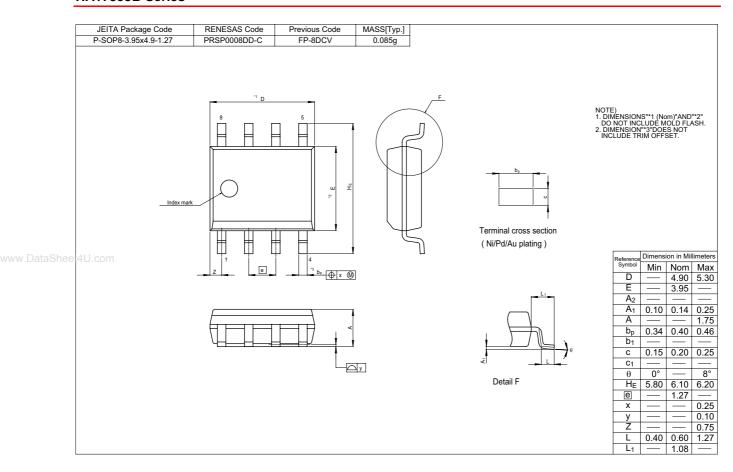
### **Typical Characteristics Curves**



### **Package Dimensions**







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