

Praetorian™ L-C LCD and Camera EMI Filter Array with ESD Protection

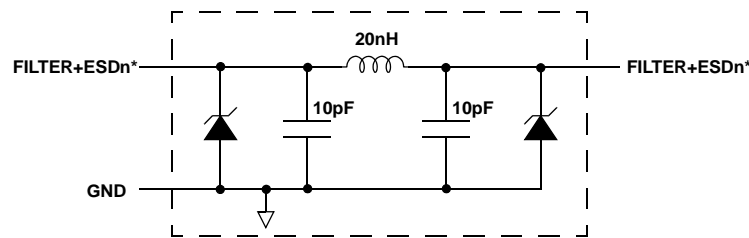
Features

- Eight channels of EMI filtering with integrated ESD protection
- Pi-style EMI filters in a capacitor-inductor-capacitor (C-L-C) network
- ±15kV ESD protection on each channel (IEC 61000-4-2 Level 4, contact discharge)
- ±30kV ESD protection on each channel (HBM)
- Greater than 30dB attenuation (typical) at 1GHz
- 16-lead uDFN lead-free package with 0.5mm lead pitch
- Tiny 4.00mm x 1.60mm uDFN package size
- Increased robustness against vertical impacts during manufacturing process
- Lead-free RoHS compliant finishing

Applications

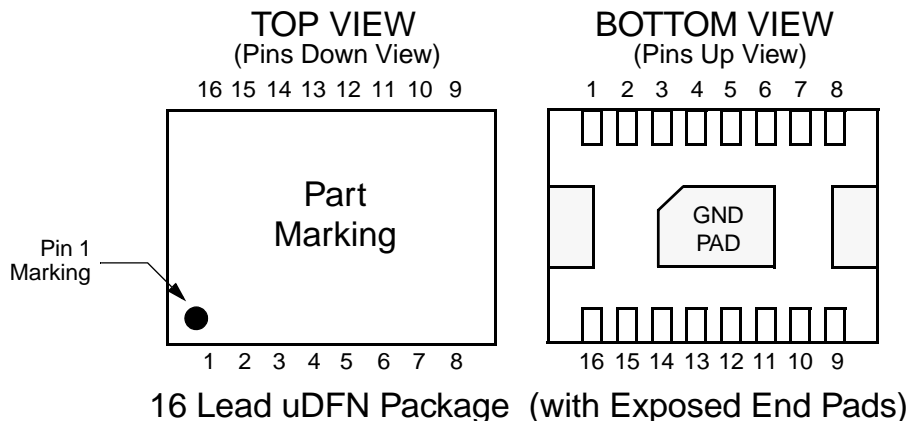
- LCD and camera data lines in mobile handsets
- I/O port protection for mobile handsets, notebook computers, PDAs etc.
- EMI filtering for data ports in cell phones, PDAs or notebook computers.
- Wireless handsets
- Handheld PCs/PDAs
- LCD and camera modules

Electrical Schematic



**1 of 8 EMI/RFI Filter Channels with Integrated
ESD Protection**

* See Package/Pinout Diagram for expanded pin information.

PACKAGE / PINOUT DIAGRAMS

Notes:

1) These drawings are not to scale.

PIN DESCRIPTIONS

PIN	NAME	DESCRIPTION	PIN	NAME	DESCRIPTION
1	FILTER1	Filter + ESD Channel 1	16	FILTER1	Filter + ESD Channel 1
2	FILTER2	Filter + ESD Channel 2	15	FILTER2	Filter + ESD Channel 2
3	FILTER3	Filter + ESD Channel 3	14	FILTER3	Filter + ESD Channel 3
4	FILTER4	Filter + ESD Channel 4	13	FILTER4	Filter + ESD Channel 4
5	FILTER5	Filter + ESD Channel 5	12	FILTER5	Filter + ESD Channel 5
6	FILTER6	Filter + ESD Channel 6	11	FILTER6	Filter + ESD Channel 6
7	FILTER7	Filter + ESD Channel 7	10	FILTER7	Filter + ESD Channel 7
8	FILTER8	Filter + ESD Channel 8	9	FILTER8	Filter + ESD Channel 8
GND PAD	GND	Device Ground			

Ordering Information
PART NUMBERING INFORMATION

Pins	Package	Lead-free Finish	
		Ordering Part Number ¹	Part Marking
16	uDFN-16 EEP	CM1661-08DE	P618E

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

Specifications

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNITS
Storage Temperature Range	-65 to +150	°C
Current per Inductor	30	mA
DC Package Power Rating	500	mW

STANDARD OPERATING CONDITIONS

PARAMETER	RATING	UNITS
Operating Temperature Range	-40 to +85	°C

ELECTRICAL OPERATING CHARACTERISTICS (SEE NOTE 1)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
L	Channel Inductance			20		nH
C _{TOTAL}	Total Channel Capacitance	At 2.5VDC Reverse Bias, 1MHz, 30mVAC	16	20	24	pF
C	Capacitance C1	At 2.5VDC Reverse Bias, 1MHz, 30mVAC		10		pF
V _{DIODE}	Standoff Voltage	I _{DIODE} =10μA		6.0		V
I _{LEAK}	Diode Leakage Current (reverse bias)	V _{DIODE} =+3.3V		0.1	1.0	μA
V _{SIG}	Signal Clamp Voltage Positive Clamp Negative Clamp	I _{LOAD} = 10mA I _{LOAD} = -10mA	5.6 -1.5	6.8 -0.8	9 -0.4	V V
V _{ESD}	In-system ESD Withstand Voltage a) Human Body Model, MIL-STD-883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	Notes 2, 3, and 4	±30 ±15			kV kV
R _{DYN}	Dynamic Resistance Positive Negative			2.3 0.9		Ω Ω
f _R	Roll-off Frequency at -6dB Attenuation Z _{SOURCE} =50Ω, Z _{LOAD} =50Ω			400		MHz

Note 1: T_A=25°C unless otherwise specified.

Note 2: ESD applied to input and output pins with respect to GND, one at a time.

Note 3: Clamping voltage is measured at the opposite side of the EMI filter to the ESD pin (i.e. if ESD is applied to pin A1 then clamping voltage is measured at pin C1). Unused pins are left open.

Note 4: These parameters are guaranteed by design and characterization.

Performance Information

Typical Filter Performance ($T_A=25^\circ\text{C}$, DC Bias=0V, 50 Ohm Environment)

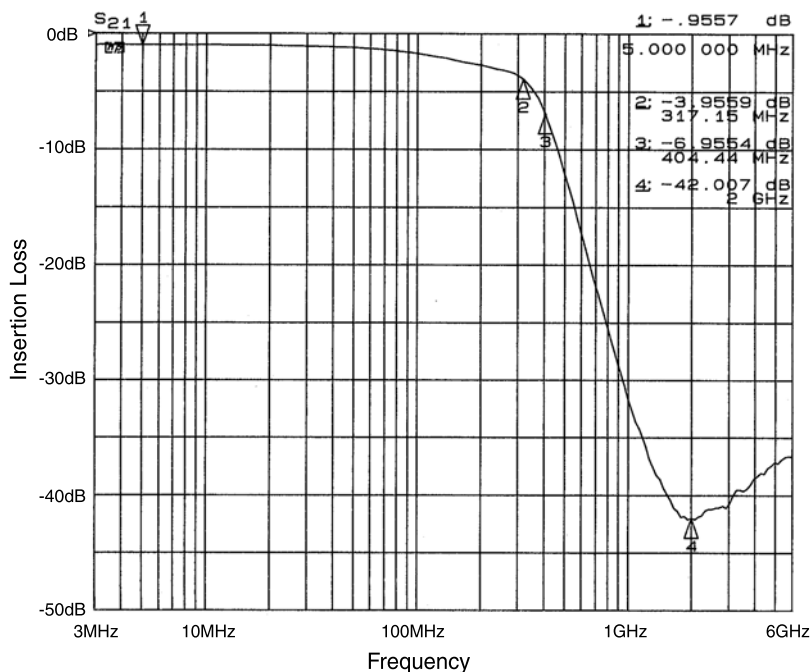


Figure 1. Filter 1 Insertion Loss (CM1661-08DE)

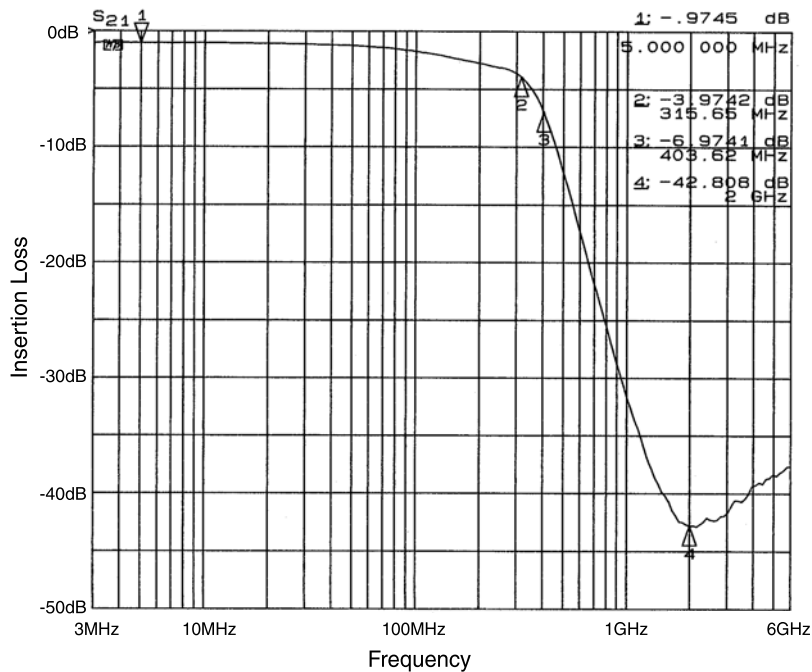


Figure 2. Filter 2 Insertion Loss (CM1661-08DE)

Performance Information

Typical Filter Performance ($T_A=25^\circ\text{C}$, DC Bias=0V, 50 Ohm Environment)

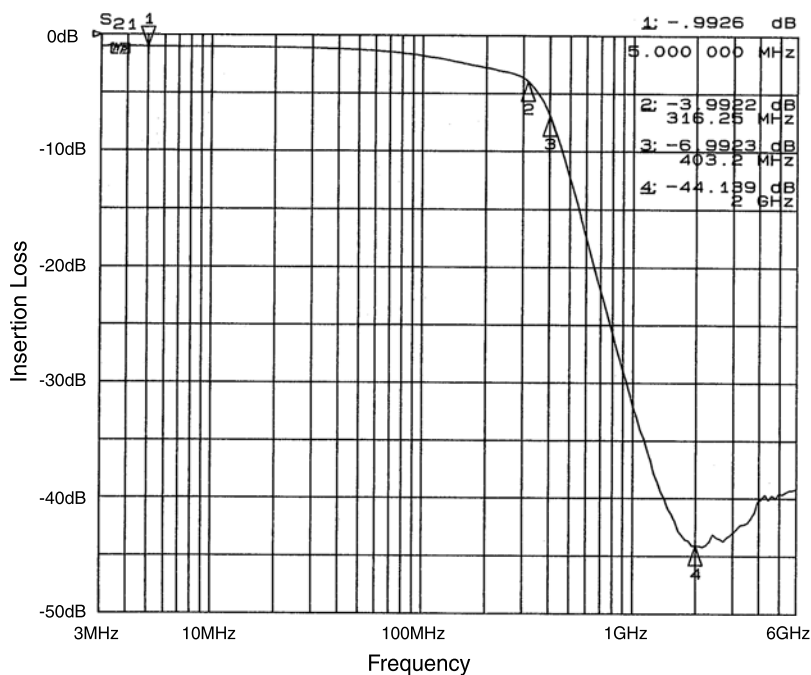


Figure 3. Filter 3 Insertion Loss (CM1661-08DE)

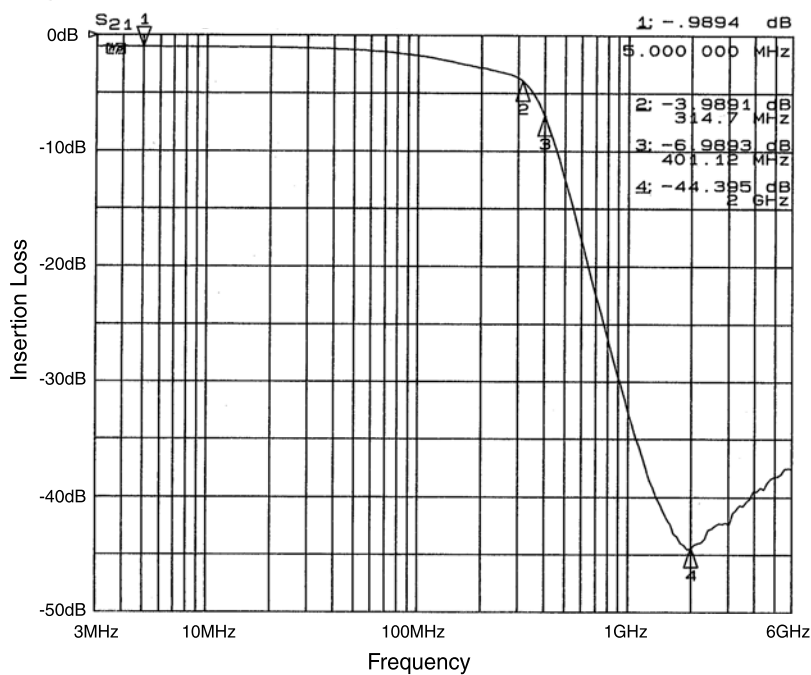


Figure 4. Filter 4 Insertion Loss (CM1661-08DE)

Performance Information

Typical Filter Performance ($T_A=25^\circ\text{C}$, DC Bias=0V, 50 Ohm Environment)

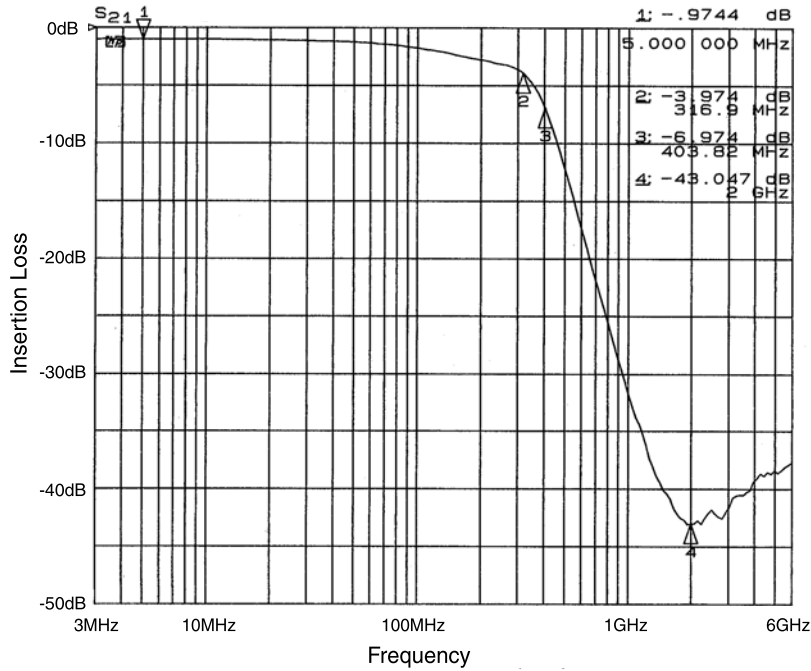


Figure 5. Filter 5 Insertion Loss (CM1661-08DE)

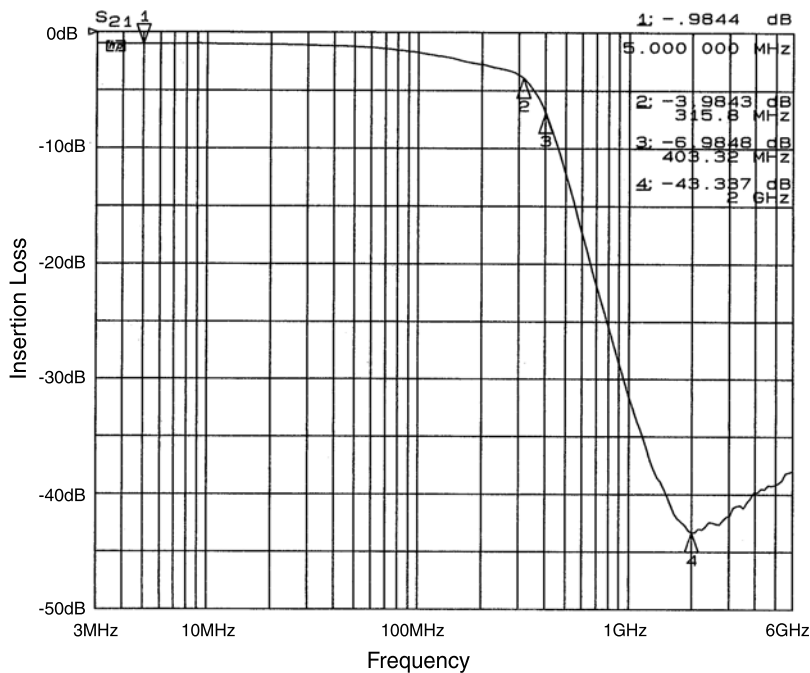


Figure 6. Filter 6 Insertion Loss (CM1661-08DE)

Performance Information

Typical Filter Performance ($T_A=25^\circ\text{C}$, DC Bias=0V, 50 Ohm Environment)

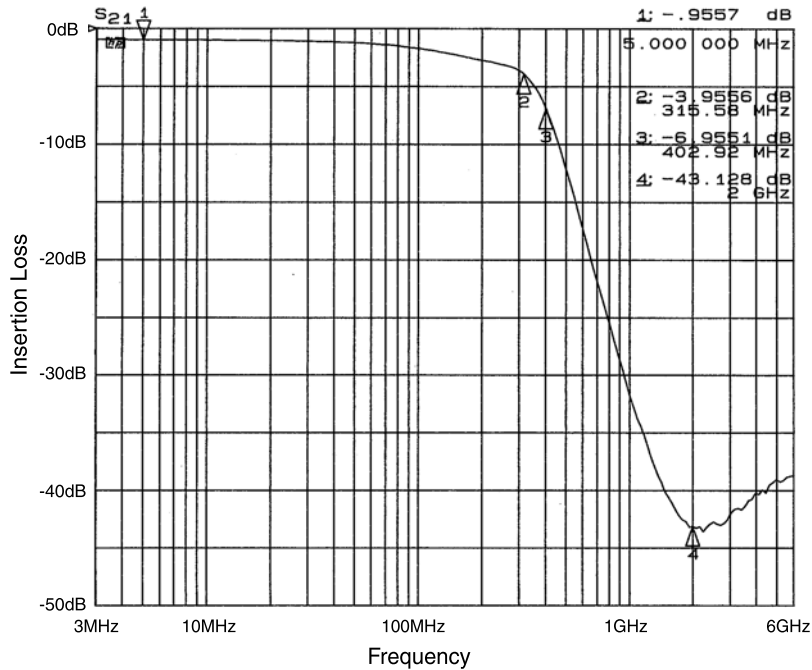


Figure 7. Filter 7 Insertion Loss (CM1661-08DE)

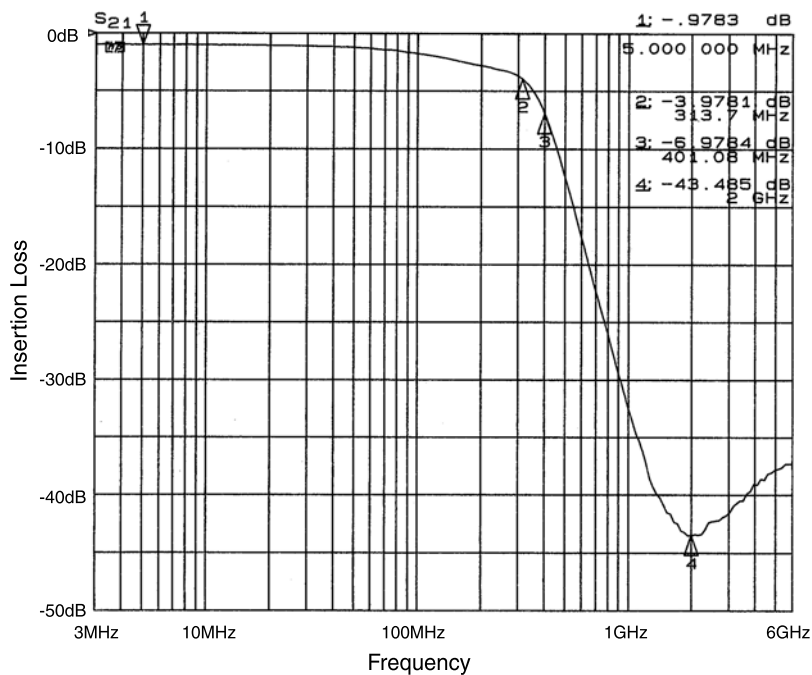
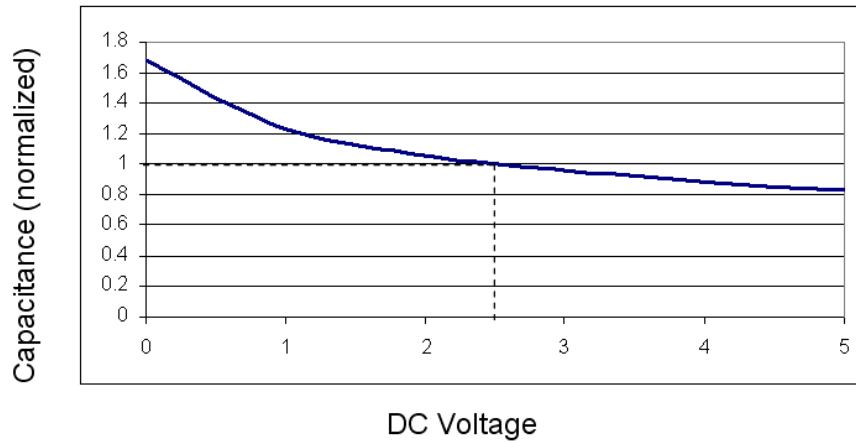


Figure 8. Filter 8 Insertion Loss (CM1661-08DE)

Typical Diode Capacitance vs. Input Voltage



**Figure 9. Filter Capacitance vs. Input Voltage
(normalized to capacitance at 2.5VDC and 25°C)**

Mechanical Details (Cont'd)

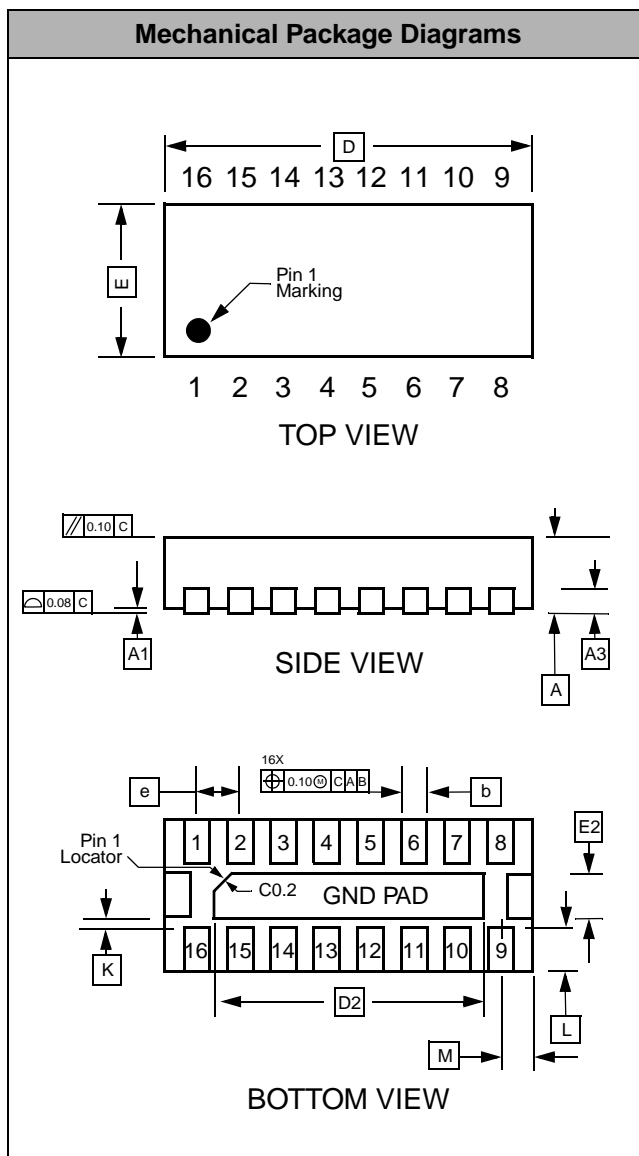
uDFN-16EEP Mechanical Specifications, 0.5mm

The CM1661-08DE is supplied in a 16-lead, 4.00mm x 1.60mm, 0.5mm pitch uDFN package with Exposed End Pads (EEP). Dimension are presented below.

For complete information on the uDFN-16, see the California Micro Devices uDFN Package Information document.

PACKAGE DIMENSIONS						
Package	uDFN					
JEDEC No.	MO-229C*					
Leads	16					
Dim.	Millimeters			Inches		
	Min	Nom	Max	Min	Nom	Max
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
A3	0.127 REF			0.005		
b	0.20	0.25	0.30	0.008	0.010	0.012
D	3.90	4.00	4.10	0.153	0.157	0.161
D2	3.10	3.20	3.30	0.122	0.126	0.130
E	1.50	1.60	1.70	0.059	0.063	0.067
E2	0.30	0.40	0.50	0.012	0.016	0.020
e	0.50 BSC			0.020 BSC		
K	0.20			0.008		
L	0.20	0.30	0.40	0.008	0.010	0.012
M	0.25 REF			0.010 REF		
# per tape and reel	3000 pieces					
Controlling dimension: millimeters						

*This package is compliant with JEDEC standard MO-229C with the exception of the D, D2, E, E2, K and L dimensions as called out in the table above.



Dimensions for 16-Lead, , 4.00mm x 1.60mm, 0.5mm pitch uDFN package with Exposed End Pads (EEP)