

HA11560FP/T

VCR/VDP RF Modulator

HITACHI

Rev. 1
Dec. 1993

This chip is designed for application as an RF modulator in VCRs and VDPs. A new type RF oscillator provides superior harmonic characteristics.

Functions

- Video modulator
- RF carrier oscillator
- White clip
- Channel switch
- Audio carrier converter
- ANT switch driver
- Carrier off switch

Features

- Superior harmonic characteristics allow easy pattern design.
- Sharp white chip characteristics with no distortion
- The use of miniature surface mount packaging allows substrate miniaturization (TSOP).

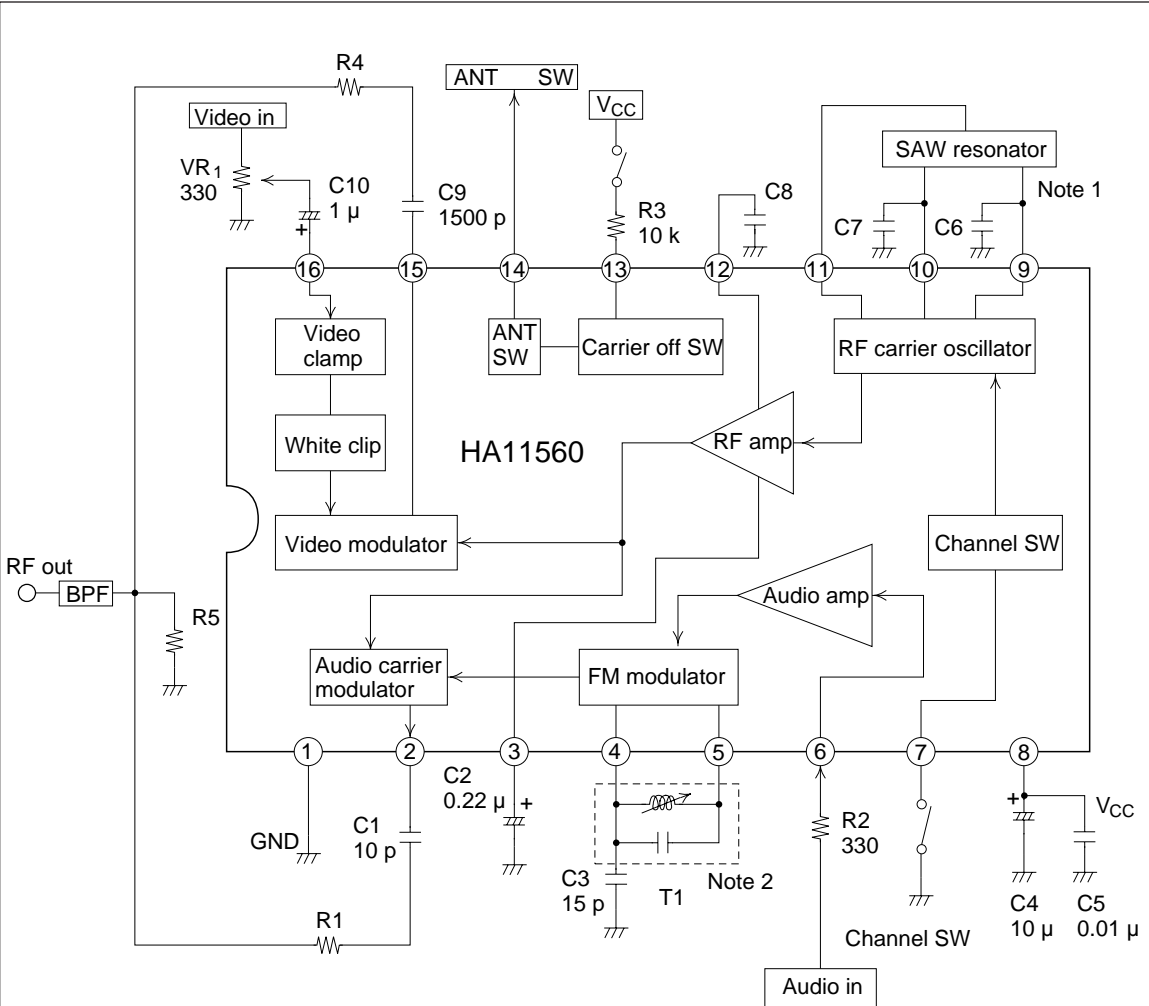
Ordering Information

Type No.	Package
HA11560FP	16 pin plastic SOP
HA11560T	16pin plastic TSOP



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Block Diagram



External Components (Notes 1 and 2)

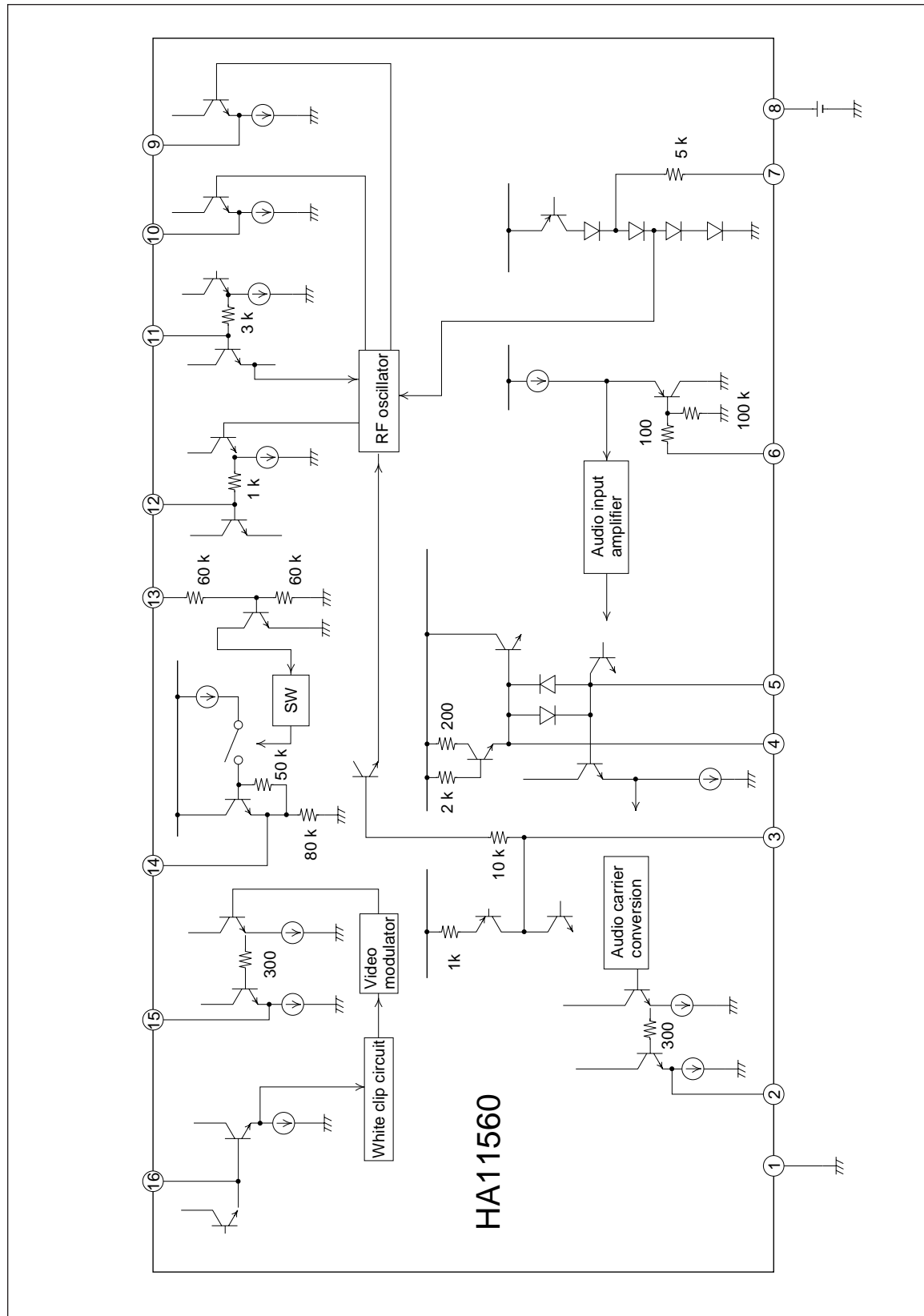
No.	Component	Model	Manufacturer	Remarks
1	SAW resonator	KAR91CS (Japanese specifications)	Kyocera, Inc.	KAR61CT (U. S. specifications)
2	4.5-MHz tank coil	291ACS-4060Z 62-pF tuning capacitor on-chip 369SN-1458Z External tuning capacitor	Toko, Inc.	Use 369SN-1458Z for greater tuning capacitance. Note this also decreases audio modulation factor.

Pin Functions

Pin No.	Symbol	Function	Voltage Pin	Remarks
1	GND	GND	GND	
2	AUDIO RF OUT	Audio RF signal output pin	3.2 V	Output Level = 85 dBμV
3	RF OSCILLATOR BIAS	RF oscillator voltage control pin	(3.1 V)	RF oscillator active state
4	FM MOD COIL	FM modulator tank coil connector pin	4.1 V	Reference voltage pin
5	FM MOD COIL	FM modulator tank coil connector pin	4.1 V	Swing Level \approx 1.0 Vp-p
6	AUDIO IN	Audio signal input pin	0 V	
7	CHANNEL SW	RF oscillation frequency channel switch	2.8 V (when open)	Pin 9 output (when open)
8	V _{CC}	V _{CC}	5V	
9	SAW.R LOW CHANNEL	SAW resonator connector pin (low ch)	3.7 V	Swing Level \approx 80 mVp-p
10	SAW.R HIGH CHANNEL	SAW resonator connector pin (high ch)	3.7 V	
11	SAW.R COMMON	SAW resonator connector pin (common)	3.2 V	
12	DP Adj.	Video differential phase and harmonic level adjust pin	2.6 V	Good oscillating stability smaller capacitance value
13	CARRIER OFF SW	RF switch driver and RF oscillator switch pin	0 to 5 V	0 V (when open)
14	ANT SW DRIVER	RF switch driver drive pin	0 to 3.6 V	Maximum output current = 30 mA
15	VIDEO RF OUT	Video RF signal output pin	3.1 V	Note: Output level = 91.5 dBμV
16	VIDEO IN	Video signal input pin	1.6 V	

Note:	Channel	Carrier frequency
	JPN1	91.25 MHz
	JPN2	97.25 MHz
	US3	61.25 MHz
	US4	67.25 MHz

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Block Operation Descriptions

The following describes the operation of each block and external circuits.

Video Clamp

The video clamp circuit employs a sync peak value clamp. Input impedance is at a level of approximately 30 k Ω to reduce compression of the sync pulse. Note that sag is generated in the sync pulse when a capacitor of 0.47 μ F or lower is used for C10 at pin 16.

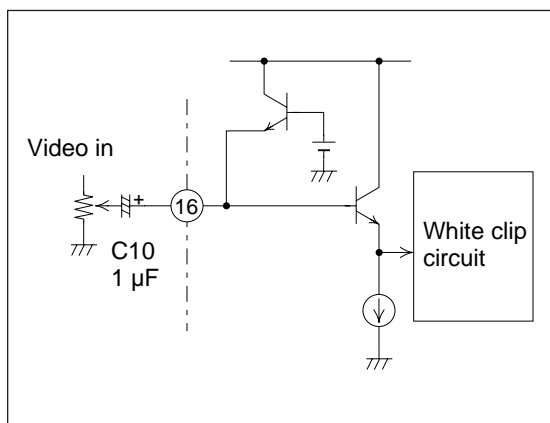


Figure 1 Video Input Circuit

White Clip Circuit

The white clip level is set to 92.5%. A circuit that produces a sharp white clip is used in order to produce high linearity characteristics for the video. Increasing the white clip frequency characteristic by another 5 MHz eliminates video carrier cutoff by the chroma signal and prevents deterioration of audio buzz characteristics.

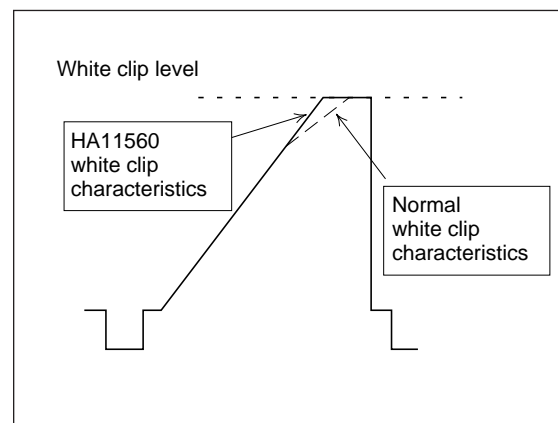


Figure 2 White Clip Characteristics

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RF Oscillator/Channel Switch

A new type RF oscillator is employed to reduce RF harmonics to a level that is dramatically lower than previous available Hitachi products.

Note that using a capacitor lower than $0.22\ \mu\text{F}$ at pin 3 causes an increase in sync pulse sag.

The input resistance at pin 11, which is the oscillator input pin, is $3\ \text{k}\Omega$. Pins 9 and 10 are oscillator output pins, and are NPN transistor emitter-follower types.

The oscillator frequency is selectable by switching the pin 7 channel switch between SAW resonator low channel and high channel. When the channel switch is open, oscillation output is performed from pin 9, while grounding the switch causes output from pin 10. The switching voltage of the channel switch is approximately $2.1\ \text{V}$. When the channel switch is grounded, a current of $30\ \mu\text{A}$ flows through pin 7, which is sufficient to drive an electronic switch.

Antenna Switch

The antenna switch links the drive circuit that switches the RF switch on and off with the chip's

RF oscillator on/off operation.

Opening the switch at pin 13 stops oscillation of the RF oscillator and the antenna switch drive output at pin 14 becomes $0\ \text{V}$. The audio system is operating in this state, which acts to suppress generation of popping noise in the audio as the antenna switches between on and off.

When pin 13 is connected to V_{CC} by a closed switch, the RF oscillator is operational and the antenna switch drive output DC level at pin 14 is high.

Pin 13 should be connected to V_{CC} when the antenna switch is not used. A maximum current of $30\ \text{mA}$ can be obtained from the antenna switch driver.

Be sure to connect $10\ \text{k}\Omega$ for R3 at pin 13 when the antenna switch is being used. R2 is used as protective resistance when the antenna switch power supply is switched on before the chip power supply.

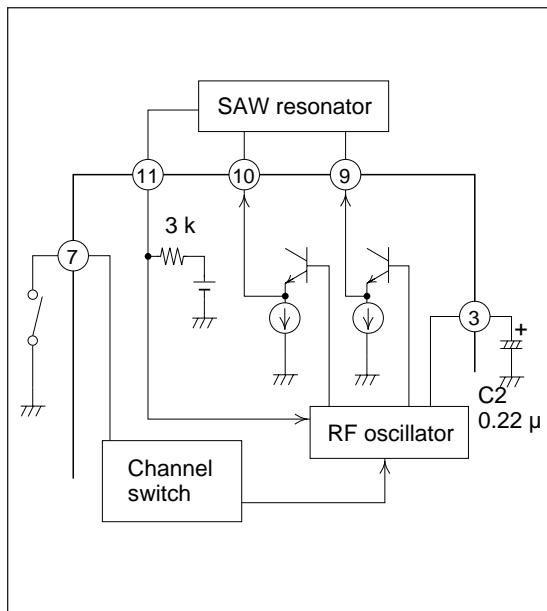


Figure 3 RF Oscillator Interface Circuit

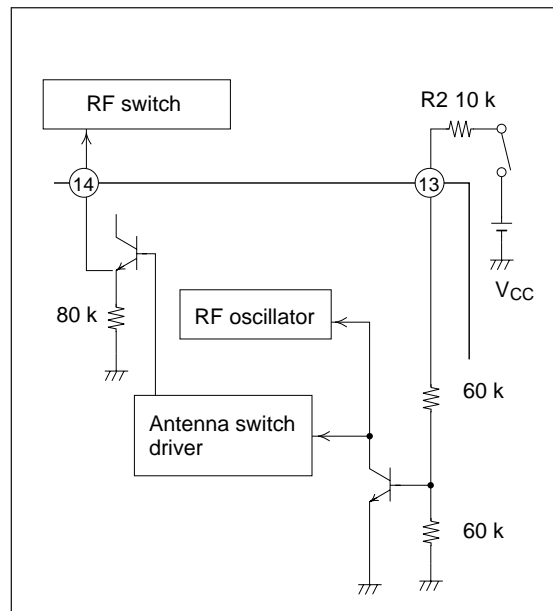


Figure 4 Antenna Switch Interface Circuit

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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Rating	Unit
Supply voltage	V _{CC}	6.0	V
Power dissipation	P _T	400 (HA11560FP) 275 (HA11560T)	mW
Storage temperature	T _{stg}	−55 to +125	°C
Operating temperature	T _{opr}	−20 to +70	°C

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions	Application Terminal
Supply current	I _{SS}	9	13	18	mA		8
Video output level	V _O	89.5	91.5	94.5	dBμ	No input	15
Video modulation factor	V _M	73	—	79	%	Video input = 0.635 Vp-p	15
		77	80	83			
		81	—	87			
Video output channel difference	ΔV _O	−2	0	2	dB		15
Video modulation factor channel difference	ΔV _M	−2	0	2	%		15
Video output S/N	V _{SN}	—	55	—	dB	VM = 50%	15
White clip level	V _W	89	92.5	96	%		15
Differential gain	DG	—	2	5	%	80% modulation at upper part of chroma signal 20 IRE	15
Differential phase	DP	—	2	5	deg		15
Audio output P/S ratio	A _O	5.0	6.5	8.0	dB		2, 15
Audio modulation factor	A _M	22.4	24.4	26.4	± kHz/ dev	Input 0.14 Vp-p	2
Audio S/N	ASN	50	60	—	dB	Audio modulation = 60 %	5
Video input impedance	R _V	10	30	—	kΩ		16
Audio input impedance	R _A	75	100	145	kΩ		6
Spurious response within band	Sin	—	−75	−65	dB	Measurement of spurious response between f _V and f _A	2, 15
920-kHz beat	cb	—	−68	−61	dB	0.635 Vp-p input	2, 15
Antenna switch pin voltage	A _{SV}	3.1	3.4	3.7	V	Load current = 15 mA when supply voltage = 5 V	14

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