Reversible motor driver BA6219B / BA6219BFP-Y

The BA6219B and BA6219BFP-Y are reversible-motor drivers suitable for brush motors. Two logic inputs allow four output modes: forward, reverse, idling, and braking. The motor revolving speed can be set arbitrarily by controlling the voltage applied to the motor.

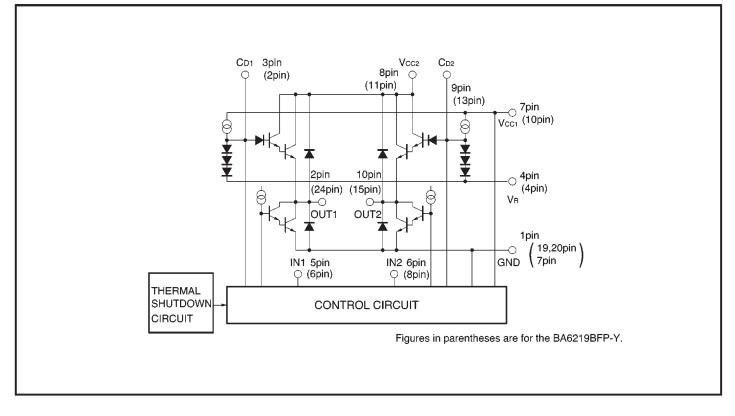
ApplicationsVCRs and cassette tape recorders

Features

- 1) Large output current. (Io=2.2A Max.)
- 2) Built-in thermal shutdown circuit.

- 3) Built-in output voltage setting pins.
- 4) Small standby supply current.

Block diagram



•Absolute maximum ratings (Ta = 25° C)

Parameter		Symbol	Limits	Unit
Power supply voltage		Vcc1, Vcc2	24	V
Power	BA6219B	Dd	2200*1	14/00
dissipation	BA6219BFP - Y	Pd	1450*²	mW
Operating te	emperature	Topr	-20~+75	Ĉ
Storage ten	nperature	Tstg	-50~+125	Ĉ
Output curre	ent	lo	2.2*3	А
Input voltage		Vin	-0.3~Vcc1	V

*1 Reduced by 22 mW for each increase in Ta of 1℃ over 25℃.

*2 Wnen mounted on a 90 X 50 X 1.6 mm glass epoxy board. Reduced by 14.5 mW for each increase in Ta of 1°C over 25°C.

*3 500 μ s pulse with a duty ratio of 1%.

• Recommended operating conditions (Ta = 25° C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Operating power	Vcc1		_	18	V
supply voltage	Vcc2	°		10	V

•Electrical characteristics (unless otherwise noted, $Ta = 25^{\circ}C$ and Vcc = 12V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Current dissipation 1	Icc1	—	1.2	2.5	mA	Both input pins LOW
Current dissipation 2	ICC2	—	16	35	mA	One input pin HIGH, the other LOW
Current dissipation 3	Іссз	_	25	60	mA	Both input pins HIGH
Input threshold voltage	Vтн	1.0	2.0	3.0	V	Low level is 1 V or less, HIGH level is 3 V or more
Output high level voltage	Vн	6.5	_	-	V	RL=60Ω, ZD=6.8V
Output low level voltage	VL	_	_	1.2	V	RL=60Ω

Electrical characteristic curves

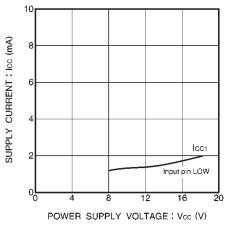


Fig.1 Current dissipation 1 vs. power supply voltage

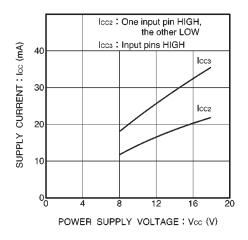


Fig.2 Current dissipation 2,3 vs. power supply voltage

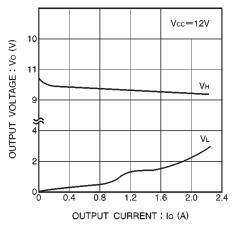


Fig.3 Output voltage vs. output current

ROHM

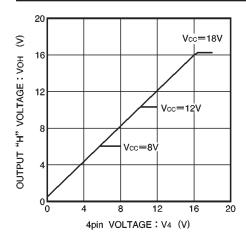
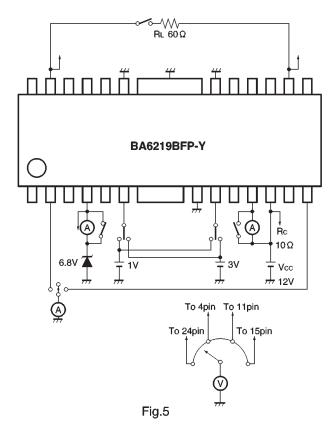


Fig.4 Output voltage vs. 4 pin voltage

Measurement circuits



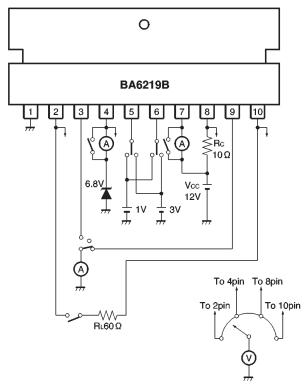


Fig.6

Input / output truth table

Inp	out	Ou	Mode	
IN1	IN2	OUT1	OUT2	MOGE
L	L	OPEN	OPEN	Idling
Н	L	Н	L	Forward
L	Н	L	Н	Reverse
Н	Н	L	L	Braking

Note: HIGH level input is 3.0 V or more. LOW level input is 1.0 V or less.





Motor driver ICs

BA6219B / BA6219BFP-Y

Pin descriptions

(BA6219BFP-Y)

1-N.C.2 C_{D1} Capacitor connection pin for preventing both output transistors being turned on at the same time3-N.C.4VROutput HIGH voltage setting5-N.C.6IN1Logic input7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13Cb2Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output25-N.C.	Pin No.	Pin name	Function
2 C_{D1} transistors being turned on at the same time3-N.C.4 V_R Output HIGH voltage setting5-N.C.6IN1Logic input7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13Cb2Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	1	—	N.C.
4 V_R Output HIGH voltage setting5-N.C.6IN1Logic input7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13CD2Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	2	CD1	
5-N.C.6IN1Logic input7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13Cb2Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	3	_	N.C.
6IN1Logic input7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13 C_{D2} Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	4	Vr	Output HIGH voltage setting
7GNDGND8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13CD2Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	5	-	N.C.
8IN2Logic input9-N.C.10Vcc1Small signal section power supply11Vcc2Motor output power supply12-N.C.13 C_{D2} Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	6	IN1	Logic input
9 - N.C. 10 Vcc1 Small signal section power supply 11 Vcc2 Motor output power supply 12 - N.C. 13 Cb2 Capacitor connection pin for preventing both output transistors being turned on at the same time 14 - N.C. 15 OUT2 Motor output 16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 23 - N.C. 23 - N.C. 24 OUT1 Motor output	7	GND	GND
10 Vcc1 Small signal section power supply 11 Vcc2 Motor output power supply 12 - N.C. 13 Cb2 Capacitor connection pin for preventing both output transistors being turned on at the same time 14 - N.C. 15 OUT2 Motor output 16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 23 - N.C. 24 OUT1 Motor output	8	IN2	Logic input
11 Vcc2 Motor output power supply 12 - N.C. 13 Cb2 Capacitor connection pin for preventing both output transistors being turned on at the same time 14 - N.C. 15 OUT2 Motor output 16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 23 - N.C. 24 OUT1 Motor output	9	—	N.C.
12-N.C.13 C_{D2} Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.22-N.C.23-N.C.24OUT1Motor output	10	Vcc1	Small signal section power supply
13 C_{D2} Capacitor connection pin for preventing both output transistors being turned on at the same time14-N.C.15OUT2Motor output16-N.C.17-N.C.18-N.C.19GNDGND20GNDGND21-N.C.23-N.C.24OUT1Motor output	11	Vcc2	Motor output power supply
13 Cb2 transistors being turned on at the same time 14 - N.C. 15 OUT2 Motor output 16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 23 - N.C. 24 OUT1 Motor output	12	_	N.C.
15 OUT2 Motor output 16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	13	CD2	
16 - N.C. 17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	14	_	N.C.
17 - N.C. 18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	15	OUT2	Motor output
18 - N.C. 19 GND GND 20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	16	-	N.C.
19 GND GND 20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	17	-	N.C.
20 GND GND 21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	18	—	N.C.
21 - N.C. 22 - N.C. 23 - N.C. 24 OUT1 Motor output	19	GND	GND
22 - N.C. 23 - N.C. 24 OUT1 Motor output	20	GND	GND
23 - N.C. 24 OUT1 Motor output	21	—	N.C.
24 OUT1 Motor output	22	_	N.C.
· · ·	23	_	N.C.
25 — N.C.	24	OUT1	Motor output
	25	_	N.C.
FIN GND GND	Fıℕ	GND	GND

(BA6219B)

Pin No.	Pin name	Function
1	GND	GND
2	OUT 1	Motor output
3	CD1	Capacitor connection pin for preventing both output transistors being turned on at the same time
4	VR	Output HIGH voltage setting
5	IN1	Logic input
6	IN2	Logic input
7	Vcc1	Control circuit power supply
8	Vcc2	Output power supply
9	Cd2	Capacitor connection pin for preventing both output transistors being turned on at the same time
10	OUT 2	Motor output

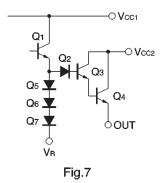
 $\ensuremath{\ast}$ All the GND pins have to be connected.

Operation notes

(1) A schematic for the internal circuit associated with the output voltage setting pin (Pin 4) is shown in Fig. 7. The maximum output voltage (V_{OMAX}.) is given by : $V_{OMAX}=V_{CC1} - V_{SAT} (Q1) - V_{BE} (Q2) - V_{BE} (Q3) - V_{BE} (Q4)$ For the condition of V_R being equal to or less than V_{OMAX}., the relationship between the output voltage (V₀) and the pin 4 voltage is given by :

 $V_{O} = V_{R} + \{ (V_{BE (Q5)} + V_{BE (Q6)} + V_{BE (Q7)}) - (V_{BE (Q2)} + V_{BE} (Q3) + V_{BE (Q4)}) \}$

Though ΔV_{BE} depends on the output power supply, V_0 is nearly equal to $V_{\text{R}.}$



(2) Thermal shutdown circuit

The thermal shutdown circuit turns off the driver output if the chip temperature rises to about 180°. The shutdown signal is not latched.

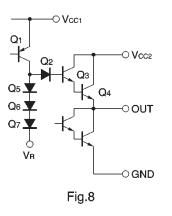
(3) Power supply impedance

When the motor stops or starts, a rush current flows in the VCC and GND lines. Depending on the way of connecting a power supply decoupling capacitor $(100\mu F)$, the control input voltage may become a negative value or the supply voltage may drop to below the output voltage. This can cause erratic operations due to parasitic effects. Make sure that pin voltages will not exceed the supply voltage by more than 0.3V or will not become less than the GND pin voltage by more than 0.3V.

(4) VCC1, VCC2, and VR are related to each other as shown in the output circuit diagram of Fig. 8. Their values should be kept within the following ranges.

Operating supply voltage ranges

Pin	Voltage	Unit
Vcc1	8~18	V
Vcc2	8~18	V
VR	Indicated in the following	_



1) When the output voltage control pin (pin 4) is used $V_R < V_{CC1} - (V_{SAT (Q1)} + V_{BE (Q5)} + V_{BE (Q6)} + V_{BE (Q7)}) \Rightarrow V_{CC1}$ - 2.5V

$$\begin{split} & V_{R} < V_{CC2} - \left\{ \left(V_{SAT (Q3)} - V_{BE (Q3)} - V_{BE (Q2)} \right) + \left(V_{BE (Q5)} + V_{BE (Q6)} + V_{BE (Q7)} \right) \right\} \\ & \stackrel{\leftarrow}{\Rightarrow} V_{CC2} - 1V \end{split}$$

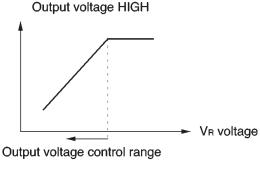
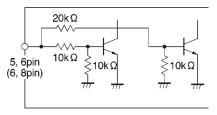


Fig.9

- 2) When the output voltage control pin (pin 4) is not used, V_R is either short-circuited to V_{CC1} or left OPEN.
- (5) Input circuit



Figures in parentheses are for the BA6219BFP-Y

Fig.10



(6) The quality of these products have been carefully checked; however, use of the products with applied voltages, operating temperatures, or other parameters that exceed the absolute maximum rating given may result in the damage of the IC and the product it is used in. If the IC is damaged, the short mode and open modes cannot be specified, so if the IC is to be used in applications where parameters may exceed the absolute maximum ratings, then be sure to incorporate fuses, or other physical safety measures.

(7) Input pins

Voltage should never be applied to the input pins when the V_{cc} voltage is not applied to the IC. Similarly, when V_{cc} is applied, the voltage on each input pin should be less than V_{cc} and within the guaranteed range for the electrical characteristics.

(8) Back-rush voltage

Depending on the ambient conditions, environment, or motor characteristics, the back-rush voltage may fluctuate. Be sure to confirm that the back-rush voltage will not adversely affect the operation of the IC.

(9) Power dissipation

The power dissipation will fluctuate depending on the mounting conditions of the IC and the ambient environment. Make sure to carefully check the thermal design of the application where these ICs will be used.

(10) Power consumption

The power consumption by the IC varies widely with the power supply voltage and the output current. Give full consideration to the power dissipation rating and the thermal resistance data and transient thermal resistance data, to provide a thermal design so that none of the ratings for the IC are exceeded.

(11) ASO

Make sure that the output current and supply voltage do not exceed the ASO values.

(12) Precautions for input mode switching

To ensure reliability, it is recommended that the mode switching for the motor pass once through the open mode.

(13) There are no circuits built into these ICs that prevent in-rush currents. Therefore, it is recommended to place a current limiting resistor or other physical countermeasure.

(14) If the potential of the output pin sways greatly and goes below the potential of ground, the operation of the IC may malfunction or be adversely affected. In such a case, place a diode between the output and ground, or other measure, to prevent this.

•Application examples

(1) BA6219B

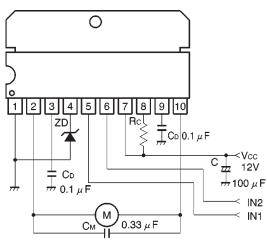


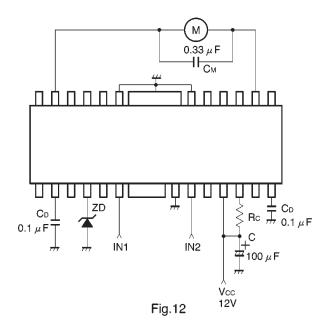
Fig.11

- ZD : Zener diode to set output voltage. Use any zener diode that is suitable for your application.
- $\label{eq:Rc} Rc : Resistor used for reducing collector loss and limiting the short-circuit current. A resistance range of $3 \sim 10 Ω is recommended.$
- C : Power supply filtering capacitor. Place as near as possible to the Vcc1 pin.
- CD: Capacitor to prevent both output transistors being turned on at the same time.
- CM : Capacitor to absorb surge voltage and prevent parasitic oscillations.

BA6219B BA6219BFP-Y 26.5±0.3 3.6±0.2 25 ± 0.2 R1.6 13.6±0.2 2.75±0.1 16.2±0.2 8888888 7.8±0.3 27 ± 0.5 5.4土0.2 8.4±0.3 \bigcirc С 1.6 8.4±0.5 0.25±0. 13 1.95±0.1 1.9±0.1 ынныны 0.6 2.54 0.8 0.11 0.8 0.36±0.1 || 0.3Min. 0.5±0.1 0.15 HSIP10 HSOP25

External dimensions (Units: mm)

(2) BA6219BFP-Y



rohm