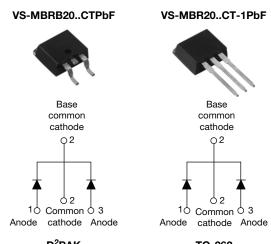


Vishay High Power Products

Schottky Rectifier, 2 x 10 A



D²PAK

TO-262

PRODUCT SUMMARY			
I _{F(AV)}	2 x 10 A		
V _R	35 V/45 V		
I _{RM}	15 mA at 125 °C		

FEATURES

- 150 °C T_J operation
- Center tap D²PAK and TO-262 packages
- · Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



RoHS COMPLIANT

HALOGEN

FREE

- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform (per device)	20	٨		
I _{FRM}	$T_{\rm C}$ = 135 °C (per leg)	20	A		
V _{RRM}		35/45	V		
I _{FSM}	t _p = 5 μs sine	1060	А		
V _F	10 Apk, T _J = 125 °C	0.57	V		
TJ	Range	- 65 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRB2035CTPbF VS-MBR2035CT-1PbF	VS-MBRB2045CTPbF VS-MBR2045CT-1PbF	UNITS
Maximum DC reverse voltage	V _R	35	45	V
Maximum working peak reverse voltage	V _{RWM}		40	v

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg	I	T 125 °C rotod V		10	
forward current per device	I _{F(AV)}	$1_{\rm C} = 133$ C, fate	$T_{\rm C}$ = 135 °C, rated $V_{\rm R}$		
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 135 °C		20	_
		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1060	A
Non-repetitive peak surge current I _{FSM}		Surge applied at single phase, 60 l	rated load conditions halfwave, Hz	150	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4 mH		8	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop V _{FM} (20 A	T _J = 25 °C	0.84	
	V _{FM} ⁽¹⁾	10 A	- T _J = 125 °C	0.57	V
		20 A		0.72	
Maximum instantaneous	I (1)	T _J = 25 °C	Rated DC voltage	0.1	mA
reverse current	I _{RM} ⁽¹⁾	T _J = 125 °C		15	
Threshold voltage	V _{F(TO)}	$T_J = T_J maximum$		0.354	V
Forward slope resistance	r _t			17.6	mΩ
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		600	pF
Typical series inductance	LS	Measured from top of terminal to mounting plane		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	TJ		- 65 to 150	°C	
Maximum storage temperature range	T _{Stg}		- 65 to 175	-0	
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	2.0	°C 4M	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	- °C/W	
			2	g	
Approximate weight			0.07	oz.	
Mounting torgue minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm	
maximum		Non-Iubricated threads	12 (10)	(lbf · in)	
Marking daviag		Case style D ² PAK	MBRB2	045CT	
Marking device		Case style TO-262	MBR204	45CT-1	



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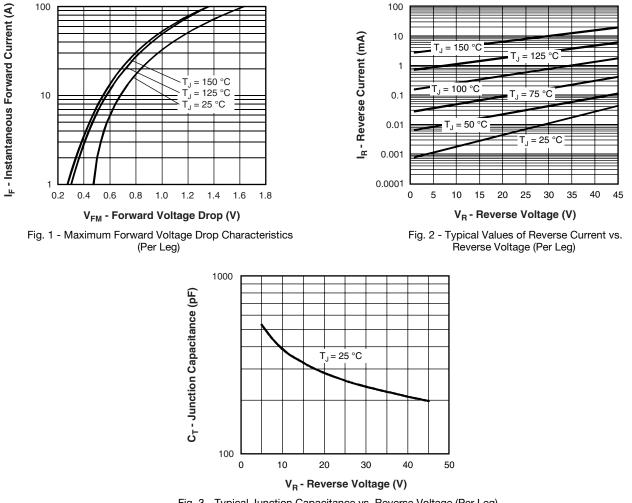


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

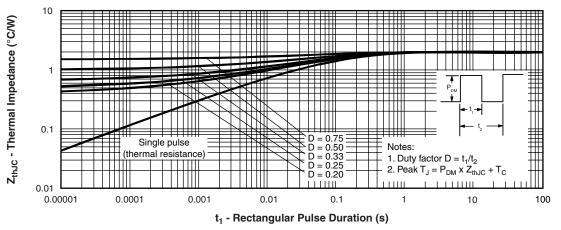
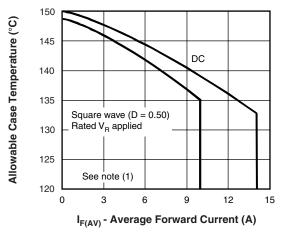
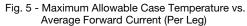


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



Vishay High Power Products Schottky Rectifier, 2 x 10 A





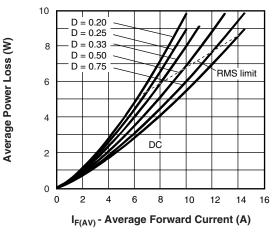
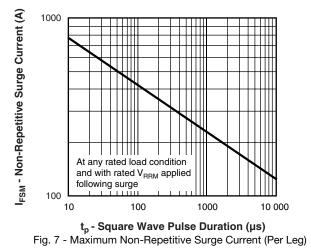


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



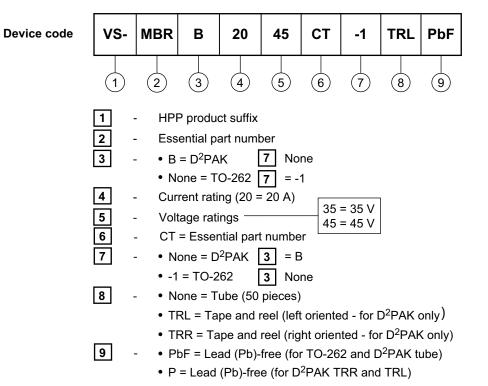
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \, \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \, \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95014</u>			
Part marking information www.vishay.com/doc?95008			
Packaging information www.vishay.com/doc?95032			



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