

Data Sheet

#### March 29, 2007

FN6475.0

# Radiation Hardened Ultra High Frequency NPN-PNP Transistor Array

intercil

The ISL73096RH is a radiation hardened transistor array consisting of three NPN transistors and two PNP transistors on a common substrate. One of our bonded wafer, dielectrically isolated fabrication processes provides an immunity to Single Event Latch-up and the capability of highly reliable performance in any radiation environment.

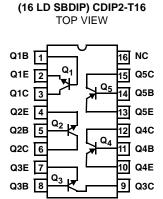
The high gain-bandwidth product and low noise figure of these transistors make them ideal for use in high frequency amplifier and mixer applications. Monolithic construction of the NPN and PNP transistors provides the closest electrical and thermal matching possible. Access is provided to each terminal of the transistors for maximum application flexibility.

Specifications for Rad Hard QML devices are controlled by the Defense Supply Center in Columbus (DSCC). The SMD numbers listed here must be used when ordering.

Detailed Electrical Specifications for these devices are contained in SMD 5962-07218. A "hot-link" is provided on our website for downloading.

ISL73096RH

## Pinout



## Features

- Electrically Screened to SMD # 5962-07218
- QML Qualified per MIL-PRF-38535 Requirements
- Radiation Environment
  - Gamma Dose (γ) ..... 3 x 10<sup>5</sup>RAD(Si)
  - SEL Immune..... Bonded Wafer Dielectric Isolation
- NPN Gain Bandwidth Product (F<sub>T</sub>) .....8GHz (Typ)
- NPN Current Gain (h<sub>FE</sub>)..... 130 (Typ)
- NPN Early Voltage (V<sub>A</sub>) ..... 50V (Typ)
- PNP Gain Bandwidth Product (F<sub>T</sub>)..... 5.5GHz (Typ)
- PNP Current Gain (h<sub>FF</sub>)..... 60 (Typ)
- PNP Early Voltage (V<sub>A</sub>) ..... 20V (Typ)
- Noise Figure (50Ω) at 1GHz .....3.5dB (Typ)
- Collector-to-Collector Leakage.....

# Applications

- High Frequency Amplifiers and Mixers
  - Refer to Application Note AN9315
- High Frequency Converters
- Synchronous Detectors

#### Ordering Information

ORDERING NUMBER	INTERNAL MKT. NUMBER	TEMP. RANGE (°C)
5962F0721801V9A	ISL73096RHVX	-55 to +125

# **Die Characteristics**

## DIE DIMENSIONS:

52.8 mils x 52.0 mils x 14 mils  $\pm 1$  mil 1340 $\mu m$  x 1320 $\mu m$  x 355.6  $\mu m$   $\pm 25.4 \mu m$ 

#### INTERFACE MATERIALS:

#### **Glassivation:**

Type: Nitride Thickness: 4kÅ ±0.5kÅ

#### **Top Metallization:**

Type: Metal 1: AlCu (2%)/TiW Thickness: Metal 1: 8kÅ ±0.5kÅ Type: Metal 2: AlCu (2%) Thickness: Metal 2: 16kÅ ±0.8kÅ

#### Substrate:

UHF-1X Bonded Wafer, DI

#### **Backside Finish:**

Silicon

# Metallization Mask Layout

#### **ASSEMBLY RELATED INFORMATION:**

Substrate Potential:

Floating

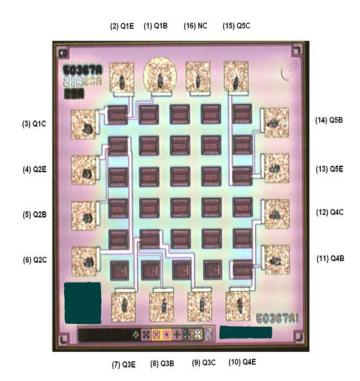
# ADDITIONAL INFORMATION:

Worst Case Current Density:  $3.04 \times 10^5 \text{A/cm}^2$ 

#### **Transistor Count:**

5

#### ISL73096RH.



All Intersil U.S. products are manufactured, assembled and tested utilizing ISO9000 quality systems. Intersil Corporation's quality certifications can be viewed at www.intersil.com/design/quality

Intersil products are sold by description only. Intersil Corporation reserves the right to make changes in circuit design, software and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by Intersil is believed to be accurate and reliable. However, no responsibility is assumed by Intersil or its subsidiaries for its use; nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Intersil or its subsidiaries.

For information regarding Intersil Corporation and its products, see www.intersil.com

