

STRUCTURE
TYPE
PRODUCT SERIES
FEATURES

Silicon Monolithic Integrated Circuit Step down DC/DC converter Controller for NOTE PC **BD9533EKN**

•Built in H³REG DC/DC controller

Switching Frequency Variable (f=200kHz~600kHz)

O ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

Parameter	Symbol	Limit	Unit
Input Voltage 1	VCC	7 *1*2	V
Input Voltage 2	VDD	7 *1*2	V
Input Voltage 3	VIN	30 *1*2	V
BOOT Voltage	BOOT	35 *1*2	V
BOOT-SW Voltage	BOOT-SW	7 *1*2	V
HG-SW Voltage	HG-SW	7 *1*2	V
LG Voltage	LG	VDD	V
REF Voltage	REF	VCC	V
Output Voltage	VOUT/Is+/Is-	VCC	V
ILIM/SCP/SS/FS/SLLM Voltage	ILIM/SCP/SS/FS/SLLM	VCC	V
VREG Voltage	VREG	VCC	V
EN Input Voltage	EN	7 *1	V
Power Dissipation 1	Pd1	0.5 *3	W
Power Dissipation 2	Pd2	0.75 *4	W
Power Dissipation 3	Pd3	1.75 *5	w
Power Dissipation 4	Pd4	2.00 *6	W
Operating Temperature Range	Topr	-10~+100	°C
Storage Temperature Range	Tstg	-55~+150	S
Maximum Junction Temperature	Tjmax	+150	°C

*1 Not to exceed Pd.
*2 Instantaneous surge voltage, back electromotive force and voltage under less than 10% duty cycle.
*3 Reduced by 4mW for each increase in Ta of 1°C over 25°C (when don't mounted on a heat radiation board)
*4 Reduced by 6mW for increase in Ta of 1°C over 25°C. (when mounted on a board 70.0mm × 70mm × 1.6mm Glass-epoxy PCB.)
*5 Reduced by 14mW for increase in Ta of 1°C over 25°C. (when mounted on a board 70.0mm × 70mm × 1.6mm Glass-epoxy PCB.)
*6 Reduced by 16mW for increase in Ta of 1°C over 25°C. (when mounted on a board 70.0mm × 70mm × 1.6mm Glass-epoxy PCB.)

O OPERATING CONDITIONS (Ta=25°C)

Parameter	Symbol	MIN	MAX	Unit
Input Voltage 1	VCC	4.5	5.5	V
Input Voltage 2	VDD	4.5	5.5	V
Input Voltage 3	VIN	4.5	28	V
BOOT Voltage	BOOT	4.5	33	V
SW Voltage	SW	-0.7	28	V
BOOT-SW Voltage	BOOT-SW	4.5	5.5	v
SLLM Input Voltage	SLLM	0	5.5	V
EN Input Voltage	EN	0	5.5	V
Output setting voltage	REF	0.7	2.0	V
Is Input Voltage	ls+/ls-	0.7	2.7	V
Minimum ON Time	Tonmin	-	200	nsec

★ This product is not designed for protection against radioactive rays.

Status of this document

The Japanese version of this document is the official specification.

This translated version is intended only as a reference, to aid in understanding the official version.

If there are any differences between the original and translated versions of this document, the official Japanese language version takes priority.

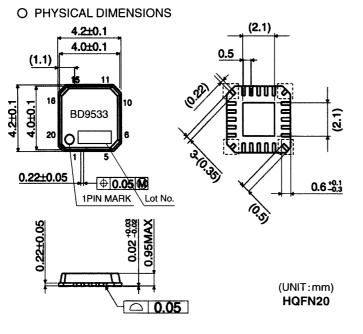
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OELECTRICAL CHARACTERISTICS

(unless otherwise noted, Ta=25°C VCC=5V,VDD=5V,EN/SLLM=5V,VIN=12V,REF=1.8V,R_{FS}=68k Ω)

(unless otherwise noted, Ia=25°C			Standard Value			
Parameter	Symbol	MIN	TYP	MAX	Unit	Condition
[Whole Device]						
VCC Bias Current	lcc	-	700	900	μA	
VIN Bias Current	lin	-	100	200	μA	
VCC Standby Current	lccstb	-	0	10	μA	EN=0V
VIN Standby Current	linstb	-	100	200	μA	EN=0V
EN Low Voltage	Enlow	GND	-	0.8	V	
EN HighVoltage	Enhigh	2.3	-	5.5	V	
EN Bias Current	len	-	7	10	μA	
VREG Voltage	Vreg	2.475	2.500	2.525	V	Ireg=100 μ A Ta=-10°C to 100°C
[Under Voltage Locked Out]						.
VCC threshold voltage	Vcc_UVLO	4.1	4.3	4.5	V	VCC:Sweep up
VCC hysteresis voltage	dVcc_UVLO	100	160	220	mV	VCC:Sweep down
VIN threshold voltage	Vin_UVLO	4.1	4.3	4.5	V	VIN:Sweep up
VIN hysteresis voltage	dVin_UVLO	100	160	220	mV	VIN:Sweep down
VREG threshold voltage	Vreg_UVLO	2.0	2.2	2.4	V	VREG:Sweep up
VREG hysteresis voltage	dVreg_UVLO	100	160	220	mV	VREG:Sweep down
[H ³ REG]						· · · · ·
ON Time	Ton	400	500	600	nsec	
MAX ON Time	Tonmax	1	3	-	μ sec	
MIN OFF Time	Toffmin	-	450	550	nsec	
[FET Driver]						
HG Higher side ON resistor	HGhon	-	3.0	6.0	Ω	
HG Lower side ON resistor	HGlon	-	2.0	4.0	Ω	
LG Higher side ON resistor	LGhon	-	2.0	4.0	Ω	
LG Lower side ON resistor	LGIon	-	0.5	1.0	Ω	
[SCP]		I				·· l
SCP Detect Voltage	Vscp	REF×	REF × 0.7	REF×	v	
SCP threshold voltage	Vscpth	1.2	1.25	1.3	v	
Charge current	Iscp	1.5	2	2.5	μA	
Standby voltage	Vscp_stb	-		50	mV	
[Soft start]						
Charge current	lss	1.5	2	2.5	μA	
Standby voltage	Vss_stb	1.0	-	50	 mV	
[Over Voltage Protection]	V33_310				1110	
OVP setting voltage	Vovp	REF×	REF × 1.2	REF×	V	
[Over Current Protection Block]	1 1010				·	
Current limit threshold1	llim1	43	50	57	mV	ILIM=0.5V Ta=-10°C to 100°C
Current limit threshold2	llim2	160	200	240	mV	ILIM=2.0V
[VOUT setting]						
VOUT offset voltage	Voutoff	REF-7m	REF	REF+7m	V	Ta=-10°C to 100°C
VOUT bias current	lvout	-100	0	100	nA	
REF bias current	Iref	-100	0	100	nA	
Is+ Input current	lls+	-100	0	1	μΑ	ls+=1.8V
Is- Input current	lls-	-1	0	1	<u>μ</u> Α	Is-=1.8V
[SLLM]	113-	L. "I	L		μ	10 ^{-1.0} V
Continuous mode threshold	Vthcon	VCC-0.5		VCC	v	
SLLM threshold	VthSLLM				V	
	VIISLLM	GND	-	0.5	<u>v</u>	l

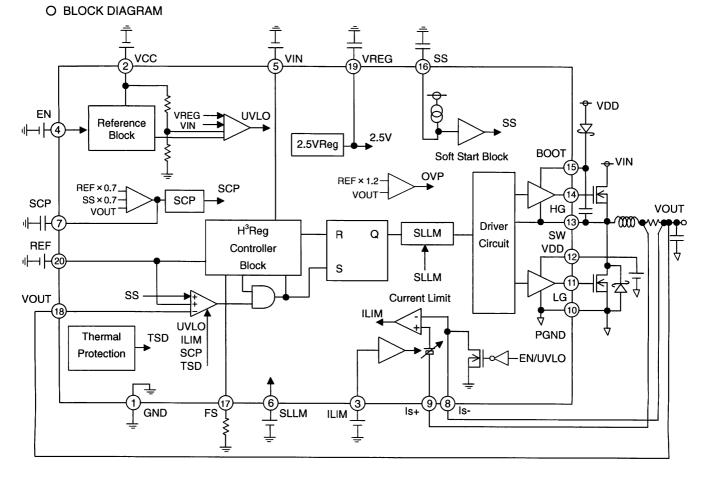
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* Mounting is not recommended to the dotted line part.

Pin Pin Name No. GND 1 2 VCC З ILIM 4 EN 5 VIN 6 SLLM 7 SCP 8 IS-IS+ 9 PGND 10 11 LG 12 VDD SW 13 14 HG BOOT 15 16 SS 17 FS 18 VOUT 19 VREG REF 20 FIN

Please short FIN to the 1PIN



O Pin Number Pin Name

3/4

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- O NOTE FOR USE
- (1) Absolute maximum rating

The device may be destroyed when applied voltage or operating temperature exceeds its absolute maximum rating. Because the source, such as short mode or open mode, cannot be identified if the device is destroyed, it is important to take physical safety measures (such as fusing) if a special mode in excess of absolute rating limits is to be implemented.

(2) Supply line

Since the motor's reverse electromotive force gives rise to the return of regenerative current, measures should be taken to establish a channel for the current, such as adding a capacitor between the power supply and GND. In determining the approach to take, make sure that no problems will be posed by the various characteristics involved, such as capacitance loss at low temperatures with an electrolytic capacitor.

(3) GND potential

Make sure the potential for the GND pin is always kept lower than the potentials of all other pins, regardless of the operating mode.

(4) Thermal design

Be sure to factor in allowable power dissipation (Pd) in actual operation, and to build sufficient margin into the thermal design to accommodate this power loss.

(5) Operation in strong magnetic fields

Use in strong electromagnetic fields may cause malfunctions. Exercise caution with respect to electromagnetic fields. (6) ASO

Set the parameters so that output Tr will not exceed the absolute maximum rating or ASO value when the IC is used.

(7) Thermal shutdown circuit

This IC is provided with a built-in thermal shutdown (TSD) circuit, which is activated when the chip temperature reaches the threshold value listed below. When TSD is on, the device goes to high impedance mode. Note that the TSD circuit is provided for the exclusive purpose shutting down the IC in the presence of extreme heat, and is not designed to protect the IC per se or guarantee performance when or after extreme heat conditions occur. Therefore, do not operate the IC with the expectation of continued use or subsequent operation once the TSD is activated.

TSD ON temperature [°C] (typ.)	Hysteresis temperature[°C] (typ.)
175	15

(8) Ground wiring pattern

When both a small-signal GND and high current GND are present, single-point grounding (at the set standard point) is recommended, in order to separate the small-signal and high current patterns, and to be sure the voltage change stemming from the wiring resistance and high current does not cause any voltage change in the small-signal GND. In the same way, care must be taken to avoid wiring pattern fluctuations in any connected external component GND.

(9) Heat sink (FIN)

Since the heat sink (FIN) is connected with the Sub, short it to the GND.

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