

Silicon PNP Power Transistors

BDT92

**DESCRIPTION**

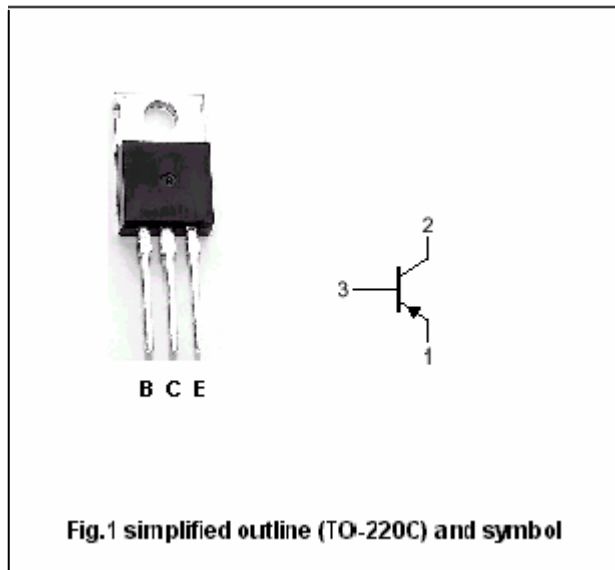
- With TO-220C package
- Complement to type BDT91

**APPLICATIONS**

- For use in general purpose power amplifier and switching applications

**PINNING**

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



**Absolute maximum ratings (Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-60	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	-60	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-5	V
I <sub>C</sub>	Collector current		-10	A
I <sub>B</sub>	Base current		-6	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> ≤25°C	90	W
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal resistance junction to case	1.39	°C/W

## Silicon PNP Power Transistors

## BDT92

## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CE0(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =-100mA; I <sub>B</sub> =0	-60			V
V <sub>CEsat-1</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-3 A; I <sub>B</sub> =-0.3 A			-1.1	V
V <sub>CEsat-2</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-10 A; I <sub>B</sub> =-3.3 A			-3.0	V
V <sub>BE</sub>	Base-emitter voltage	I <sub>C</sub> =-4A ; V <sub>CE</sub> =-4V			-1.6	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-60V; I <sub>E</sub> =0			-0.1	mA
I <sub>CEO</sub>	Collector cut-off current	V <sub>CE</sub> =-30V; I <sub>B</sub> =0			-1.0	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-5V; I <sub>C</sub> =0			-1.0	mA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =-4A ; V <sub>CE</sub> =-4V	20		200	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =-10A ; V <sub>CE</sub> =-4V	5			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-0.5A ; V <sub>CE</sub> =-10V	4			MHz

Silicon PNP Power Transistors

BDT92

PACKAGE OUTLINE

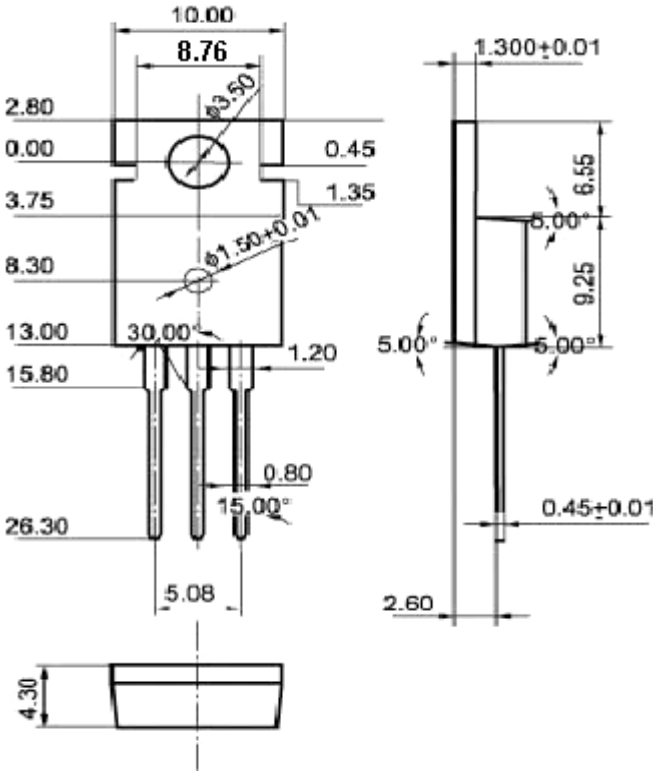


Fig.2 Outline dimensions (unindicated tolerance:±0.10 mm)