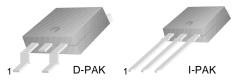
Rev. A2, June 2001



MJD30/30C

General Purpose Amplifier Low Speed Switching Applications

- Load Formed for Surface Mount Application (No Suffix)
- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular TIP30 and TIP30C



1.Base 2.Collector 3.Emitter

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage		
020	: MJD30	- 40	V
	: MJD30C	- 100	V
V _{CEO}	Collector-Emitter Voltage		
	: MJD30	- 40	V
	: MJD30C	- 100	V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current (DC)	- 1	А
I _{CP}	Collector Current (Pulse)	- 3	Α
I _B	Base Current	- 4	Α
P _C	Collector Dissipation (T _C =25°C)	15	W
	Collector Dissipation (Ta=25°C)	1.56	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage				
	: MJD30	$I_C = -30 \text{mA}, I_B = 0$	- 40		V
	: MJD30C		- 100		V
I _{CEO}	Collector Cut-off Current				
	: MJD30	$V_{CE} = -40V, I_{B} = 0$		- 50	μΑ
	: MJD30C	$V_{CE} = -60V, I_{B} = 0$		- 50	μΑ
I _{CES}	Collector Cut-off Current				
	: MJD30	$V_{CE} = -40V, V_{BE} = 0$		- 20	μΑ
	: MJD30C	$V_{CE} = 100V, V_{BE} = 0$		- 20	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{BE} = -5V, I_{C} = 0$		- 1	mA
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.2A$	40		
· -		$V_{CE} = -4V, I_{C} = -1A$	15	75	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = - 1A, I _B = - 125mA		- 0.7	V
V _{BE} (on)	* Base-Emitter ON Voltage	V _{CE} = - 4A, I _C = - 1A		- 1.3	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 10V, I _C = - 200mA	3		MHz

* Pulse Test: PW≤300ms, Duty Cycle≤2%

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 $I_C=10I_B$

Typical Characteristics

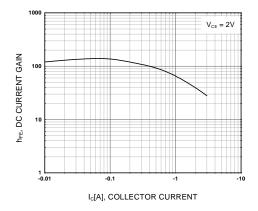


Figure 1. DC current Gain

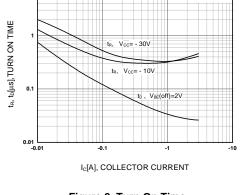


Figure 2. Turn On Time

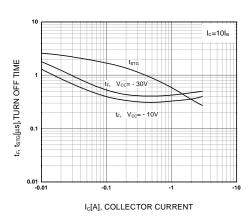


Figure 3. Turn Off Time

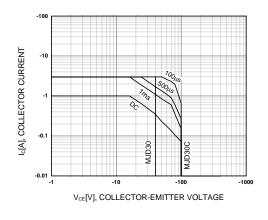


Figure 4. Safe Operating Area

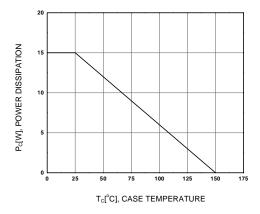
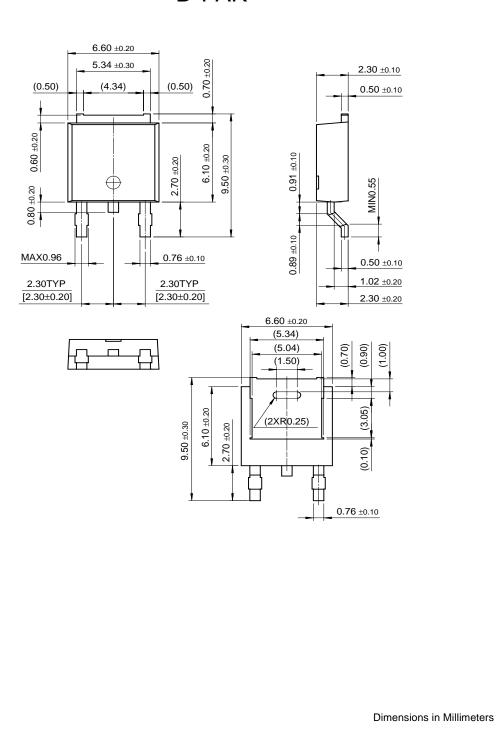


Figure 5. Power Derating

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Package Demensions

D-PAK



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