

## Silicon PNP Power Transistors

## 2SB554

## DESCRIPTION

- With TO-3 package
- Complement to type 2SD424
- High power dissipation

## APPLICATIONS

- For use in power amplifier applications

## PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

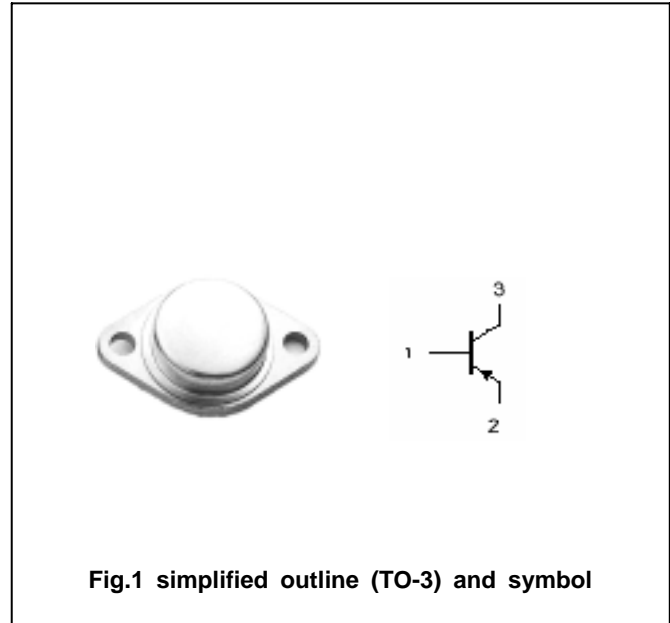


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings( $T_a =$ )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	-180	V
$V_{CEO}$	Collector-emitter voltage	Open base	-180	V
$V_{EBO}$	Emitter-base voltage	Open collector	-5	V
$I_C$	Collector current		-15	A
$I_B$	Base current		-4	A
$P_C$	Collector power dissipation	$T_C=25$	150	W
$T_j$	Junction temperature		150	
$T_{stg}$	Storage temperature		-55~200	

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## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-25mA ; I <sub>B</sub> =0	-180			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-10A; I <sub>B</sub> =-1A			-3.0	V
V <sub>BE</sub>	Base-emitter on voltage	I <sub>C</sub> =-2A ; V <sub>CE</sub> =-5V			-1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-90V; I <sub>E</sub> =0			-0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-5V; I <sub>C</sub> =0			-0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-2A ; V <sub>CE</sub> =-5V	40		140	
C <sub>OB</sub>	Output capacitance	I <sub>E</sub> =0 ; V <sub>CB</sub> =-10V; f=1.0MHz		450		pF
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-2A ; V <sub>CE</sub> =-5V		6		MHz

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PACKAGE OUTLINE



Fig.2 outline dimensions (unindicated tolerance:  $\pm 0.1\text{mm}$ )