

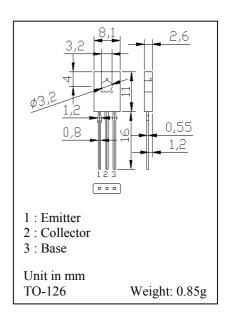
PNP SILICON POWER TRANSISTOR

...designed for output stage of 3 watts audio amplifier, voltage regulator, DC-DC converter and relay driver.

MAXIMUM RATINGS (Ta = 25 °C)

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Characteristic	Symbol	Value	Unit			
Collector Base Voltage	Vсво	-40	V			
Collector Emitter Voltage	VCEO	-30	V			
Emitter Base Voltage	VEBO	-5	V			
Collector Current (DC)	IC(DC)	-3	Α			
Collector Current (Pulse)	IC(Pulse)*	-7	Α			
Total Power Dissipation	Ptot		W			
Ta=25°C		1				
Tc=25°C		10				
Storage Temperature	Tstg	-55 ~ 150	°C			
Junction Temperature	Tj	150	°C			
*Dulas Test DM < 250s Duty Cycle < 20	/					

^{*}Pulse Test PW \leq 350 μ s, Duty Cycle \leq 2%

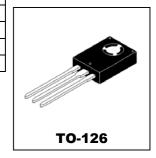


ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

Characteristic	Symbol	Test Condition Min. T		Тур.	Max.	Unit
Collector Cutoff Current	Ісво	VCB =-30V, IE =0	-	-	-1	μΑ
Emitter Cutoff Current	ІЕВО	VEB =-3.0V, IC =0	-	-	-1	μΑ
Collector Saturation Voltage	VCE(sat)	Ic =-2.0A, I _B =-0.2A**	-	-0.3	-0.5	V
Base Saturation Voltage	VBE(sat)	Ic =-2.0A, IB =-0.2A**	-	-1	-2	V
DC Current Gain	hFE1	VcE=-2.0V, Ic=-20mA**	30	220	-	-
DC Current Gain	hFE2	VCE =-2.0V, IC =-1.0A**	60	160	400	-
Gain Bandwidth Product	f⊤	VCE =-50V, IC =-0.1A	-	80	-	MHz
Output Capacitance	Cob	VcB =-10V, IE =0, f=1.0MHz	-	55	-	pF

^{**}Pulse Test: PW $\leq 350 \mu s$, Duty Cycle $\leq 2\%$

PNP SILICON POWER TRANSISTOR



Classification of hFE(2)

Class	R	Q	Р	E
hFE(2)	60 to 120	100 to 200	160 to 320	200 to 400

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