

TRIPLE DIFFUSED PLANER TYPE
HIGH POWER DARLINGTON
HIGH VOLTAGE, SWITCHING

■ **Features**

- High D.C. current gain
- Low saturation voltage
- Excellent safe operating area
- High reliability

■ **Applications**

- Electronic ignitor
- Relay & solenoid drivers
- Switching regulators
- Motor controls

■ **Maximum ratings and characteristics**

- **Absolute maximum ratings (Tc=25°C unless otherwise specified)**

Item	Symbol	Ratings	Unit
Collector-Base voltage	V _{CB0}	400	V
Collector-Emitter voltage	V _{CE0}	400	V
Collector-Emitter voltage	V _{CE0(SUS)}	350	V
Emitter-Base voltage	V _{EB0}	15	V
Collector current	I _c	6	A
Base current	I _b	0.3	A
Collector power dissipation	P _c	40	W
Operating junction temperature	T _j	+150	°C
Storage temperature	T _{stg}	-45 to +150	°C

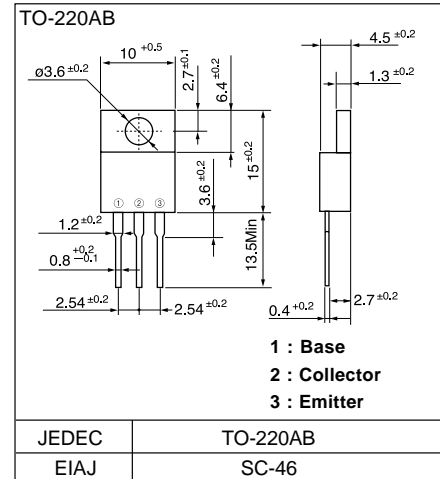
- **Electrical characteristics (Tc =25°C unless otherwise specified)**

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector-Base voltage	V _{CB0}	I _{cB0} = 0.1mA	400			V
Collector-Emitter voltage	V _{CE0}	I _{cE0} = 0.1mA	400			V
Collector-Emitter voltage	V _{CE0(SUS)}	I _c = 1A	350			V
Emitter-Base voltage	V _{EB0}	I _{EB0} = 100mA	15			V
Collector-Base leakage current	I _{cB0}	V _{CB0} = 400V			0.1	mA
Emitter-Base leakage current	I _{EB0}	V _{EB0} = 15V			100	mA
D.C. current gain	h _{FE}	I _c = 4A, V _{CE} = 1.5V	400			
Collector-Emitter saturation voltage	V _{CE(Sat)}	I _c = 4A, I _b = 10mA			1.5	V
Base-Emitter saturation voltage	V _{BE(Sat)}				2.0	V
*1 Switching time	t _{on}	I _c = 4A, I _{b1} = 40mA			1.0	μs
	t _{stg}	I _{b2} = -40mA, R _L = 10 ohm			12.0	μs
	t _r	P _w = 20μs Duty=<2%			6.0	μs

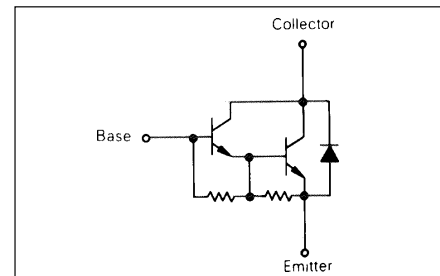
● **Thermal characteristics**

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R _{th(j-c)}	Junction to case			3.0	°C/W

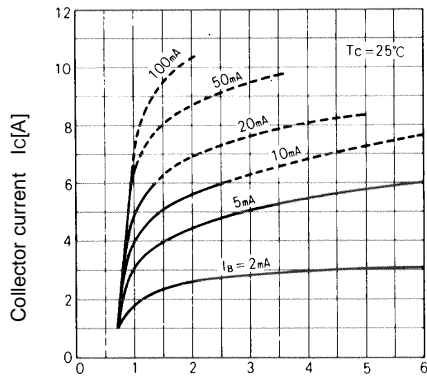
■ **Outline Drawings**



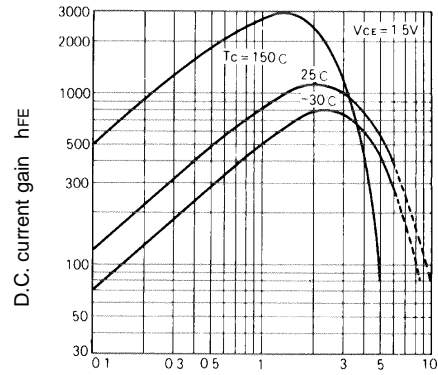
■ **Equivalent Circuit Schematic**



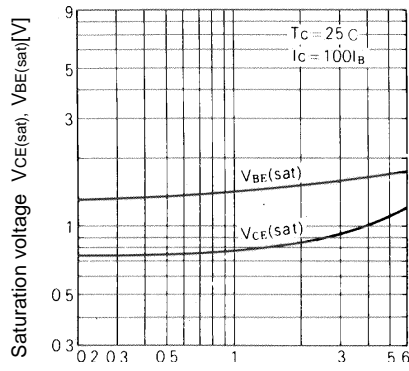
Characteristics



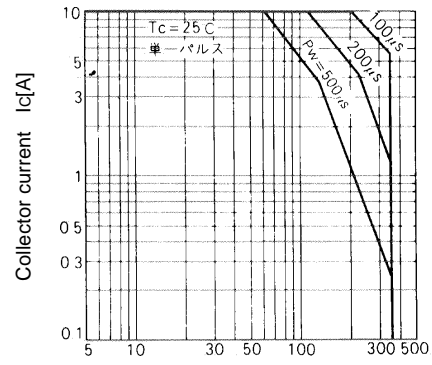
Collector Output Characteristics



DC Current Gain



Base and Collector Saturation Voltage



Safe Operating Area

*1 Switching Time Test Circuit

