

DUAL OPERATIONAL AMPLIFIER

GENERAL DESCRIPTION

The NJM4565 integrated circuit is a high-gain, wide-bandwidth, dual low noise operational amplifier capable of driving 20V peak-to-peak into 400 Ω load. The NJM4565 is good characteristics compared to the NJM4560.







NJM4565M

FEATURESOperating Voltage

- Wide Gain Bandwidth Product
- Slew Rate
- Package Outline
- Bipolar Technology
- (±4V~±18V) (4MHz typ.) (4V/ µs typ.) DIP8, DMP8, SSOP8, SIP8

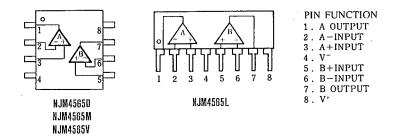
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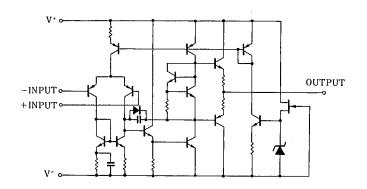


NJM4565V

PIN CONFIGURATION



■ EQUIVALENT CIRCUIT (1/2 Shown)



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ABSOLUTE MAXIMUM RATINGS			(Ta=25℃)	
PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V*/V-	±18	V	
Differential Input Voltage	Vid	±30	V	
Input Voltage	V _{IC}	±15 (note)	v	
Power Dissipation		(DIP8) 500	mW	
		(DMP8) 300	mW	
	PD	(SSOP8) 250	mW	
		(SIP8) 800	mW	
Operating Temperature Range	Topr	-20~+75	C	
Storage Temperature Range	Tsig	-40~+125	C	

(note) For supply voltage less than $\pm 15V$, the absolute maximum input voltage is equal to the supply voltage.

ELECTRICAL CHARACTERISTICS

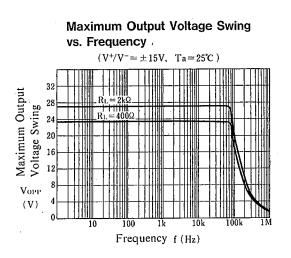
 $(Ta=25^{\circ}C, V^{*}/V^{-}=\pm 15V)$

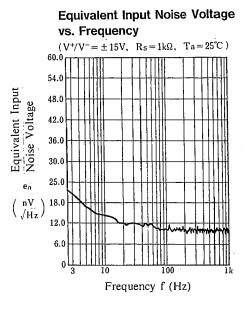
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V ₁₀	$R_{s} \leq 10k\Omega$	_	0.5	3.0	mV
Input Offset Current	Lio		—	2	50	nA
Input Bias Current	IB		_	50	200	пA
Input Resistance	RIN		0.3	5	-	MΩ
Large Signal Voltage Gain	Av	$R_{L} \ge 2k\Omega, V_{O} = \pm 10V$	86	100		dBi
Maximum Output Voltage Swing 1	V _{OM1}	$R_{L} \ge 2k\Omega$	±12	±14	-	v
Maximum Output Voltage Swing 2	V _{OM2}	$I_{O}=25mA$	±10	±11.5		v
Input Common Mode Voltage Range	V _{ICM}		±12	±14	-	v
Common Mode Rejection Ratio	CMR	$R_{s} \leq 10 k\Omega$	70	90	_	dB
Supply Voltage Rejection Ratio	SVR	R _s ≦10kΩ	76.5	90	-	dB
Operating Current	1 _{CC}			4.5	7	mA
Slew Rate	SR		-	4	-	V/µs
Gain Bandwidth Product	GB		-	10	-	MHz
Equivalent Input Noise Voltage	V _{NI}	RIAA, $R_s=2.2k\Omega$, 30kHz LPF	-	1.2	-	μVrn

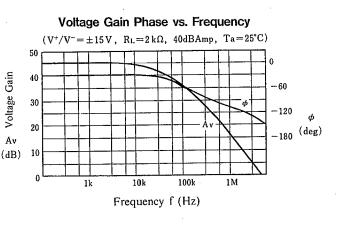
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TYPICAL CHARACTERISTICS

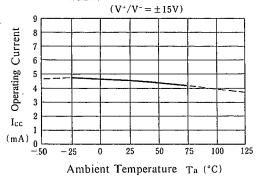




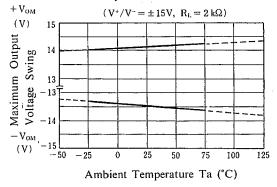




Load Resistance R_L (k Ω)



Maximum Output Voltage Swing vs. Temperature



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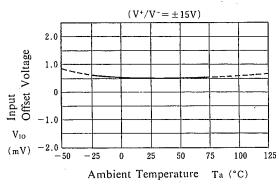
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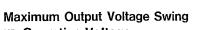


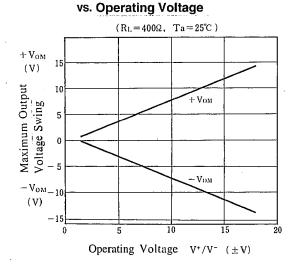
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TYPICAL CHARACTERISTICS s 📰



Input Offset Voltage vs. Temperature





 $(V^{+}/V^{-} = \pm 15V)$ 90 80 Input Bias Current: 70 60 50 40 30 20 $I_{\rm B}$ 10 (nA) 0 - 50

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Ambient Temperature Ta (°C)

50

75

15

20

100

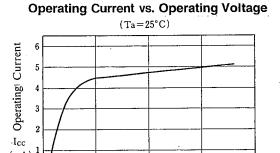
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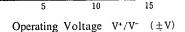
-25

(mA)

0

0





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Input Bias Current vs. Temperature

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MEMO

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