

DIOTEC ELECTRONICS CORP.

18020 Hobart Blvd., Unit B Gardena, CA 90248 U.S.A

Tel.: (310) 767-1052 Fax: (310) 767-7958

2 AMP SILICON BRIDGE RECTIFIERS

FEATURES

- PRV Ratings from 50 to 1000 Volts
- Surge overload rating to 60 Amps peak
- Reliable low cost molded plastic construction
- Ideal for printed circuit board applications

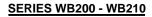
UL RECOGNIZED - FILE #E141956

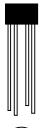
MECHANICAL DATA

- Case: Molded plastic, U/L Flammability Rating 94V-0
- Terminals: Round silver plated pins
- Soldering: Per MIL-STD 202 Method 208 guaranteed
- Polarity: Marked on case
- Mounting Position: Any
- Weight: 0.05 Ounces (1.3 Grams)

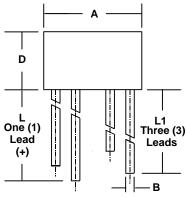
MECHANICAL SPECIFICATION

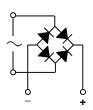
ACTUAL SIZE OF WB PACKAGE

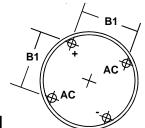












SYM	MILLIN	IETERS	INCHES			
	MIN	MAX	MIN	MAX		
Α	8.6	8.89	0.340	0.350		
В	0.76	0.81	0.030	0.032		
B1	4.6	5.6	0.180	0.220		
D	5.1	5.6	0.200	0.220		
L	27.94	n/a	1.10	n/a		
L1	25.4	n/a	1.0	n/a		

MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive loads, derate current by 20%.

PARAMETER (TEST CONDITIONS)		RATINGS						UNITS	
Series Number		WB 200	WB 201	WB 202	WB 204	WB 206	WB 208	WB 210	
Maximum DC Blocking Voltage		50	100	200	400	600	800	1000	
Maximum RMS Voltage		35	70	140	280	420	560	700	VOLTS
Maximum Peak Recurrent Reverse Voltage	VRRM	50	100	200	400	600	800	1000	
Average Forward Rectified Current @ TA = 25° C		2						AMPS	
Peak Forward Surge Current. Single 60Hz Half-Sine Wave Superimposed on Rated Load (JEDEC Method). TJ = 150° C		60							
Maximum Forward Voltage (Per Diode) at 2 Amp DC		1.1					VOLTS		
Maximum Average DC Reverse Current @ TA = 25° C At Rated DC Blocking Voltage @ TA = 100° C		10 (Typical < 0.5μA) 1						μ Α m A	
Typical Thermal Resistance Junction to Ambient (Note 1) Junction to Lead (Note 1)		40 15						°C/W	
Operating and Storage Temperature Range		-55 to +150					°C		

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RATING & CHARACTERISTIC CURVES FOR SERIES WB200 - WB210

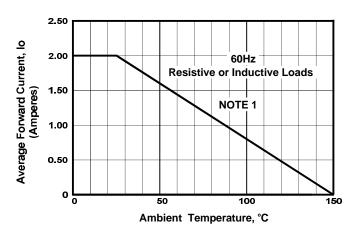


FIGURE 1. FORWARD CURRENT DERATING CURVE

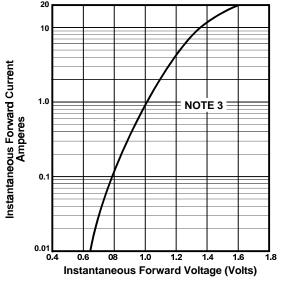


FIGURE 3. TYPICAL FORWARD CHARACTERISTIC PER DIODE

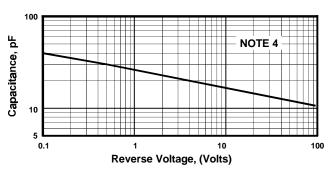


FIGURE 5. TYPICAL JUNCTION CAPACITANCE PER DIODE

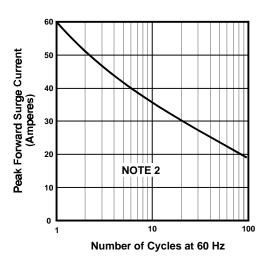
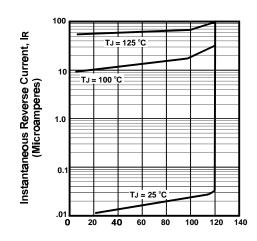


FIGURE 2. MAXIMUM NON-REPETITIVE SURGE CURRENT



Percent of Rated Peak Reverse Voltage
FIGURE 4. TYPICAL REVERSE CHARACTERISTICS

NOTES

- (1) Bridge Mounted on PC Board With 0.22" Sq. (5.5mm Sq.) Copper Pads And Bridge Lead Length of 0.375" (9.5mm)
- (2) $T_J = 150^{\circ} C$
- (3) T_J = 25°C; Pulse Width = 300 μ Sec, 1% Duty Cycle
- (4) $T_J = 25^{\circ} C$; f = 1 MHz; Vsig = 50mVp-p

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