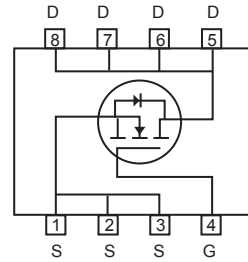
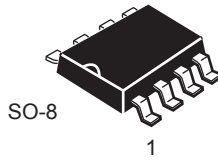


N-Channel Enhancement Mode Field Effect Transistor

FEATURES

- 30V, 12A, $R_{DS(ON)} = 10.5m\Omega @ V_{GS} = 10V$.
 $R_{DS(ON)} = 15.0m\Omega @ V_{GS} = 4.5V$.
- Super high dense cell design for extremely low $R_{DS(ON)}$.
- High power and current handling capability.
- Lead free product is acquired.
- Surface mount Package.



ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Limit	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	12	A
Drain Current-Pulsed ^a	I_{DM}	50	A
Maximum Power Dissipation	P_D	2.5	W
Operating and Store Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Ambient ^b	$R_{\theta JA}$	50	$^\circ C/W$



CEM3082

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Typ	Max	Units	
Off Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	30			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	μA	
Gate Body Leakage Current, Forward	I_{GSSF}	$V_{GS} = 20V, V_{DS} = 0V$			100	nA	
Gate Body Leakage Current, Reverse	I_{GSSR}	$V_{GS} = -20V, V_{DS} = 0V$			-100	nA	
On Characteristics ^c							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}, I_D = 250\mu A$	1		3	V	
Static Drain-Source	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 12A$		8.5	10.5	m Ω	
On-Resistance		$V_{GS} = 4.5V, I_D = 10A$		11	15	m Ω	
Forward Transconductance	g_{FS}	$V_{DS} = 5V, I_D = 10A$		18		S	
Dynamic Characteristics ^d							
Input Capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V,$ $f = 1.0\text{ MHz}$		2115		pF	
Output Capacitance	C_{oss}				410		pF
Reverse Transfer Capacitance	C_{rss}				265		pF
Switching Characteristics ^d							
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15V, I_D = 1A, \square$ $V_{GS} = 10V, R_{GEN} = 6\Omega$		15	30	ns	
Turn-On Rise Time	t_r			9	18	ns	
Turn-Off Delay Time	$t_{d(off)}$			45	90	ns	
Turn-Off Fall Time	t_f			15	30	ns	
Total Gate Charge	Q_g	$V_{DS} = 15V, I_D = 12A,$ $V_{GS} = 5V$		19.6	26	nC	
Gate-Source Charge	Q_{gs}			6.5		nC	
Gate-Drain Charge	Q_{gd}			6.0		nC	
Drain-Source Diode Characteristics and Maximum Ratings							
Drain-Source Diode Forward Current ^b	I_S				4.5	A	
Drain-Source Diode Forward Voltage ^c	V_{SD}	$V_{GS} = 0V, I_S = 4.5A$			1.0	V	
Notes : <input type="checkbox"/> a.Repetitive Rating : Pulse width limited by maximum junction temperature. <input type="checkbox"/> b.Surface Mounted on FR4 Board, $t \leq 10\text{ sec.}$ <input type="checkbox"/> c.Pulse Test : Pulse Width $\leq 300\mu s,$ Duty Cycle $\leq 2\%.$ <input type="checkbox"/> d.Guaranteed by design, not subject to production testing. <input type="checkbox"/> <input type="checkbox"/>							



CEM3082

5

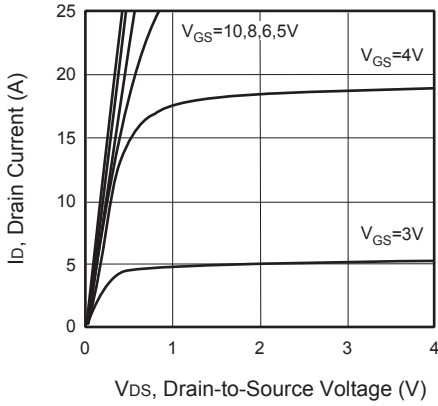


Figure 1. Output Characteristics

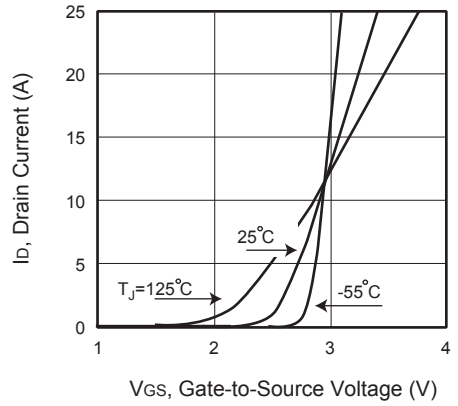


Figure 2. Transfer Characteristics

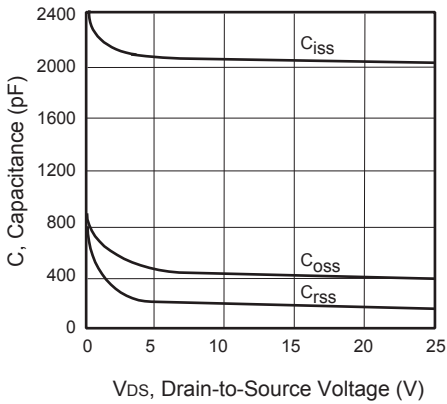


Figure 3. Capacitance

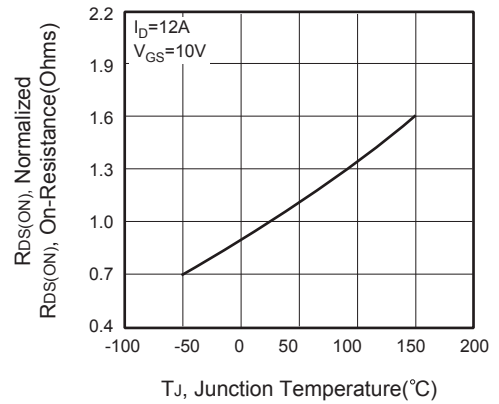


Figure 4. On-Resistance Variation with Temperature

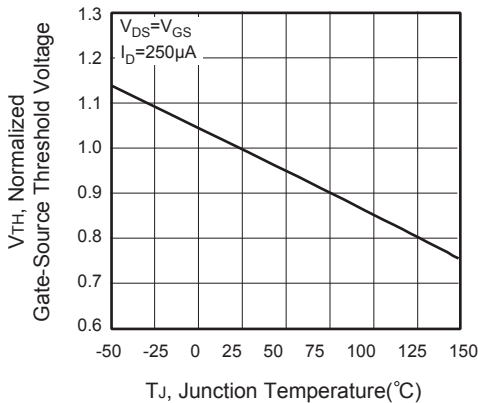


Figure 5. Gate Threshold Variation with Temperature

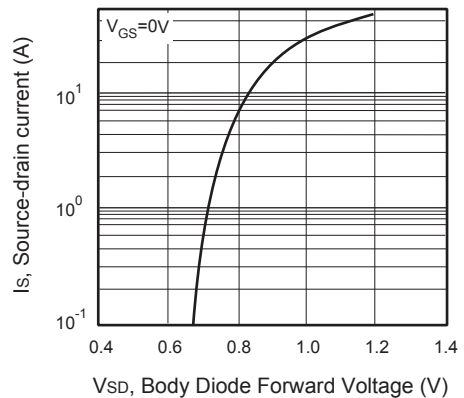


Figure 6. Body Diode Forward Voltage Variation with Source Current

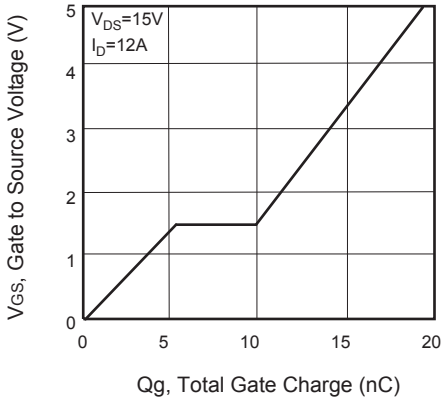


Figure 7. Gate Charge

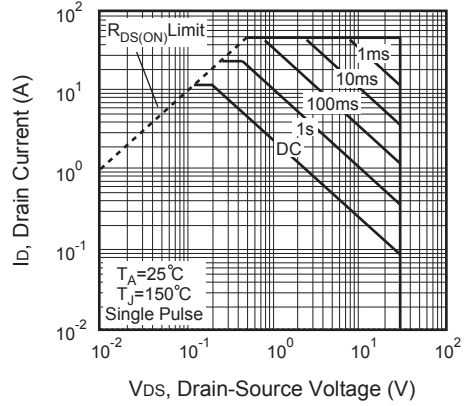


Figure 8. Maximum Safe Operating Area



Figure 9. Switching Test Circuit

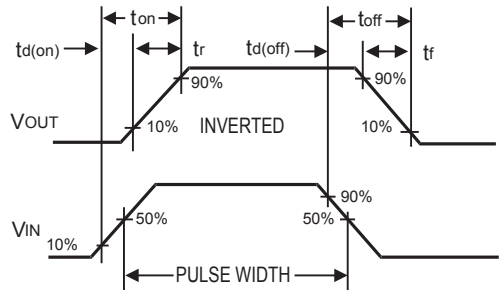


Figure 10. Switching Waveforms

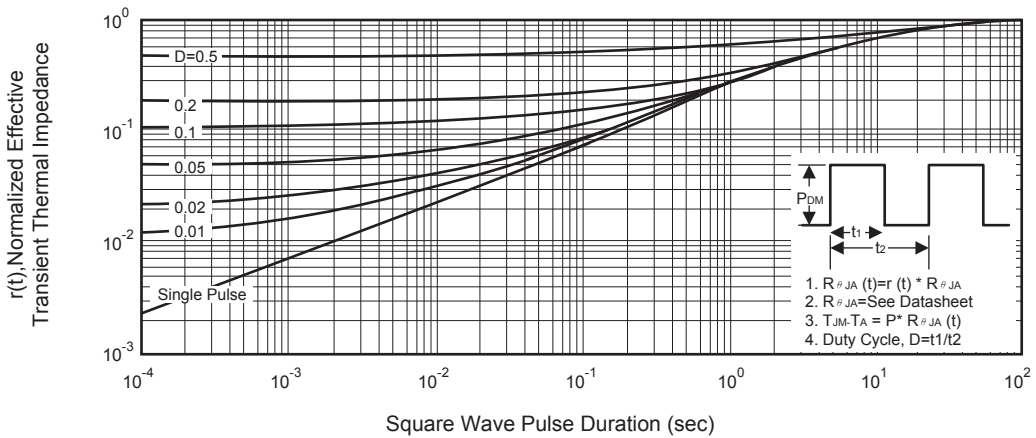


Figure 11. Normalized Thermal Transient Impedance Curve