

CD54AC04/3A CD54ACT04/3A

COMPLETE DATA SHEET **COMING SOON!**

June 1997

Hex Inverters, Active Outputs

Description

The CD54AC04/3A and CD54ACT04/3A are hex inverters that utilize the Harris Advanced CMOS Logic technology. The CD54AC04/3A and CD54ACT04/3A have active outputs.

The CD54AC04/3A and CD54ACT04/3A are supplied in 14 lead dual-in-line ceramic packages (F suffix).

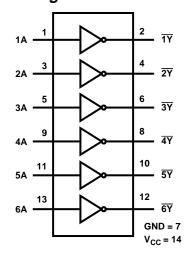
ACT INPUT LOAD TABLE

INPUT	UNIT LOAD (NOTE 1)
nA	0.18

NOTE:

1. Unit load is ΔI_{CC} limit specified in DC Electrical Specifications Table, e.g., 2.4mA Max at +25°C.

Functional Diagram



Absolute Maximum Ratings

DC Supply Voltage, V _{CC} 0.5V to +6V	Power Dissipa
DC Input Diode Current, I _{IK}	$T_{A} = -55^{\circ}C$
For $V_I < -0.5V$ or $V_I > V_{CC} + 0.5V$ ±20mA	$T_A = +100^{\circ}$
DC Output Diode Current, I _{OK}	
For $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$	Operating Ten
DC Output Source or Sink Current, Per Output Pin, IO	Package Ty
For $V_O > -0.5V$ or $V_O < V_{CC} + 0.5V$	Storage Temp
DC V _{CC} or GND Current, I _{CC} or I _{GND}	Lead Tempera
For Up to 4 Outputs Per Device, Add ±25mA For Each	At Distance
Additional Output±100mA	From Case
	Unit Inserte

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Recommended Operating Conditions

Supply Voltage Range, V _{CC}
Unless Otherwise Specified, All Voltages Referenced to GND
T _A = Full Package Temperature Range
CD54AC Types
CD54ACT Types
DC Input or Output Voltage, V _I , V _O 0V to V _{CC}

Operating Temperature, T _A	55°C to +125°C
Input Rise and Fall Slew Rate, dt/dv	
at 1.5V to 3V (AC Types)	. 0ns/V to 50ns/V
at 3.6V to 5.5V (AC Types)	. 0ns/V to 20ns/V
at 4.5V to 5.5V (AC Types)	. Ons/V to 10ns/V