

**IECQ-CECC**

**QC 88000-C003**

**COMPONENT**

**ISSUE 1**

**SPECIFICATION**

**March 2007**

**Component Specification  
For  
Ceramic Hermetically Sealed  
Low Input Current  
High Gain Optocouplers**



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## **FOREWORD**

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This Component Specification is based upon the requirements of IEC Publication QC 001002-2, and has been prepared by:

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## **AMENDMENT RECORD**

No previous issue.

## **REQUIREMENTS**

The requirements for IECQ-CECC Component Specifications as detailed in QC 001002-2 Amendment 1 clause 5.4 are satisfied by the following data sheet.

It should note that IECQ-CECC are not responsible for manufacturers declarations made in data sheets which fall outside the limits of approved detailed in IECQ-CECC certificates.

This Component Specification is intended for use with applicable IECQ-CECC Assessment Specifications. Eg: QC 88000-A001

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## Ceramic Hermetically Sealed Low Input Current High Gain Optocouplers

- 4N49
- 6N140A
- CD750
- CD5731
- CH370
- CS5700
- CS700
- CSM141/A
- CSM160/161/162
- CSM1700
- CSM6730

### Features

- Release to IECQ-CECC
- Hermetically Sealed
- High Density Packaging
- 1500V DC withstand Test Voltage
- Low Input Requirements: 0.5mA
- High Current Transfer Ratio: 1000% Typical

### Applications

- Military, high reliability system
- Medical instruments
- Mos, Cmos Applications
- Logic Interfacing
- Data Transmission
- Transportation

### Description

These devices are single, dual and quad, hermetically sealed optocouplers. The products are capable of operation and storage over the full military and space temperature range. Each channel is composed of a light emitting diode, optically coupled to an integrated high gain photon detector. The high gain output features an open collector output providing both lower saturation voltage and higher signalling speed. Package styles for these devices include 8 pin, 16 pin, 16 pin flat pack, leadless 6 pin and hybrid 5 pin, with surface mount, butt cut and gull wing options available.

The same electrical die, assembly processes and materials are used for each channel of each device shown below. Therefore absolute maximum ratings, recommended operating conditions, electrical specifications and performance characteristics are identical for all units. Any exceptions, due to packaging variations and limitations, are as noted.

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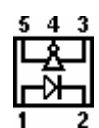
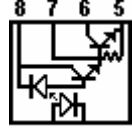
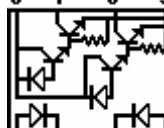

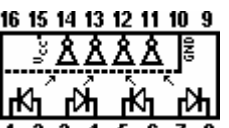
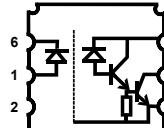
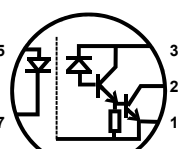
## Selection Guide Package Styles and Configuration Options

Package	16 pin DIP	8 pin DIP	8 pin DIP	16 pin Flat Pack	6 Pad LCC	6 pin Metal Can TO-5
Lead Style						
Channels	4	1	2	4	1	1
Common Channel Wiring						

### Isocom Part Numbers and Options

Commercial	6N140A	CS700	CD750 /CD5731	CSM160 /161/162	CSM1700 CSM141/A	4N49
Defense Level	6N140A/L2	CS700/L2	CD750 /CD5731/L2	CSM160 /161/162 /L2	CSM1700 CSM141/A	4N49/L2
Space Level	6N140A/L2S	CS700/L2S	CD750 /CD5731/L2S	CSM160 /161/162/L2S	CSM1700 CSM141/A	4N49/L2S
Standard Gold Plate Finish	Gold Plate	Gold Plate	Gold Plate	Gold Plate	Gold Plate	Gold Plate
Solder Dipped	Option 20	Option 20	Option 20			
Butt Cut/Gold Plate	Option 10	Option 10	Option 10			
Gull Wing/Soldered	Option 30	Option 30	Option 30			
Crew Cut/Gold Plate	Option 60	Option 60	Option 60			

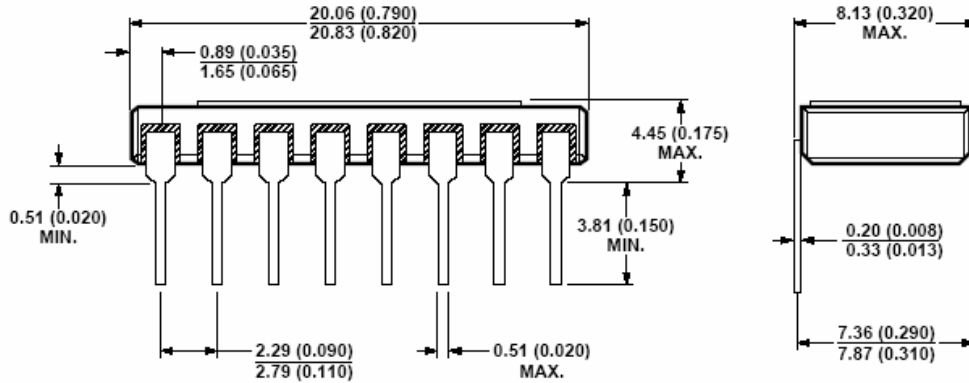
## Functional Diagrams

CH370	CS700	CD750 CD5731	6N140A	CSM160 CSM161/162	CSM141/A CSM1700
	8 pin DIP	8 pin DIP	16 pin DIP	16 pin Flat Pack	6 Pad LCC
1 Channel	1 Channel	2 Channel	4 Channel	4 Channel	1 Channel
					
<b>4N49</b>					
TO-5					
1 Channel					
					

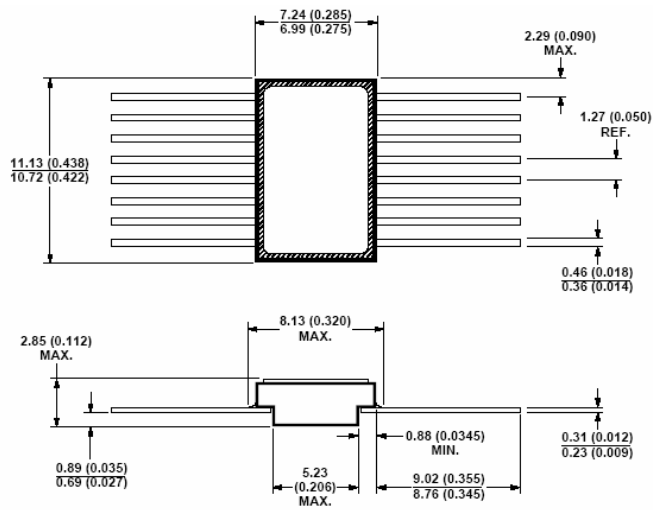
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## Outline Drawings

### 16 pin DIP, 4 Channel

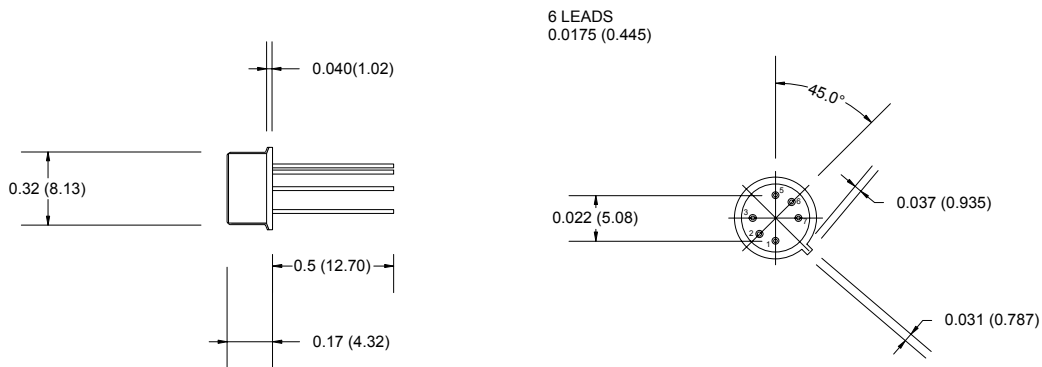


### 16 pin Flat Pack, 4 Channel



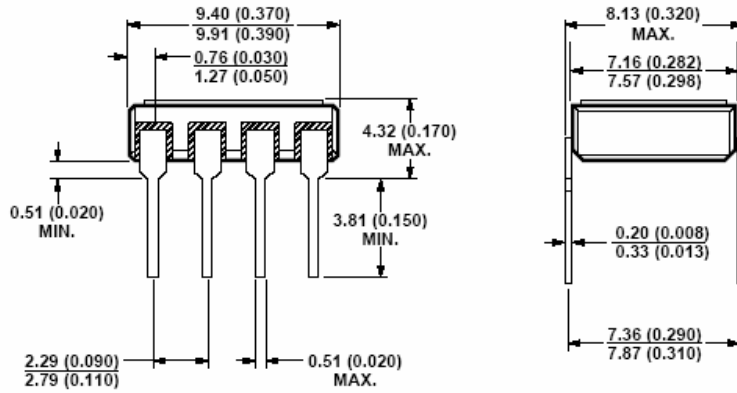
NOTE: DIMENSIONS IN MILLIMETERS

### 6 pin TO-5, 1 Channel

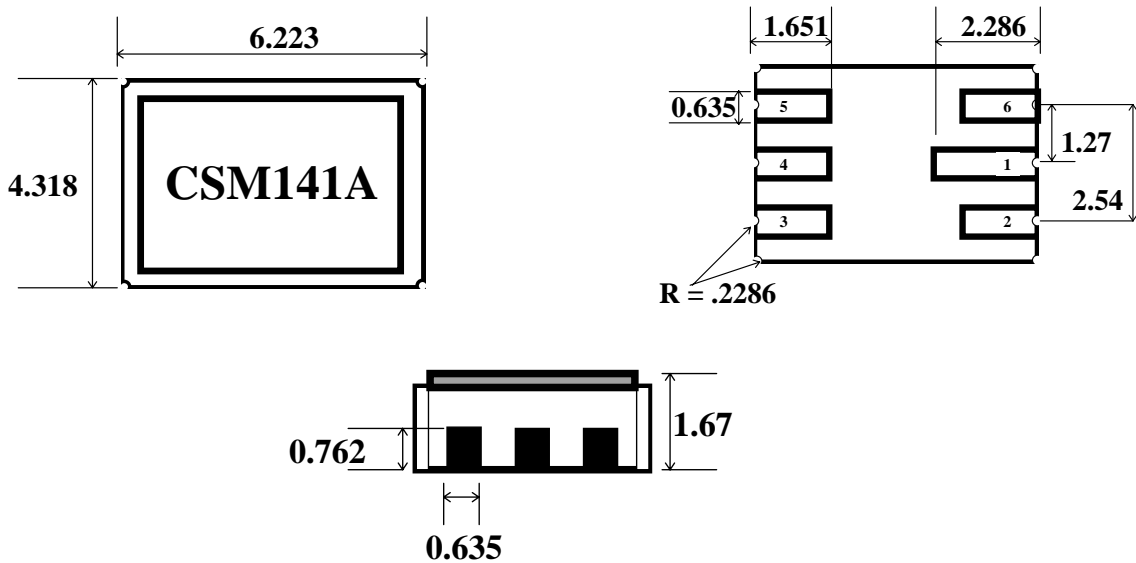


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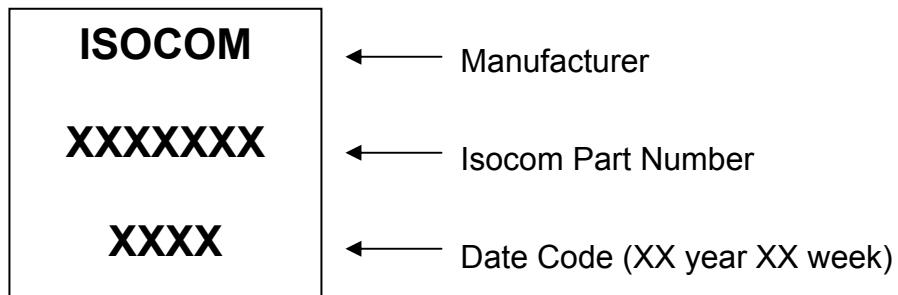
## 8 pin DIP 1 and 2 Channel



## 6 Terminal LCC Surface Mount, 1 Channel

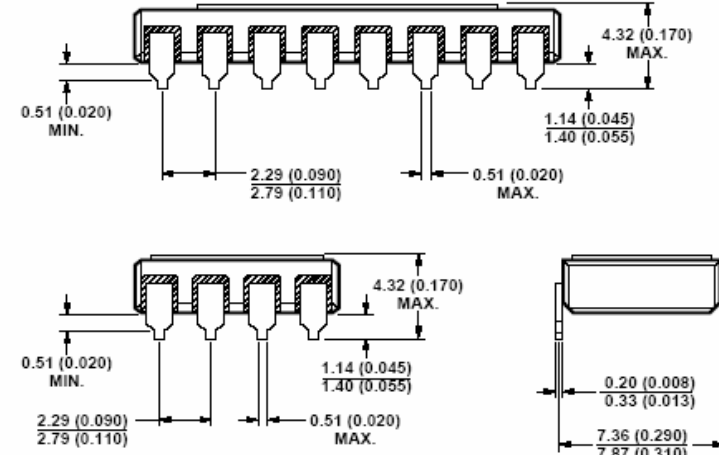
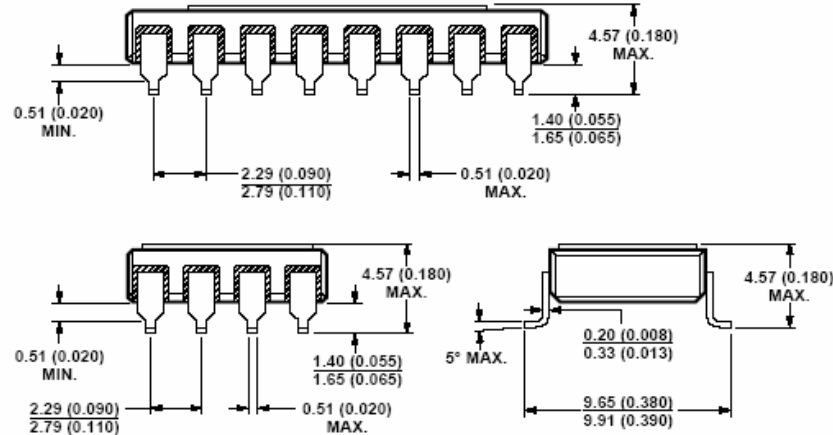
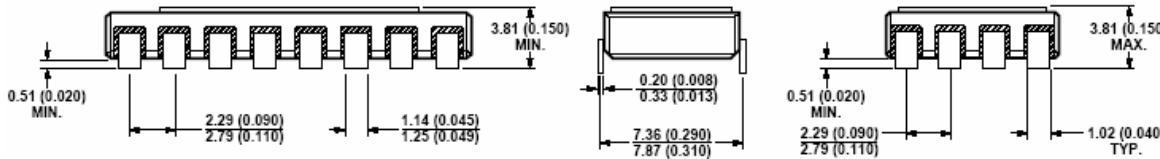


## Device Marking



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## Hermetic Optocoupler Options

Option	Description
10	<p>Surface mountable hermetic optocoupler with leads trimmed for butt joint assembly. This option is available on commercial hi-rel product in 8 and 16 pin DIP</p> 
20	
30	<p>Surface mountable hermetic optocoupler with leads cut and bent for gull wing assembly. This option is available on commercial and hi-rel product in 8 and 16 pin DIP.</p> 
60	<p>Surface mountable hermetic optocoupler with leads trimmed for butt joint assembly. This option is available on commercial hi-rel product in 8 and 16 pin DIP</p> 

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## Absolute Maximum Ratings

Storage Temperature	-65°C to +150°C
Operating Temperature	-55°C to +125°C
Lead Soldering Temperature	260C for 10S, 1.6mm below seating plane where appropriate

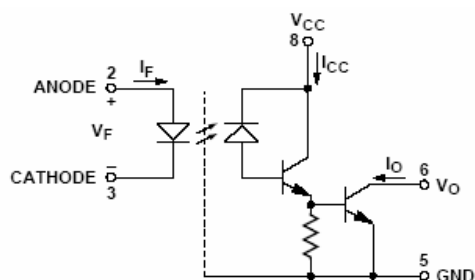
## Input Diode

Peak Forward Current	20mA	≤ 1 mS duration, 500pps
Average Forward Current	10mA	(See note 3)
Reverse Voltage	5V	
Power Dissipation	35mW	

## Output Detector

Supply Voltage	-0.5 to 20V	V <sub>CC</sub> (See note 1)
Current	40mA	I <sub>O</sub>
Collector Power Dissipation	50mW	(See note 2)
Voltage	-0.5 to 20V	V <sub>O</sub> (See note 1)

## Single Channel Schematic



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## Electrical Characteristics

$T_A = -55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  U.O.S.

All typical values at  $V_{CC} = 5\text{V}$ ,  $T_A = 25^{\circ}\text{C}$  (each channel where appropriate).

Parameter	Symbol	Test Conditions	Device	Min	Type	Max	Units
High Level Output Current (See notes 4 & 6)	$I_{OH}$ $I_{OHX}$	$I_F = 2\mu\text{A}$ , $V_O = V_{CC} = 5.5\text{V}$		-	0.001	250	$\mu\text{A}$
Lower Level Output Voltage (See note 4)	$V_{OL}$	$I_F = 0.5\text{mA}$ , $I_{OL} = 1.5\text{mA}$ , $V_{CC} = 4.5\text{V}$		-	0.1	0.4	V
		$I_F = 5\text{mA}$ , $I_{OL} = 10\text{mA}$ , $V_{CC} = 4.5\text{V}$		-	0.15	0.4	
High Level Supply Current	$I_{CCH}$	$V_{CC} = 18\text{V}$ , $I_{F1} = I_{F2} = I_{F3} = I_{F4} = 0$		-	0.1	40	$\mu\text{A}$
		$V_{CC} = 5.5$ , $I_{F1} = I_{F2} = I_{F3} = I_{F4} = 0$	Csm160/1			60	
Low Level Supply Current (See note 4)	$I_{CCL}$	$V_{CC} = 18\text{V}$ , $I_{F1} = I_{F2} = I_{F3} = I_{F4} = 1.6\text{mA}$		-	1.4	4	mA
		$V_{CC} = 5.5\text{V}$ , $I_{F1} = I_{F2} = I_{F3} = I_{F4} = 4\text{mA}$	CSM160/1			8	
Input-Output Insulation Leakage Current (See notes 7 & 13)	$I_{I-O}$	$RH = 45\%$ , $T_A = 25^{\circ}\text{C}$ , $t = 5\text{S}$ $V_{IO} = 1500\text{Vdc}$		-	-	1.0	$\mu\text{A}$
Input Forward Voltage (See note 4)	$V_F$	$I_F = 1.6\text{mA}$ , $T_A = 25^{\circ}\text{C}$		-	1.45	1.9	V
		$I_F = 4.0\text{mA}$	CSM160/1				
Input Reverse Breakdown Voltage (See note 4)	$B_{VR}$	$I_R = 10\mu\text{A}$ , $T_A = 25^{\circ}\text{C}$		5	-	-	V
Propagation Delay Time to Logic High Output (See note 4)	$t_{PLH}$	$R_L = 4.7\text{K}\Omega$ , $V_{CC} = 5\text{V}$ , $I_F = 0.5\text{A}$ , $T_A = 25^{\circ}\text{C}$	6N140 CS5700 CSM6730 CH370 CD750 CSM1700	-	8	60	$\mu\text{S}$
			CSM160/1			100	
		$R_L = 680\Omega$ , $V_{CC} = 5\text{V}$ , $I_F = 5\text{mA}$ , $T_A = 25^{\circ}\text{C}$	6N140	-	8	20	
			CS5700 CSM6730			30	
		CH390 CD750 CSM1700			60		

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## Electrical Characteristics Continued

Propagation Delay Time to Logic Low Output  (See note 4)	t <sub>PHL</sub>	R <sub>L</sub> = 4.7KΩ, V <sub>CC</sub> = 5V, I <sub>F</sub> = 0.5A, T <sub>A</sub> = 25°C	CH370 CD750 CSM1700.	-	35	100	μS	
			CSM160 CSM161 CS5700 CSM6730			100		
			R <sub>L</sub> = 680Ω, V <sub>CC</sub> = 5V, I <sub>F</sub> = 5mA, T <sub>A</sub> = 25°C	CH390 CD750 CSM1700	-	3	12	
				CS5700 CSM6730 CSM160/1			10 5	
Current Transfer Ratio (See notes 4 & 5)	CTR	I <sub>F</sub> = 0.5mA, V <sub>O</sub> = 0.4V, V <sub>CC</sub> = 4.5V		300	700	-	%	
		I <sub>F</sub> = 1.6mA, V <sub>O</sub> = 0.4V, V <sub>CC</sub> = 4.5V		200	1000	-		
		I <sub>F</sub> = 5mA, V <sub>O</sub> = 0.4V, V <sub>CC</sub> = 4.5V		200	600	-		
Common Mode Transient Immunity at Logical High Output Level (See notes 4, 10 & 12)	CM <sub>H</sub>	V <sub>CC</sub> = 5V, T <sub>A</sub> = 25°C, V <sub>CM</sub> = 50V p-p R <sub>L</sub> = 1.5KΩ, I <sub>F</sub> = 0Ma		500	1000	-	V/μS	
			Ch370 Cs700 Csm1700 Csm6730					
Common Mode Transient Immunity at Logical Low Output Level (See notes 4, 10 & 12)	CM <sub>L</sub>	V <sub>CC</sub> = 5V, T <sub>A</sub> = 25°C, V <sub>CM</sub> = 50V p-p R <sub>L</sub> = 1.5KΩ, I <sub>F</sub> = 1.6mA		500	-1000	-	V/μS	
			Ch370 Cs700 Csm1700 Csm6730					

## Typical Characteristics

T<sub>A</sub> = 25°C

Parameter	Symbol	Test Conditions	Notes	Min	Type	Max	Units
Resistance	R <sub>IO</sub>	V <sub>10</sub> = 500Vdc	4 & 8	-	10 <sup>12</sup>	-	Ω
Capacitance	C <sub>IO</sub>	f = 1MHz	4 & 8	-	1.5	-	pF
Input Capacitance	C <sub>IN</sub>	f = 1MHz, V <sub>F</sub> = 0	4	-	60	-	pF
Temperature Coefficient of Forward Voltage	$\frac{\Delta V_F}{\Delta T_A}$	I <sub>F</sub> = 1.6mA	1	-	-1.8	-	mV/°C
Input-Input Insulation Leakage Current	I <sub>I-I</sub>	45% Relative Humidity V <sub>II</sub> = 500Vdc, t = 5S	9	-	0.6	-	nA
Resistance	R <sub>I-I</sub>	V <sub>II</sub> = 500Vdc	9	-	10 <sup>12</sup>	-	Ω
Capacitance	C <sub>I-I</sub>	f = 1MHz	9	-	1	-	pF

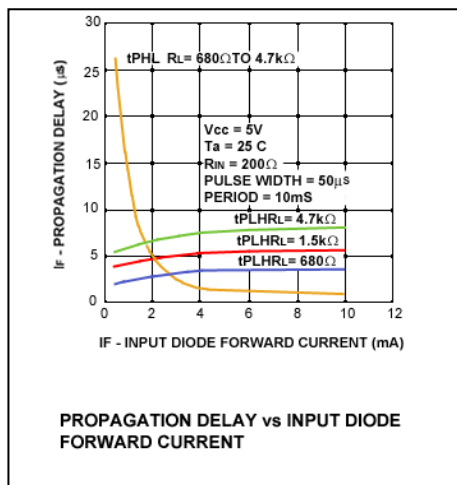
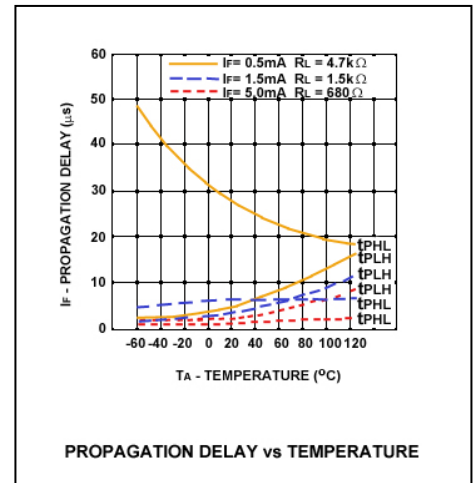
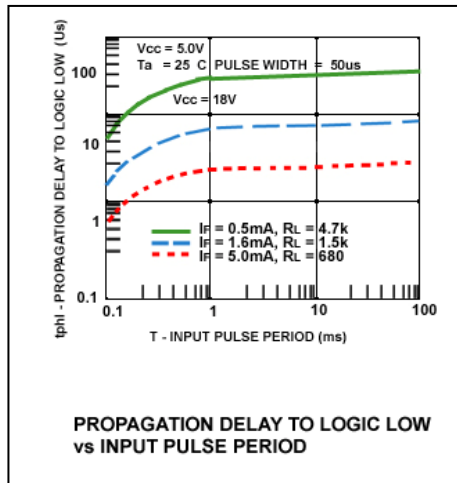
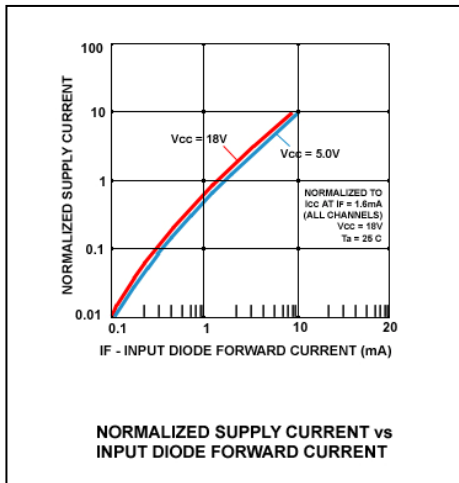
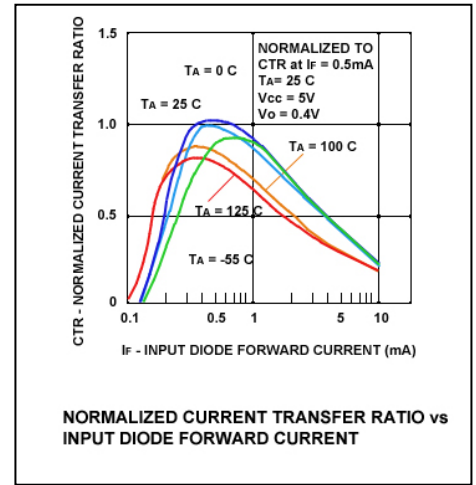
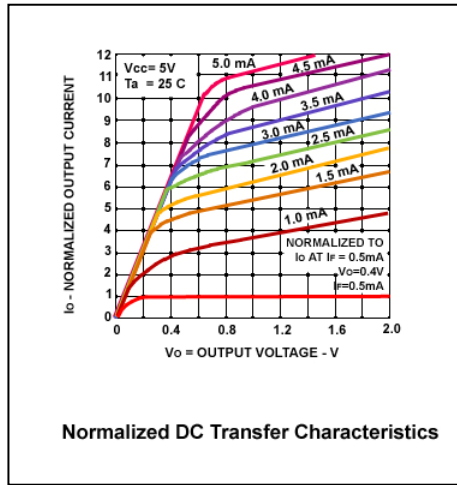
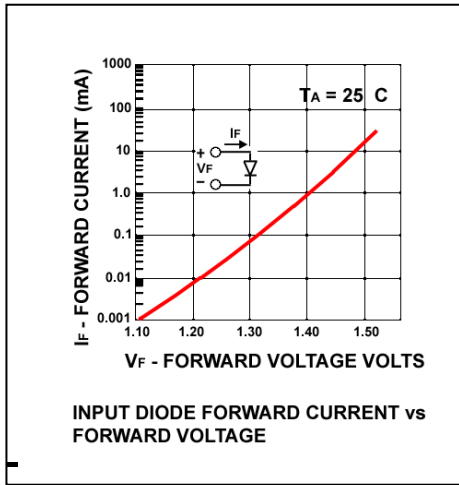
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**Notes:**

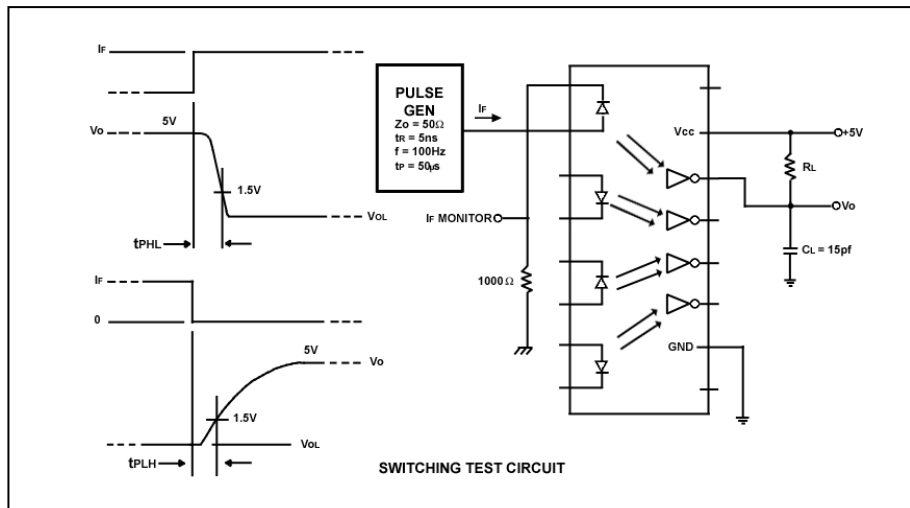
1. The ground pin should be the most negative voltage at the detector side. Keeping  $V_{CC}$  as low as possible, but greater than 2.0V, will provide lowest total  $I_{OH}$  over temperature.
2. Output power is collector output plus one fourth of total supply power. Derate at 1.66mW/°C above 110°C.
3. Derate  $I_F$  at 0.33mA/°C above 110°C.
4. Each channel.
5. Current Transfer Ratio is defined as the ratio of output collector current,  $I_O$ , to the forward LED input current,  $I_F$ , times 100%.
6.  $I_{OHX}$  is the leakage current resulting from channel to channel optical crosstalk.  $I_F = 2\mu A$  for channel under test. For all other channels,  $I_F = 10mA$ .
7. Input pins are shorted together, and output pins are shorted together.
8. Measured between the LED anode and cathode shorted together and pins at output shorted together.
9. Measured between adjacent input pairs shorted together.
10.  $CM_H$  is the maximum tolerable common mode transient to assure that the output will remain in a high logic state (i.e.,  $V_O > 2.0V$ ).
11.  $CM_L$  is the maximum tolerable common mode transient to assure that the output will remain in the logic low state (i.e.,  $V_O < 0.8V$ ).
12. In applications where  $dV/dt$  may exceed 50,000V/ $\mu S$  (such as a static discharge), a series resistor,  $R_{CC}$ , should be included to protect the detector IC's from destructively high surge currents. The recommended value is
 
$$R_{CC} = \frac{1V}{0.6I_F(mA)} \text{ k}\Omega$$
13. This is a momentary withstand test, not an operating condition.

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## Electrical Characteristics



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