



**HIGH CMR, VERY HIGH SPEED
OPTICALLY COUPLED ISOLATOR
LOGIC GATE OUTPUT**

APPROVALS

- UL recognised, File No. E91231

DESCRIPTION

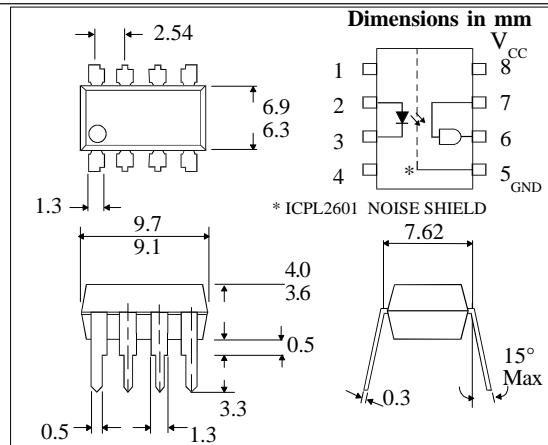
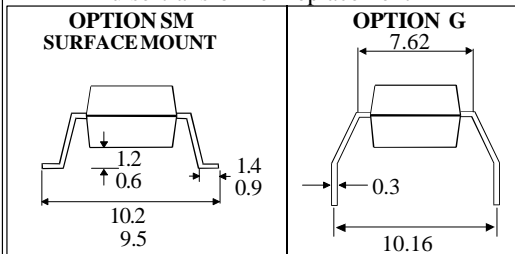
The 6N137 / ICPL2601 optocouplers consist of a GaAsP light emitting diode and a high gain integrated photo detector to provide 2500Volts^{RMS} electrical isolation between input and output. An enable input allows the detector to be strobed. The output of the detector I.C. is an open collector Schottky clamped transistor. The ICPL2601 has an internal shield which provides a guaranteed common mode transient immunity specification of 1000V/μs minimum. This unique design provides maximum ac and dc circuit isolation while achieving TTL compatibility. The coupled parameters are guaranteed over the temperature range of 0°C to 70°C, such that a maximum input signal of 5mA will provide a minimum output sink current of 13mA (equivalent to fan-out of eight gates)

FEATURES

- High speed - 10MBit/s
- High Common Mode Transient Immunity 10kV/μs typical
- Logic gate output
- ICPL2601 has improved noise shield for superior common mode rejection
- Options :-
10mm lead spread - add G after part no.
Surface mount - add SM after part no.
Tape&reel - add SMT&R after part no.

APPLICATIONS

- Line receiver, data transmission
- Computer-peripheral interface
- Data multiplexing
- Pulse transformer replacement



TRUTH TABLE

INPUT	ENABLE	OUTPUT
H	H	L
L	H	H
H	L	H
L	L	H

A 0.1μF bypass capacitor must be connected between pins 8 and 5 (See note 1)

**ABSOLUTE MAXIMUM RATINGS
(25°C unless otherwise specified)**

Storage Temperature _____ -55°C to + 125°C
Operating Temperature _____ 0°C to + 70°C
Lead Soldering Temperature
(1/16 inch (1.6mm) from case for 10 secs) 260°C

INPUT DIODE

Average Forward Current _____ 20mA
Reverse Voltage _____ 5V

DETECTOR

Enable Input Voltage (V_E) _____ 5.5V
(not to exceed V_{CC} by more than 500mV)
Reverse Supply Voltage ($-V_{CC}$) _____ -500mV
Supply Voltage (V_{CC}) _____ 7V
(1 minute maximum)
Output Current (I_O) _____ 25mA
Output Voltage (V_O) _____ 7V
Collector Output Power Dissipation _____ 40mW

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ELECTRICAL CHARACTERISTICS ($T_A = 0^\circ\text{C}$ to 70°C Unless otherwise noted)

PARAMETER	SYM	DEVICE	MIN	TYP*	MAX	UNITS	TEST CONDITION
High Level Output Current	I_{OH}			0.02	250	μA	$V_{CC} = 5.5\text{V}$, $V_O = 5.5\text{V}$ $I_F = 250\mu\text{A}$, $V_E = 2\text{V}$
Low Level Output Voltage	V_{OL}			0.4	0.6	V	$V_{CC} = 5.5\text{V}$, $I_F = 5\text{mA}$ $V_E = 2\text{V}$ $I_{OL}(\text{sinking}) = 13\text{mA}$
High Level Supply Current	I_{CCH}			10	15	mA	$V_{CC} = 5.5\text{V}$, $I_F = 0\text{mA}$ $V_E = 0.5\text{V}$
Low Level Supply Current	I_{CCL}			15	18	mA	$V_{CC} = 5.5\text{V}$, $I_F = 10\text{mA}$ $V_E = 0.5\text{V}$
High Level Enable Current	I_{EH}			-1.0		mA	$V_{CC} = 5.5\text{V}$, $V_E = 2\text{V}$
Low Level Enable Current	I_{EL}			-1.5	-2.0	mA	$V_{CC} = 5.5\text{V}$, $V_E = 0.5\text{V}$
High Level Enable Voltage (note 10)	V_{EH}		2			V	$V_{CC} = 5.5\text{V}$, $I_F = 10\text{mA}$
Low Level Enable Voltage	V_{EL}				0.8	V	$V_{CC} = 5.5\text{V}$, $I_F = 10\text{mA}$
Input Forward Voltage	V_F			1.55	1.75	V	$I_F = 10\text{mA}$, $T_A = 25^\circ\text{C}$
Input Reverse Breakdown Voltage	V_{BR}		5			V	$I_R = 10\mu\text{A}$, $T_A = 25^\circ\text{C}$
Input Capacitance	C_{IN}			60		pF	$V_F = 0$, $f = 1\text{MHz}$
Temperature Coefficient of Forward Voltage	$\frac{\Delta V_F}{\Delta T_A}$			-1.4		mV/ $^\circ\text{C}$	$I_F = 10\text{mA}$
Input-output Isolation Voltage (note 3)	V_{ISO}		2500	5000		V_{RMS}	R.H.equal to or less than 50%, $t = 1\text{min}$, $T_A = 25^\circ\text{C}$
Input-output Insulation Leakage Current (note 3)	I_{LO}				1	μA	R.H. = 45% $t = 5\text{s}$, $T_A = 25^\circ\text{C}$ $V_{LO} = 3000\text{V dc}$
Resistance (Input to Output) (note 3)	R_{LO}			10^{12}		Ω	$V_{LO} = 500\text{V dc}$
Capacitance (Input to Output) (note 3)	C_{LO}			0.6		pF	$f = 1\text{MHz}$

* All typicals at $T_A = 25^\circ\text{C}$

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	MAX	UNITS
Input Current, Low Level	I_{FL}	0	250	μA
Input Current, High Level	I_{FH}	6.3*	15	mA
Supply Voltage, Output	V_{CC}	4.5	5.5	V
Enable Voltage, Low Level	V_{EL}	0	0.8	V
Enable Voltage, High Level	V_{EH}	2.0	V_{CC}	V
Fan Out (TTL Load)	N		8	
Operating Temperature	T_A	0	70	$^\circ\text{C}$

*6.3mA is a guard banded value which allows for at least 20% CTR degradation.
Initial input current threshold value is 5.0mA or less

FIG. 2 TEST CIRCUIT FOR TRANSIENT IMMUNITY AND TYPICAL WAVEFORMS

