

# AC05DSMA,AC05FSMA

### **5 A RESIN INSULATION TYPE TRIAC**

#### **DESCRIPTION**

The AC05DSMA and AC05FSMA are resin insulation type TRIACs with an effective current of 5 A ( $Tc = 99^{\circ}C$ ).

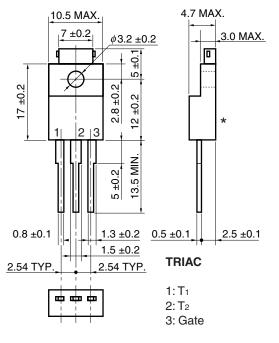
These products are covered with resin mold on the entire case and are electrically insulated with electrodes, giving them a considerable advantage over conventional TRIACs when mounting on a heatsink board or performing high-density mounting.

These products features ratings and electrical characteristics equal to TO-220AB package TRIAC and a high reliability design.

#### **FEATURES**

- Insulation type TRIAC fully covered with resin on the entire case other than electrode leads
- Insulation voltage and conduction equal to conventional mica and polyester film
- Can be replaced with TO-220AB package
- High allowable on-current when using a single unit

# **★** PACKAGE DRAWING (Unit: mm)



\*: Tc test bench-mark

Standard weight: 2 g

## **APPLICATIONS**

Non-contact switches of motor speed control, heater temperature control, lamp light control

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## **MAXIMUM RATINGS**

Parameter	Symbol	AC05DSMA	C05DSMA AC05FSMA		Remarks		
Non-repetitive Peak Off-state Voltage	V <sub>DSM</sub>	500	700		-		
Repetitive Peak Off-state Voltage	$V_{DRM}$	400	600	V	-		
Effective On-state Current	I <sub>T(RMS)</sub>	5 (Tc =	= 99°C)	Α	Refer to Figure 11 and 12.		
Surge On-state Current	Ітѕм	50 (50 H	z 1 cycle)	Α	Refer to Figure 2.		
		55 (60 Hz 1 cycle)					
Fusing Current	∫i⊤²dt	10 (1 ms ≤	( t ≤ 10 ms)	A <sup>2</sup> s	-		
Critical Rate Rise of On-state Current	dl⊤/dt	50		A/μs	_		
Peak Gate Power Dissipation	Р <sub>GМ</sub>	3 (f≥ 50 Hz,	Duty ≤ 10%)	W	-		
Average Gate Power Dissipation	P <sub>G(AV)</sub>	0	.3	W	-		
Peak Gate Current	Ідм	±1.5 (f ≥ 50 Hz, Duty ≤ 10%)		Α	-		
Junction Temperature	Tj	-40 <b>∼</b> +125		°C	-		
Storage Temperature	T <sub>stg</sub>	<b>-55∼+150</b>		°C			

# **ELECTRICAL CHARACTERISTICS (Tj = 25°C)**

Parameter		Symbol	Conditions		MIN.	TYP.	MAX.	Unit	Remarks
Repetitive Peak Off-state Current		IDRM	V <sub>DM</sub> = V <sub>DRM</sub>	T <sub>j</sub> = 25°C	_	_	100	μΑ	_
				T <sub>j</sub> = 125°C	_	_	1	mA	_
On-state Voltage		Vтм	Iтм = 5 A		_	_	1.8	V	Refer to Figure 1.
Gate Trigger Current	Mode I	Іст	V <sub>DM</sub> = 12 V,	T <sub>2</sub> +, G+	_	_	10	mA	Refer to Figure 4.
	II		R <sub>L</sub> = 30 Ω	T <sub>2</sub> , G+	_	_	_		
	III			T2-, G-	_	_	10		
	IV			T2+, G-	_	_	10		
Gate Trigger Voltage	Mode I	V <sub>GT</sub>	V <sub>DM</sub> = 12 V,	T <sub>2</sub> +, G+	_	_	1.5	V	Refer to Figure 4.
	II		R <sub>L</sub> = 30 Ω	T <sub>2</sub> , G+	_	_	_		
	III			T2-, G-	_	_	1.5		
	IV			T2+, G-	_	_	1.5		
Gate Non-trigger Voltage		V <sub>GD</sub>	$T_j = 125^{\circ}C, V_{DM} = \frac{1}{2} V_{DRM}$		0.2	_	_	V	-
Holding Current		lн	V <sub>DM</sub> = 24 V		-	10	_	mA	_
Critical Rate Rise of Off-state Voltage		dv/dt	$T_j = 125^{\circ}C, V_{DM} = \frac{2}{3} V_{DRM}$		-	100	_	V/μs	_
Commutating Critical Rate Rise of		(dv/dt)c	T <sub>j</sub> = 125°C,		5	_	_	V/μs	-
Off-state Voltage			$(di\tau/dt)c = -2.7 \text{ A/ms}, V_D = 400 \text{ V}$						
Thermal Resistance Note		Rth(j-c)	Junction-to-case AC		_	_	4.2	°C/W	Refer to Figure 13.

**Note** The thermal resistance with a 50 Hz or 60 Hz sine wave current, as shown in the following expression:

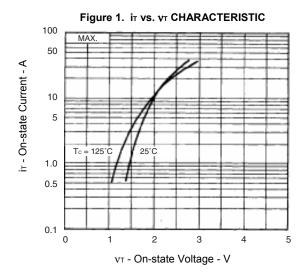
 $R_{th(j-c)} = \frac{T_{j(max)} - T_{c}}{P_{T(AV)}}$   $T_{j(max)}$ : Maximum junction temperature

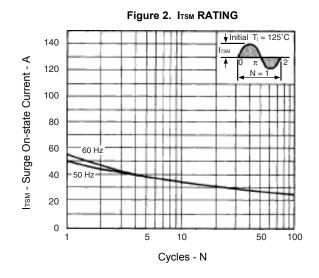
Tc: Case temperature

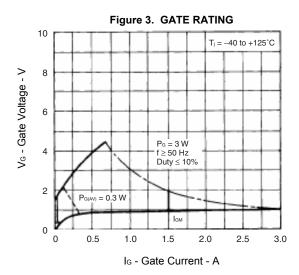
P<sub>T(AV)</sub>: Average on-dissipation

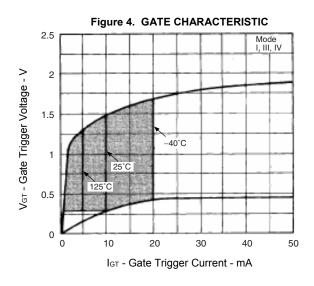


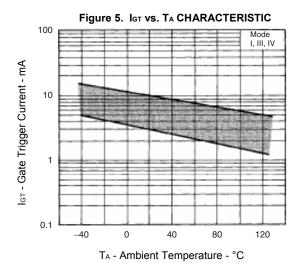
### TYPICAL CHARACTERISTICS

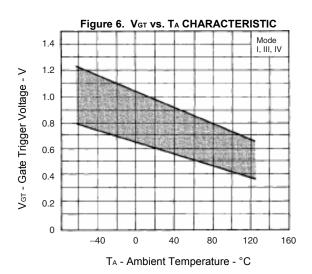


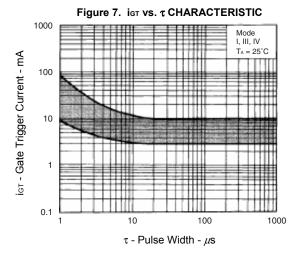


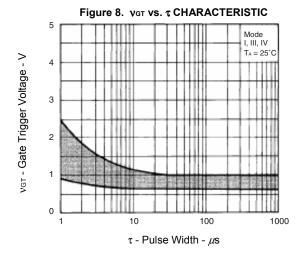


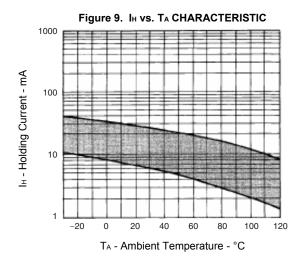


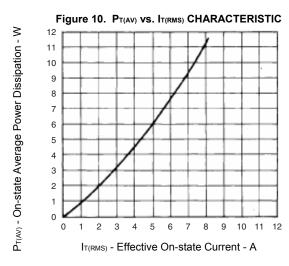


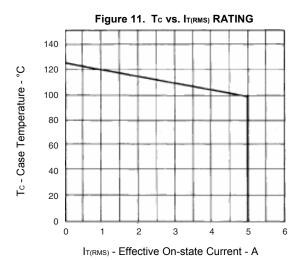


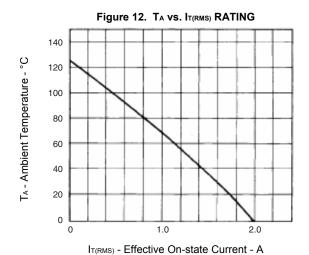


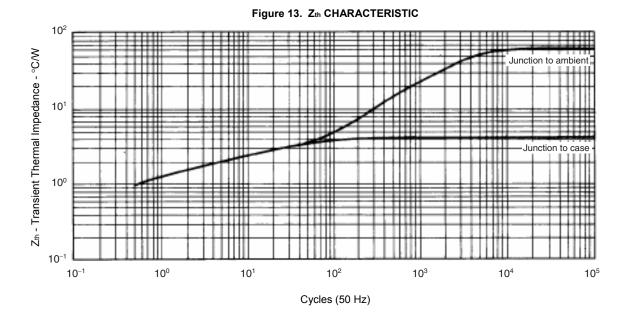












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