



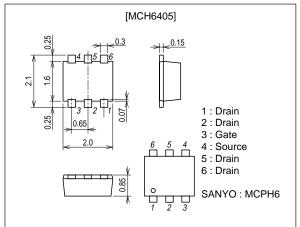
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2193A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	20	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.5		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3A	6.3	9		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=3A, VGS=4V		31	41	mΩ
	R _{DS} (on)2	I _D =1.5A, V _{GS} =2.5V		38	54	mΩ

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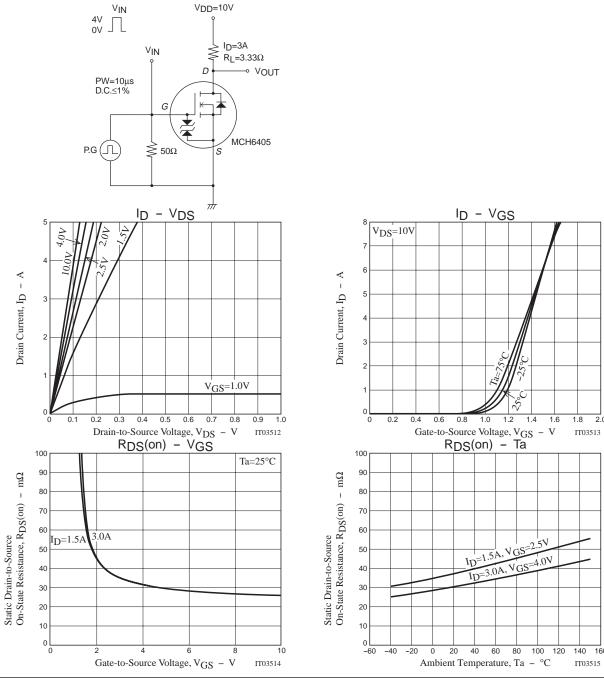
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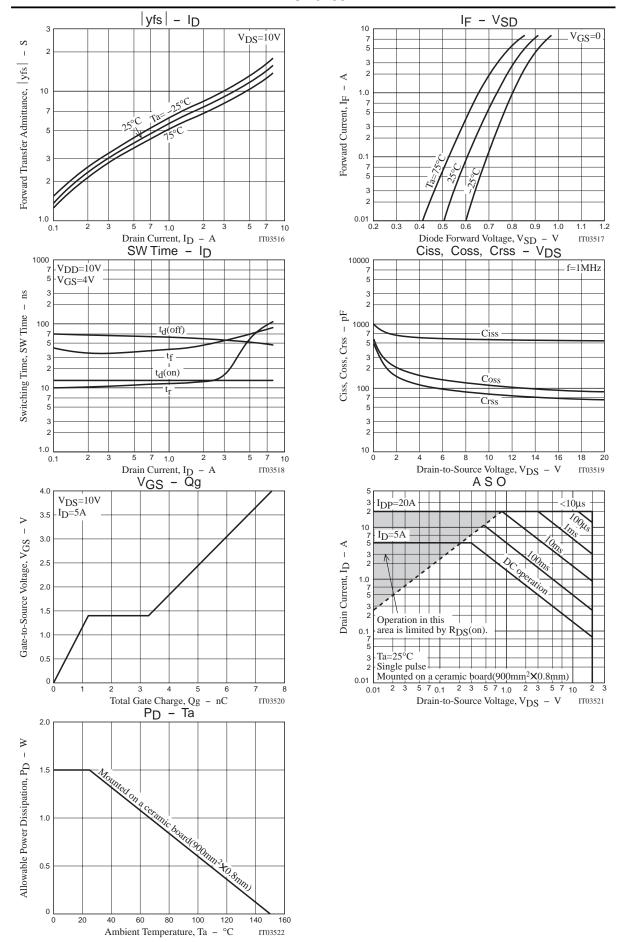
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TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Input Capacitance	Ciss	VDS=10V, f=1MHz		570		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		110		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		80		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		13		ns
Rise Time	tr	See specified Test Circuit		16		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		55		ns
Fall Time	tf	See specified Test Circuit		54		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =4V, I _D =5A		7.6		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =4V, I _D =5A		1.2		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =4V, I _D =5A		2.1		nC
Diode Forward Voltage	V _{SD}	I _S =5A, V _{GS} =0		0.86	1.2	V

Switching Time Test Circuit





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