

### **General Description**

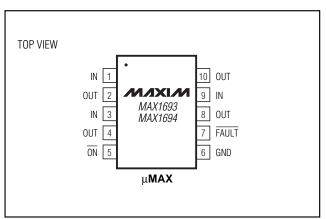
The MAX1693/MAX1694 are current-limited,  $60m\Omega$ switches with built-in fault blanking. Their accurate preset current limit of 0.7A to 1.0A makes them ideally suited for USB applications. Their low quiescent supply current (14µA) and shutdown current (1µA) conserve battery power in portable applications. The MAX1693/ MAX1694 operate with inputs from +2.7V to +5.5V, making them ideal for both +3V and +5V systems.

A fault signal notifies the microprocessor that the internal current limit has been reached. A 10ms fault-blanking feature allows momentary faults (such as those caused when hot-swapping into a capacitive load) to be ignored, thus preventing false alarms to the host system. This fault blanking also prevents a fault signal from being issued when the device is powering up.

In the MAX1693, an output overcurrent condition causes the switch to current limit at 0.7A to 1.0A and FAULT to go low after the 10ms blanking period. When the overcurrent condition is removed, FAULT returns to its high-impedance state. In the MAX1694, any overcurrent longer than 10ms will latch the switch open and set FAULT low. The latch is cleared by cycling the ON input or by powering up the device again. This feature saves power by preventing the device from thermally cycling on and off in case of a persistent short-circuit condition.

The MAX1693/MAX1694 have several safety features to ensure that the USB port is protected. Built-in thermaloverload protection limits power dissipation and junction temperatures. Both devices have accurate internal current-limiting circuitry to protect the input supply against overload. They are available in space-saving 10-pin µMAX packages.

## Pin Configuration



#### **Features**

- ♦ Accurate Current Limit (0.7A min, 1.0A max)
- **♦** Guaranteed 0.75A Short-Circuit Protection
- ♦ 10ms Internal Fault-Blanking Timeout
- ♦ No Fault Signal During Power-Up
- **♦ Latched FAULT Output Turns Off Power Switch** (MAX1694)
- **♦ Thermal Shutdown Protection**
- ♦ +2.7V to +5.5V Supply Range
- ♦ 14µA Supply Current
- ♦ Small 10-Pin µMAX Package
- ♦ UL Recognition Pending

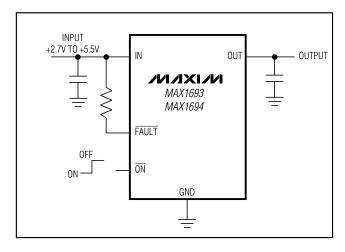
## **Applications**

USB Ports and Hubs Notebook Computers Portable Equipment **Docking Stations** Hot Plug-In Power Supplies Battery-Charger Circuits

## **Ordering Information**

PART	TEMP. RANGE	PIN-PACKAGE
MAX1693EUB	-40°C to +85°C	10 μMAX
MAX1694EUB	-40°C to +85°C	10 μMAX

## Typical Operating Circuit



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#### **ABSOLUTE MAXIMUM RATINGS**

IN, ON, FAULT to GND	0.3V to +6V
OUT to GND	
Maximum Continuous Switch Current	1.2A (internally limited)
OUT Short Circuit to GND	Continuous
Continuous Power Dissipation (TA = -	+70°C)
10-Pin µMAX (derate 5.6mW/°C abo	ove +70°C)444mW

40°C to +85°C
65°C to +150°C
+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{IN} = +5V, T_A = 0^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ 

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Voltage	VIN			2.7		5.5	V
Quiescent Current	Io	$V \overline{ON} = GND, IOUT = 0$	Timer not running		14	25	μΑ
Quiescent Current	iQ	V 0N = QND, 1001 = 0	Timer running		35		
Off-Supply Current		VON = VIN = VOUT = 5	5.5V		0.001	1	μΑ
Undervoltage Lockout	UVLO	Rising edge, 100mV h	ysteresis	2.0		2.6	V
Off-Switch Leakage		$V_{\overline{ON}} = V_{IN},$ $V_{IN} = 5.5V,$	T <sub>A</sub> = +25°C		0.01	2	- μΑ
On-Switch Leakage		$V_{OUT} = GND$	$T_A = 0$ °C to +85°C			15	
		T <sub>A</sub> = +25°C	$V_{IN} = 4.4V \text{ to } 5.5V$		60	90	
On-Resistance	Ron	$T_A = 0^{\circ}C \text{ to } +85^{\circ}C$	$V_{IN} = 4.4V \text{ to } 5.5V$			125	mΩ
		1A = 0 0 to +00 0	$V_{IN} = 3V$		72	150	
Current Limit	ILIMIT	(Note 1)		700	850	1000	mA
Continuous Short-Circuit Current Limit	I <sub>SC</sub>	OUT shorted to GND, MAX1693 only (Note 2)			500	700	mA
ON Input Logic Low Voltage	VIL	V <sub>IN</sub> = 2.7V to 5.5V				0.8	V
ON logget Logic Lligh Voltage	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	V <sub>IN</sub> = 2.7V to 3.6V		2			V
ON Input Logic High Voltage	VIH	V <sub>IN</sub> = 3.7V to 5.5V		2.4			V
ON Input Leakage		V ON = VIN or GND				±1	μΑ
FAULT Output Logic Low Voltage	VoL	I <sub>SINK</sub> = 1mA, V <sub>IN</sub> = 3V				0.4	V
FAULT Output High Leakage Current		V <sub>IN</sub> = V <sub>FAULT</sub> = 5.5V				1	μА
Fault-Blanking Timeout Period	t <sub>FB</sub>	From overcurrent condition to FAULT assertion		7	10	13	ms
Start-Up Time		$V_{IN} = 5V$ , $C_{OUT} = 150\mu$ from $\overline{ON}$ driven low to			1		ms
Switch Turn-On Time	ton	ILOAD = 400mA			80	200	μs
Switch Turn-Off Time	toff	I <sub>LOAD</sub> = 400mA		3	6	20	μs
Thermal Shutdown Threshold					165		°C

#### **ELECTRICAL CHARACTERISTICS**

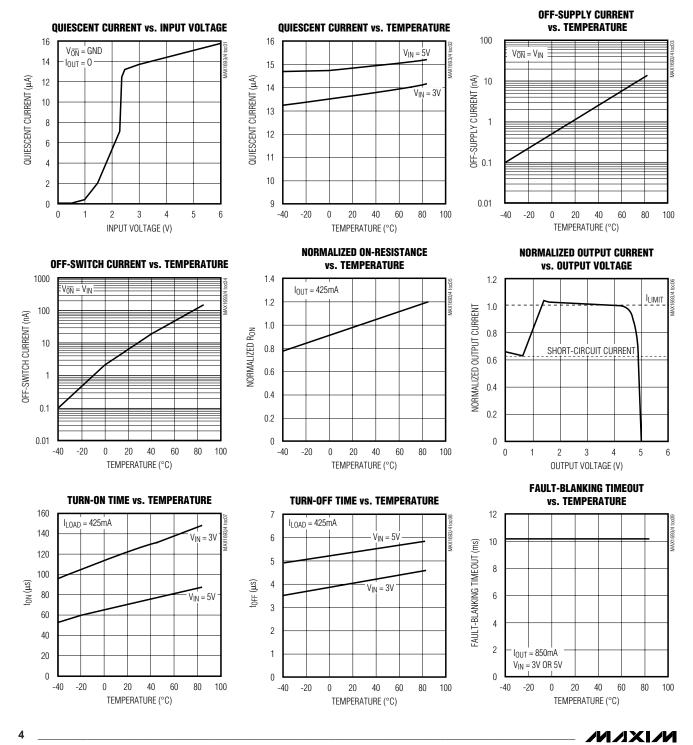
 $(V_{IN} = +5V, T_A = -40$ °C to +85°C, unless otherwise noted.) (Note 3)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Operating Voltage	VIN		3		5.5	V	
Quiescent Current	IQ	$V_{\overline{ON}} = GND$ , $I_{OUT} = 0$ , timer not running			25	μΑ	
Off-Supply Current		$V_{\overline{ON}} = V_{\overline{IN}} = V_{OUT} = 5.5V$			2	μA	
Undervoltage Lockout	UVLO	Rising edge, 100mV hysteresis	2.0		2.9	V	
Off-Switch Leakage		$V_{\overline{ON}} = V_{IN} = 5.5V, V_{OUT} = GND$			15	μΑ	
On Desigtance	Davi	V <sub>IN</sub> = 4.4V to 5.5V			125	0	
On-Resistance	Ron	V <sub>IN</sub> = 3V			150	mΩ	
Current Limit	ILIMIT	(Note 1)	640		1060	mA	
Continuous Short-Circuit Current Limit		OUT shorted to GND, MAX1693 only (Note 2)			750	mA	
ON Input Logic Low Voltage	VIL	V <sub>IN</sub> = 3V to 5.5V			0.8	V	
ON Input Logic High Voltage	Viii	V <sub>IN</sub> = 3V to 3.6V	2			V	
ON Input Logic High Voltage	VIH	V <sub>IN</sub> = 3.7V to 5.5V	2.4			]	
ON Input Leakage		VON = VIN or GND			±1	μΑ	
FAULT Output Logic Low Voltage	V <sub>OL</sub>	I <sub>SINK</sub> = 1mA, V <sub>IN</sub> = 3V			0.4	V	
FAULT Output High Leakage Current		V <sub>IN</sub> = V <sub>FAULT</sub> = 5.5V			1	μΑ	
Fault-Blanking Timeout Period	t <sub>FB</sub>	From overcurrent condition to FAULT assertion	6		14	ms	
Switch Turn-On Time	ton	ILOAD = 400mA			200	μs	
Switch Turn-Off Time	toff	I <sub>LOAD</sub> = 400mA	1		20	μs	

- **Note 1:** MAX1693's current limit is tested by forcing V<sub>OUT</sub> to 4.5V. The MAX1694's current limit is tested by increasing the output current until the switch is latched off.
- Note 2: This specification applies to the MAX1693 only. The MAX1694 latches the switch off under a sustained (>10ms) short-circuit condition.
- **Note 3:** Specifications to -40°C are guaranteed by design, not production tested.

### **Typical Operating Characteristics**

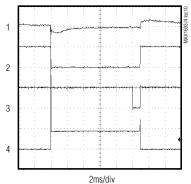
 $(V_{IN} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$ 



## **Typical Operating Characteristics (continued)**

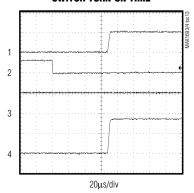
 $(V_{IN} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$ 

#### MAX1693 CURRENT-LIMIT AND FAULT RESPONSE



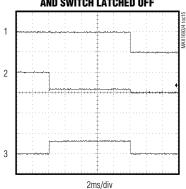
 $\begin{array}{l} CH1=V_{IN},\,200mV/div,\,AC\text{-}COUPLED;\,CH2=V_{OUT},\\ 5V/div;\,CH3=V_{\overline{FAULT}},\,5V/div;\,CH4=I_{OUT},\,500mA/div \end{array}$ 

#### MAX1693 SWITCH TURN-ON TIME



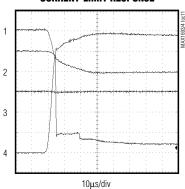
CH1 =  $V_{OUT}$ , 5V/div; CH2 =  $V_{\overline{ON}}$ , 5V/div; CH3 =  $V_{\overline{FAULT}}$ , 5V/div; CH4 =  $I_{OUT}$ , 200mA/div

#### MAX1694 OVERCURRENT TO FAULT AND SWITCH LATCHED OFF



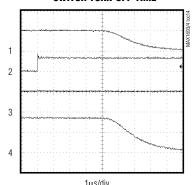
 $CH1 = V_{\overline{FAULT}}$ , 5V/div;  $CH2 = I_{OUT}$ , 5V/div;  $CH3 = I_{OUT}$ , 1A/div

#### MAX1693 CURRENT-LIMIT RESPONSE



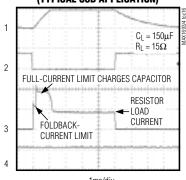
 $\begin{aligned} &\text{CH1} = \text{V}_{\text{IN}}, \ 200\text{mV/div}, \ \text{AC-COUPLED}; \ \text{CH2} = \text{V}_{\text{OUT}}, \\ &\text{5V/div}; \ \text{CH3} = \text{V}_{\overline{\text{FAULT}}}, \ \text{5V/div}; \ \text{CH4} = \text{I}_{\text{OUT}}, \ \text{1A/div} \end{aligned}$ 

#### MAX1693 SWITCH TURN-OFF TIME



 $\begin{array}{l} CH1 = V_{OUT},\,5V/div;\,CH2 = V_{\overline{ON}},\,5V/div;\\ CH3 = V_{\overline{FAULT}},\,5V/div;\,CH4 = I_{OUT},\,200mA/div \end{array}$ 

#### MAX1693 Start-up time (Typical USB application)



$$\begin{split} CH1 &= V_{OUT}, \, 5V/div; \, CH2 = V_{ON}, \, 5V/div; \\ CH3 &= I_{OUT}, \, 500mA/div; \, CH4 = V_{FAULT}, \, 5V/div \end{split}$$

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### **Pin Description**

PIN	NAME	FUNCTION		
1, 3, 9	IN Input. P-channel MOSFET source. Connect all IN pins together and bypass with a 1µF ceramic capacitor to ground.			
2, 4, 8, 10	Switch Output. P-channel MOSFET drain. Connect all OUT pins together and bypass 0.1µF capacitor to ground.			
5	ŌN	Active-Low Switch On Input. A logic low turns the switch on.		
6	GND	Ground		
7 FAULT		Fault-Indicator Output. This open-drain output goes low when the device is in thermal shut-down or undervoltage lockout or on a sustained (>10ms) current-limit condition. For the MAX1694 only, this output latches low when asserted, and the power switch is turned off until the latch is reset.		

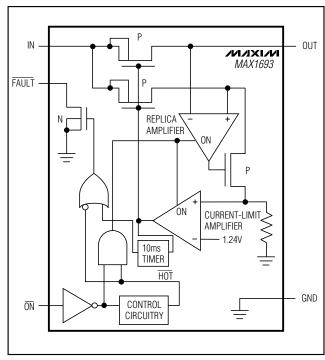


Figure 1. MAX1693 Functional Diagram

## **Detailed Description**

The MAX1693/MAX1694 P-channel MOSFET power switches limit output current to 0.7A min to 1.0A max. When the output current is increased beyond the current limit ( $I_{LIMIT}$ ), the current also increases through the replica switch ( $I_{OUT}$ /6500). The current-limit error amplifier compares the voltage to the internal 1.24V reference and regulates the current back to the  $I_{LIMIT}$ .

These switches are not bidirectional; therefore, the input voltage must be higher than the output voltage.

#### **Continuous Short-Circuit Protection**

The MAX1693/MAX1694 are foldback short circuit-protected switches. In the event of an output short-circuit or current-overload condition, the current through the switch is foldback-current limited to 500mA continuous for the MAX1693. For the MAX1694, if the short circuit lasts longer than 10ms, the switch is latched off.

#### **Thermal Shutdown**

The MAX1693/MAX1694 feature thermal shutdown. The switch turns off and the  $\overline{\text{FAULT}}$  output goes low immediately (no fault blanking) when the junction temperature exceeds +165°C. The MAX1694 remains latched with the switch off and the  $\overline{\text{FAULT}}$  output low. When the MAX1693 cools 20°C, the switch turns back on. If the fault short-circuit condition is not removed, the switch will cycle on and off, resulting in a pulsed output.

#### FAULT Indicator

The MAX1693/MAX1694 provide a fault output (FAULT). A 100k $\Omega$  pull-up resistor from FAULT to IN provides a logic control signal. This open-drain output goes low when any of the following conditions occur:

- The input voltage is below the undervoltage-lockout (UVLO) threshold.
- The die temperature exceeds the thermal shutdown temperature limit of +165°C.
- The device is in current limit and the 10ms faultblanking period is exceeded.

#### **Fault Blanking**

The MAX1693/MAX1694 feature 10ms fault blanking. Fault blanking allows current-limit faults, including

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momentary short-circuit faults that occur when hotswapping a capacitive load, and also ensures that no fault is issued during power-up. When a load transient causes the device to enter current limit, an internal counter starts. If the load fault persists beyond the 10ms fault-blanking timeout, the FAULT output asserts low. Ensure that the MAX1693/MAX1694's input is adequately bypassed to prevent input glitches from triggering spurious FAULT outputs. Input voltage glitches less than 150mV will not cause a spurious FAULT output. Load-transient faults less than 10ms (typ) will not cause a FAULT output assertion.

Only current-limit faults are blanked. Die overtemperature faults and input voltage droops below the UVLO threshold will cause an immediate fault output.

### Fault Latching (MAX1694 Only)

The MAX1694 features a latched FAULT output. Whenever the FAULT output is activated, it latches the FAULT output low and also turns the switch off. To clear the latch, either cycle the ON input or cycle the input voltage below UVLO.

## **Applications Information**

### **Input Capacitor**

To limit the input voltage drop during momentary output short-circuit conditions, connect a capacitor from IN to GND. A 1 $\mu$ F ceramic capacitor will be adequate for most applications; however, higher capacitor values will further reduce the voltage drop at the input. See Figure 2.

#### **Output Capacitor**

Connect a  $0.1\mu F$  capacitor from OUT to GND. This capacitor helps prevent inductive parasitics from pulling OUT negative during turn-off.

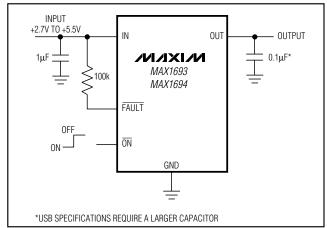


Figure 2. Typical Application Circuit

#### **Layout and Thermal Dissipation**

To optimize the switch-response time to output short-circuit conditions, it is very important to keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors as close to the device as possible (no more than 5mm). All IN and all OUT pins must be connected with short traces to the power bus. Wide power bus planes will provide superior heat dissipation through the switch IN and OUT pins. Figure 3 shows suggested pin connections for a single-layer board.

Under normal operating conditions, the package can dissipate and channel heat away. Calculate the maximum power dissipation as follows:

$$P = (ILIMIT)^2 \cdot RON$$

where ILIMIT is the preset current limit (1.0A max) and R<sub>ON</sub> is the on-resistance of the switch (125m $\Omega$  max).

When the output is short-circuited, foldback-current limiting activates and the voltage drop across the switch equals the input supply. The power dissipated across the switch increases, as does the die temperature. If the fault condition is not removed, the thermal-overload-protection circuitry activates (see the *Thermal Shutdown* section). Wide power-bus planes connected to IN and OUT and a ground plane in contact with the device will help dissipate additional heat.

### \_Chip Information

**TRANSISTOR COUNT: 715** 

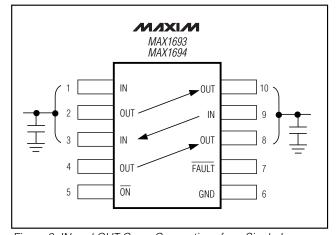
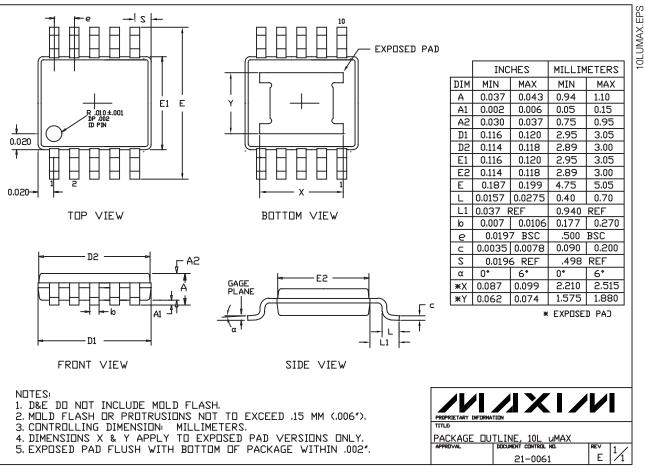


Figure 3. IN and OUT Cross Connections for a Single-Layer Board

### **Package Information**



Note: Neither the MAX1693 nor MAX1694 has an exposed pad.

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