

SIOV metal oxide varistors

Strap varistors, HighE (AdvanceD) series

Series/Type: LS41 ... QP

Date: December 2007

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HighE, AdvanceD, LS41 series

Construction

- Rectangular varistor element
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Bolt-holed strap terminals for screw fixing or soldering
- Bent strap terminals

Features

- Dimensions equivalent to SIOV-LS40
- High surge current up to 50 kA
- Wide operating voltage range 130 ... 460 V_{RMS}
- Designed in accordance with the requirements of IEC 61643-1, class II, for low-voltage surge protection devices

Approvals

- UL
- CSA

Delivery mode

■ Vacuum-packed styrofoam box

General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to CECC 42 000	-25 + 85	°C
Storage temperature		-25 +110	°C
Electric strength	to CECC 42 000	≥2.5	kV _{RMS}
Insulation resistance	to CECC 42 000	≥10	$M\Omega$
Response time		<25	ns







Maximum ratings ($T_A = 85 \, ^{\circ}C$)

Ordering code	Туре	V_{RMS}	V_{DC}	i _{max}	I _{max} 1)	I _{nom} ²⁾	W _{max}	P _{max}
				(8/20 μs)	(8/20 μs)	(8/20 μs)	(2 ms)	
	SIOV-	V	V	Α	Α	Α	J	W
B72241L0131K100	LS41K130QP	130	170	50000	45000	20000	310	1.4
B72241L0141K100	LS41K140QP	140	180	50000	45000	20000	340	1.4
B72241L0151K100	LS41K150QP	150	200	50000	45000	20000	360	1.4
B72241L0231K100	LS41K230QP	230	300	50000	45000	20000	460	1.4
B72241L0251K100	LS41K250QP	250	320	50000	45000	20000	490	1.4
B72241L0271K100	LS41K275QP	275	350	50000	45000	20000	550	1.4
B72241L0321K100	LS41K320QP	320	420	50000	45000	20000	640	1.4
B72241L0381K100	LS41K385QP	385	505	50000	45000	20000	800	1.4
B72241L0421K100	LS41K420QP	420	560	50000	45000	20000	910	1.4
B72241L0441K100	LS41K440QP	440	585	50000	45000	20000	950	1.4
B72241L0461K100	LS41K460QP	460	615	50000	45000	20000	960	1.4

Characteristics ($T_A = 25$ °C)

Ordering code	Туре	V_{v}	ΔV_{v}	V _{c, max}	i _c	C_{typ}
		(1 mA)	(1 mA)	(i _c)		(1 kHz)
	SIOV-	V	%	V	Α	pF
B72241L0131K100	LS41K130QP	205	±10	340	300	5600
B72241L0141K100	LS41K140QP	220	±10	365	300	5200
B72241L0151K100	LS41K150QP	240	±10	395	300	4800
B72241L0231K100	LS41K230QP	360	±10	595	300	3200
B72241L0251K100	LS41K250QP	390	±10	650	300	2900
B72241L0271K100	LS41K275QP	430	±10	710	300	2700
B72241L0321K100	LS41K320QP	510	±10	840	300	2300
B72241L0381K100	LS41K385QP	620	±10	1025	300	1900
B72241L0421K100	LS41K420QP	680	±10	1120	300	1800
B72241L0441K100	LS41K440QP	715	±10	1180	300	1700
B72241L0461K100	LS41K460QP	750	±10	1240	300	1600

¹⁾ I_{max} = Maximum discharge current to IEC 61643-1, class II 2) I_{nom} = Nominal discharge current to IEC 61643-1, class II

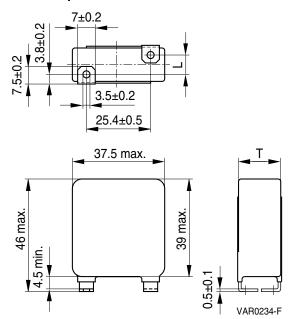




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Dimensional drawings

Bent strap terminals

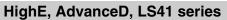


Ordering code	T _{max} mm	L ±1.0 mm
B72241L0131K100	8.1	-3.5
B72241L0141K100	8.2	-3.3
B72241L0151K100	8.3	-3.2
B72241L0231K100	9.0	-2.0
B72241L0251K100	9.2	-1.8
B72241L0271K100	9.4	-1.6
B72241L0321K100	9.9	-1.1
B72241L0381K100	10.6	-0.4
B72241L0421K100	10.9	0.0
B72241L0441K100	11.1	0.2
B72241L0461K100	11.4	0.4

SIOV-LS41K ... QP

Weight: 20 ... 40 g







Reliability data

Test	Test methods/conditions	Requirement		
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called V_v (1 mA _{DC} @ 0.2 2 s).	To meet the specified value.		
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value.		
Max. DC operating voltage	The maximum allowable DC operating voltage V_{DC} at UCT +5/-0 °C is applied for 1000 \pm 48 h.	I_{leak} (t = 1000 h) $\leq I_{leak}$ (t = 0 h $ \Delta V/V$ (1 mA) \leq 10%		
	The leakage current I_{leak} (t) during test is recorded. Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_v shall be measured.			
Surge current derating, 8/20 μs	10 surge currents (8/20 μ s), unipolar, interval \geq 60 s, amplitude corresponding to derating curve for 10 impulses at 20 μ s	ΔV/V (1 mA) ≤10% (measured in direction of surge current) No visible damage		
Fast temperature cycling	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	∆V/V (1 mA) ≤10% No visible damage		
Damp heat, steady state	IEC 60068-2-78 The specimen shall be subjected to 40 ± 2 °C, 90 to 95% r. H. for 56 ± 2 days with 10% of the maximum continuous DC operating voltage V_{DC} . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V_v shall be measured.	∆V/V (1 mA) ≤10%		

Note:

UCT = Upper category temperature LCT = Lower category temperature





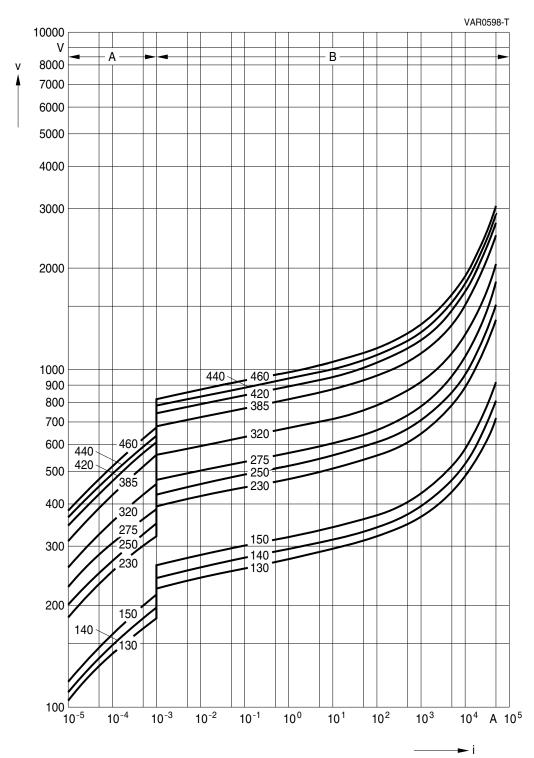
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v/i characteristics

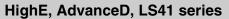
v = f(i) – for explanation of the characteristics refer to "General technical information", 1.6.3

A = Leakage current \int for worst-case B = Protection level

\varistor tolerances





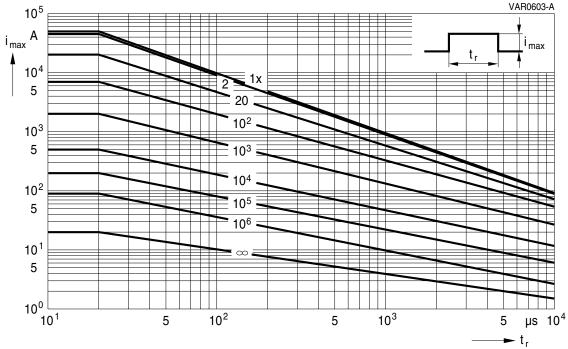




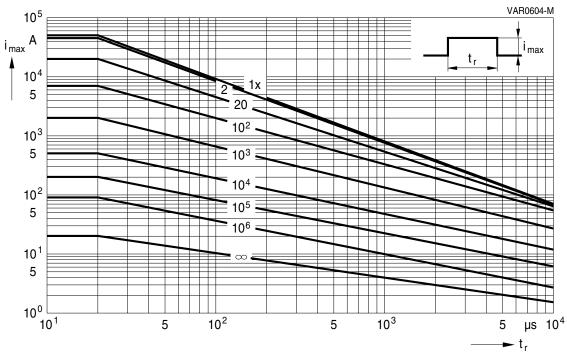
Derating curves

Maximum surge current $i_{max} = f(t_r, pulse train)$

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-LS41K130QP ... K150QP



SIOV-LS41K230QP ... K460QP



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Cautions and warnings

General

- 1. EPCOS metal oxide varistors (SIOVs) are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

- 1. Store SIOVs only in original packaging. Do not open the package before storage.
- 2. Storage conditions in original packaging:

Storage temperature: -25 °C ... +45 °C Relative humidity: <75% annual average,

<95% on maximum 30 days a year.

Is to be avoided. Dew precipitation:

- 3. Avoid contamination of an SIOV's surface during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered within the time specified:

SIOV-S, -Q, -LS 24 months ETFV and SFS types 12 months.

Handling

- SIOVs must not be dropped.
- Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.



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Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- 3. Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in the presence of deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas, etc), corrosive agents, humid or salty conditions. Avoid contact with any liquids and solvents.



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The following applies to all products named in this publication:

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