# MIL-PRF-38534 CERTIFIED



M.S.KENNEDY CORP.

# RAD HARD ULTRA LOW DROPOUT POSITIVE ADJUSTABLE 5826RH LINEAR REGULATOR

#### 4707 Dey Road Liverpool, N.Y. 13088

(315) 701-6751

#### FEATURES:



- Manufactured using
- Replaces IR OMR9605 and IRUH50PA23A
- Total Dose Tested to 450K RAD (Method 1019.7 Condition A)
- Ultra Low Dropout for Reduced Power Consumption
- External Shutdown Function
- Latching Overload Protection
- Optimized for 5.0V Input
- 1.265V to (VIN-0.4V) Output Voltage Range
- Output Current Limit
- · Available in 2 Lead Form Options: Straight and Gullwing
- Contact MSK for MIL-PRF-38534 Qualification Status
- Available soon to DSCC SMD 5962F09210

EQUIVALENT SCHEMATIC

#### **DESCRIPTION:**

The MSK 5826RH is a rad hard adjustable output linear regulator capable of delivering 3.0 amps of output current. Typical dropout is only 0.30 volts with a 3 amp load. An external shutdown function is ideal for power supply sequencing. This device also has internal latching overload protection. The MSK 5826RH is radiation hard and specifically designed for space/satellite applications. The device is packaged in a hermetically sealed space efficient 8 pin flatpack that is electrically isolated from the internal circuitry allowing for direct heat sinking.

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#### • Satellite System Power Supplies

- Switching Power Supply Post Regulators
- Constant Voltage/Current Regulators
- Microprocessor Power Supplies

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PIN-OUT INF	ORMATION
1 GND	8 VIN
2 GND	7 VIN

-		•	
2	GND	7	VIN
3	SHUTDOWN	6	VOUT
4	ADJUST	5	VOUT

4

# **ABSOLUTE MAXIMUM RATINGS**

+ Vin	Supply Voltage
OUT	Output Current ⑦
Tc	Case Operating Temperature Range

9

- MSK5826K/H/E RH . . . . . . -55°C to + 125°C MSK5826RH .....-40°C to + 85°C
- Тѕт Storage Temperature Range -65°C to + 150°C
- TLD
  - (10 Seconds)
- Power Dissipation. . . . . . . . . . See SOA Curve PD Tc

# **ELECTRICAL SPECIFICATIONS**

Parameter	Test Conditions ① 10		Group A	MSK 5826 K/H/E RH			MSK 5826RH			Units
T diamotor			Subgroup	Min.	Typ.	Max.	Min.	Typ.	Max.	Onita
			1,2,3	1.225	1.265	1.305	-	-	-	V
Reference Voltage		Post 100KRAD(Si)	1	1.225	-	1.305	1.225	-	1.305	V
		Post 300KRAD(Si)	1	1.225	-	1.310	1.225	-	1.310	V
Line Desulation	4.5V <u>&lt;</u> VIN <u>&lt;</u> 5.5V, IO=50mA		1	-0.25	-	+0.25	-0.25	-	+0.25	%/V
Line Regulation			2,3	-0.5	-	+0.5	-	-	-	%/V
Land Desulation	VIN=5.0V, 50mA <iout<3.0a< td=""><td>1</td><td>-1.0</td><td>-</td><td>1.0</td><td>-1.0</td><td>-</td><td>1.0</td><td>%/A</td></iout<3.0a<>		1	-1.0	-	1.0	-1.0	-	1.0	%/A
Load Regulation			2,3	-2.0	-	2.0	-	-	-	%/A
Input Voltage Range-Operating (2) IO = 3.0A			1,2,3	4.5	-	6.5	4.5	-	6.5	V
Dropout Voltage (5) IO = 3.0A, VOUT = 2.5V			1,2,3	-	-	0.5	-	-	0.5	V
Current Limit 7 VIN = 5.0V, Overcurrent Latchup			1,2,3	3.0	-	-	3.0	-	-	А
Ripple Rejection 2	F = 120Hz,	10 = 50mA	4	65	-	-	65	-	-	dB
		Post 300KRAD(Si)	4	40	-	-	40	-	-	dB
Shutdown Source Current (8) VSHDN = 5.0V			-	-	200	-	-	200	-	uA
Shutdown Pin Threshold (8) VIN = 5.0V		1,2,3	1.0	-	1.6	1.0	-	1.6	V	
Output Voltage at Shutdown $VIN = 5.0V, IO = 50mA$ VSHDN = + 5.0V			1,2,3	-0.1	-	+0.1	-0.1	-	+0.1	V
Thermal Resistance ② Junction to Case @ 125°C Output Device			-	-	5.0	6.5	-	5.0	6.5	°C/W

#### NOTES:

- 1 Unless otherwise specified, VIN= 5.0V, VSHUTDOWN= 0V and IOUT = 50mA. See figure 2 for typical test circuit.
- ② Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.
  ③ Industrial grade and "E" suffix devices shall be tested to subgroups 1 and 4 unless otherwise requested.
  ④ Military grade devices ("H" and "K" suffix) shall be 100% tested to subgroups 1,2,3 and 4.

- 5 Dropout limited by minimum value of VIN.
- 6 Subgroup 1,4  $\overrightarrow{Tc} = +25^{\circ}C$
- Subgroup 2 Tc= + 125°C
- Subgroup 3  $T_A = -55^{\circ}C$
- ⑦ Output current limit is dependent upon the values of VIN and VOUT. See Figure 1 and typical performance curves.
- Refer to typical performance curves.
- Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- 1 Pre and post irradiation limits at 25°C, up to 300Krad TID, are identical unless otherwise specified.
- ① Reference DSCC SMD 5962F09210 for electrical specification for devices purchased as such.

#### **PIN FUNCTIONS**

Vin - These pins provide power to all internal circuitry including bias, start-up, thermal limit and overcurrent latch. Input voltage range is 2.9V to 6.5V but the MSK 5826RH is optimized for 5.0V input. See MSK 5824RH for 3.3V input version.

**GND** - Internally connected to ground, these pins should be connected externally by the user to the circuit ground.

**SHUTDOWN** - There are two functions to the SHUTDOWN pin. It may be used to disable the output voltage or to reset a current latch condition. To activate the shutdown/ reset functions the user must apply a voltage greater than 1.6V to the SHUTDOWN pin. The output voltage will turn on when the SHUTDOWN pin is pulled below the threshold voltage. If the SHUTDOWN pin is not used, it should be connected to ground. It should be noted that with the shutdown pin tied to ground, a current latch condition can only be reset by cycling power off, then on.

**Vour** - These are the output pins for the device.

**ADJUST** - This pin is used to set the output voltage. Connect a resistor between the adjust pin (4) and the output pins (5 and 6) to fix the output voltage at the desired value. Use the following equations to determine the nominal value of the adjust resistor (RADJ).

VOUT = VREF x  $(1 + RADJ/1K\Omega)$ 

 $RADJ = (VOUT/VREF - 1) \times 1K\Omega$ 

#### **OVERCURRENT LATCH**

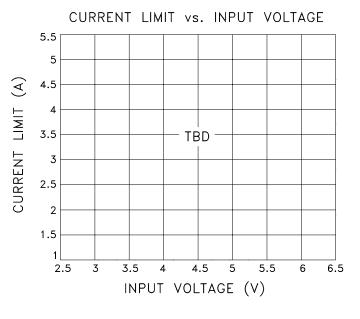
Overcurrent protection is provided by the MSK 5826RH series through the use of a timed latch off circuit. The internal latch timeout is triggered by an overcurrent condition. To allow for start up surge currents, the timeout is approximately TBDmS at 25°C. If the overcurrent condition remains at the end of the timeout cycle, the regulator will latch off until the latch is reset. To reset the latch, toggle the shutdown pin high then low or cycle VIN off then back on. A thermal limit condition will trigger the latch with no time out delay.

# INPUT POWER SUPPLY BYPASSING

The MSK 5826RH contains an internal 4.7 $\mu$ F tantalum input capacitor, type CWR19HB475KCBB. The capacitor is rated for 15V maximum applied voltage and has been surge tested in accordance with condition B of MIL-PRF-55365. The system designer must ensure proper de-rating in accordance with their system requirements. To maximize transient response and minimize power supply transients it is recommended that a 100 $\mu$ F tantalum capacitor is connected between VIN and ground. A 0.1 $\mu$ F ceramic capacitor should also be used for high frequency bypassing.

# OUTPUT CAPACITOR SELECTION

The MSK 5826RH contains three internal 4.7µF tantalum output capacitors, type CWR19HB475KCBB. The capacitors are rated for 15V maximum applied voltage and have been surge tested in accordance with condition B of MIL-PRF-55365. The system designer must ensure proper de-rating in accordance with their system requirements. Output capacitors are required to maintain regulation and stability. A 220µF surface mount tantalum capacitor from the output to ground should suffice under most conditions. Ceramic output capacitors (0.1µF typical) should be placed directly across the load power connections as close to the load as possible. If the user finds that tighter voltage regulation is needed during output transients, more capacitance may be added. If more capacitance is added to the output, the bandwidth may suffer.



#### FIGURE 1

# CURRENT LIMIT AND SOA

The MSK 5826RH current limit function is directly affected by the input and output voltages. Figure 1 illustrates the relationship between VIN and ICL for various output voltages. It is very important for the user to consult the SOA curve when using input voltages which result in current limit conditions beyond 3.5 Amps. When using input voltages which result in current limit above 3.5 Amps, the user must maintain output current within the SOA curve to avoid damage to the device. The current limit is adjusted internally for an input voltage of 5V.

# **APPLICATION NOTES CONT.**

#### THERMAL LIMITING

The MSK 5826RH control circuitry has a thermal shutdown temperature of approximately 150°C. This thermal shutdown can be used as a protection feature, but for continuous operation, the junction temperature of the pass transistor must be maintained below 150°C. Proper heat sink selection is essential to maintain these conditions. Exceeding the thermal limit activates the latch feature of the MSK 5826RH. Toggle the shutdown pin high then low or cycle power to reset the latch. See shutdown pin description and overcurrent latch description for more information.

#### HEAT SINK SELECTION

To select a heat sink for the MSK 5826RH, the following formula for convective heat flow may be used.

#### Governing Equation:

 $T_{J} = P_{D} X (R_{\theta JC} + R_{\theta CS} + R_{\theta SA}) + T_{A}$ 

Where

TJ= Junction TemperaturePD= Total Power DissipationRθJC= Junction to Case Thermal ResistanceRθCS= Case to Heat Sink Thermal ResistanceRθSA= Heat Sink to Ambient Thermal ResistanceTA= Ambient Temperature

Power Dissipation = (VIN-VOUT) x lout

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is  $150^{\circ}$  C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (ResA).

#### Example:

An MSK 5826RH is connected for  $V_{IN}$  = + 5.0V and  $V_{OUT}$  = + 3.3V. IOUT is a continuous 3A DC level. The ambient temperature is + 25°C. The maximum desired junction temperature is + 125°C.

 $R_{\theta JC}\!=\!6.5^{\,\circ}\,C/W$  and  $R_{\theta CS}\!=\!0.15^{\,\circ}\,C/W$  for most thermal greases

Power Dissipation= (5.0V-3.3V) x (3A) = 5.1 Watts

Solve for Resa:

$$R_{\theta}SA = \left[\frac{125^{\circ}C - 25^{\circ}C}{5.1W}\right] - 6.5^{\circ}C/W - 0.15^{\circ}C/W$$
$$= 12.9^{\circ}C/W$$

In this example, a heat sink with a thermal resistance of no more than 12.9° C/W must be used to maintain a junction temperature of no more than 125° C.

# TYPICAL APPLICATIONS CIRCUIT

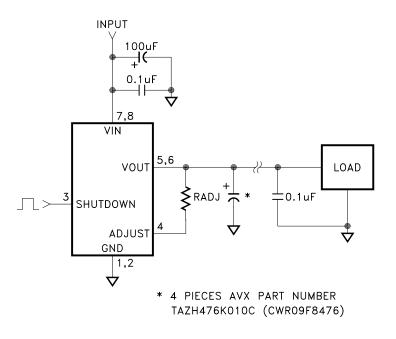


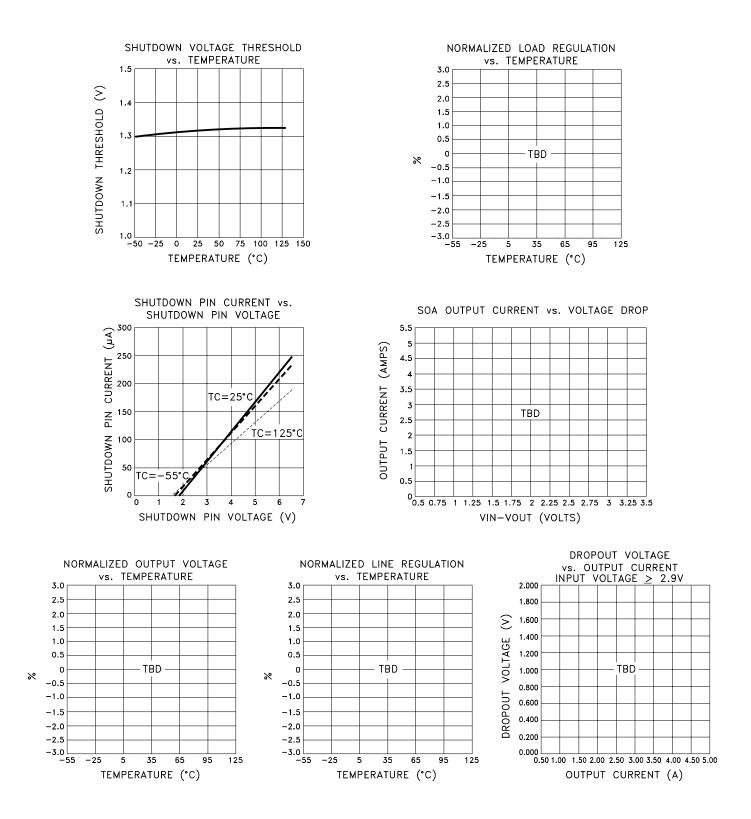
FIGURE 2

# TOTAL DOSE RADIATION TEST PERFORMANCE

Radiation performance curves for TID testing have been generated for all radiation testing performed by MS Kennedy. These curves show performance trends throughout the TID test process and are located in the MSK 5826RH radiation test report. The complete radiation test report is available in the RAD HARD PROD-UCTS section on the MSK website.

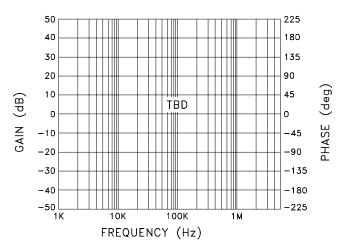
http://www.mskennedy.com/store.asp?pid=9951&catid=19680

# TYPICAL PERFORMANCE CURVES

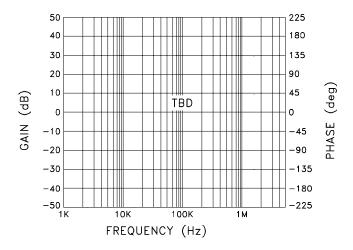


# TYPICAL PERFORMANCE CURVES

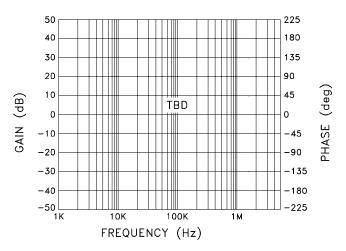
#### MSK5826RH GAIN AND PHASE vs. FREQUENCY



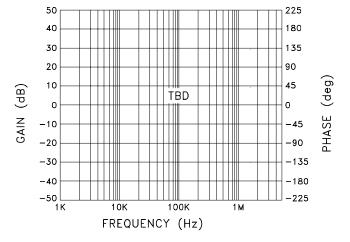
MSK5826RH GAIN AND PHASE vs. FREQUENCY



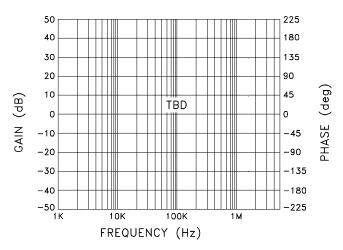
MSK5826RH GAIN AND PHASE vs. FREQUENCY



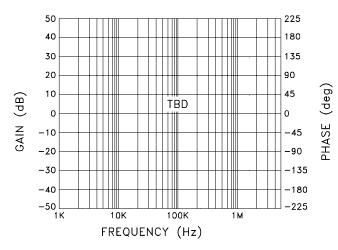
MSK5826RH GAIN AND PHASE vs. FREQUENCY



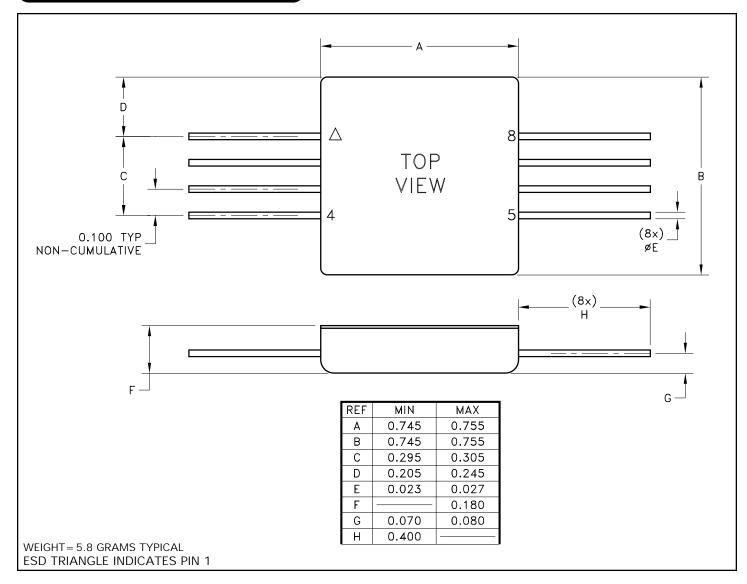
MSK5826RH GAIN AND PHASE vs. FREQUENCY



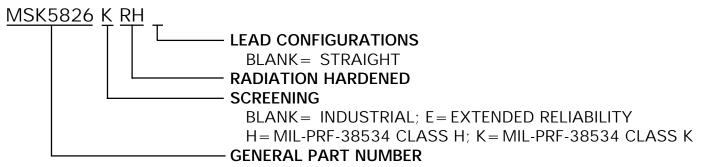
MSK5826RH GAIN AND PHASE vs. FREQUENCY



# MECHANICAL SPECIFICATIONS

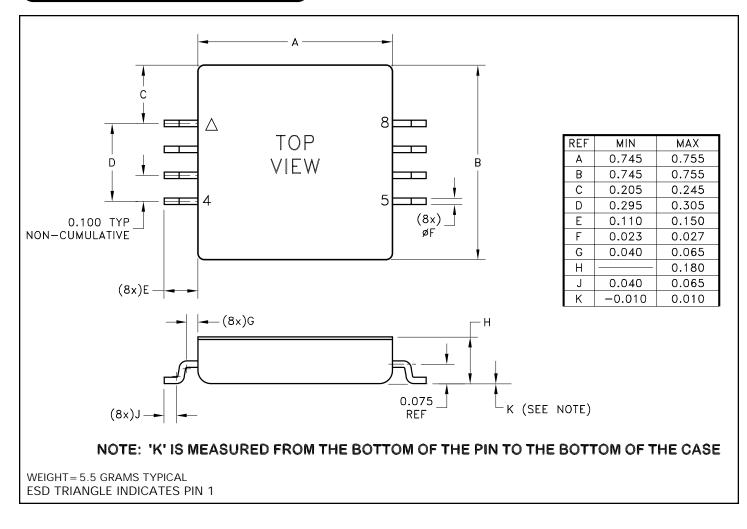


# ORDERING INFORMATION

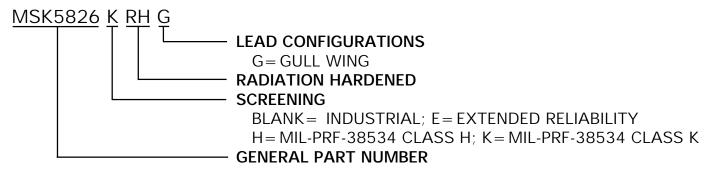


The above example is a Class K regulator with straight leads. NOTE: See DSCC SMD 5962F09210 for DSCC part number options.

# MECHANICAL SPECIFICATIONS



# ORDERING INFORMATION



The above example is a Class K regulator with gull wing formed leads. NOTE: See DSCC SMD 5962F09210 for DSCC part number options.

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Contact MSK for MIL-PRF-38534 Class H, Class K status.