# ACS244MS

## Radiation Hardened Non-Inverting, Octal, Three-State Buffer/Line Driver

March 1998

#### Features

- QML Qualified Per MIL-PRF-38535 Requirements
- 1.25Micron Radiation Hardened SOS CMOS
- Radiation Environment
  - Latch-up Free Under any Conditions
  - Total Dose . . . . . . . . . . . . . . . . . . 3 x 10<sup>5</sup> RAD(Si) - SEU Immunity . . . . . . <1 x 10<sup>-10</sup> Errors/Bit/Day
  - SEU LET Threshold . . . . . . . >100MeV/(mg/cm<sup>2</sup>)
- Input Logic Levels .  $V_{IL} = (0.3V)(V_{CC})$ ,  $V_{IH} = (0.7V)(V_{CC})$
- Quiescent Supply Current......20μA
- Propagation Delay ...... 9.0ns

## **Applications**

- Databus Driving
- Data Routing
- Redundant Data Control Circuitry

## Description

The Radiation Hardened ACS244MS is a Non-Inverting, Octal, Three-State Buffer/Line Driver with two active-LOW Enable inputs ( $\overline{AE}$  and  $\overline{BE}$ ). Each Enable input controls four outputs. When an Enable input is LOW, the corresponding outputs are active. A HIGH on an Enable input causes the corresponding outputs to be high impedance, regardless of the input levels.

The ACS244MS is fabricated on a CMOS Silicon on Sapphire (SOS) process, which provides an immunity to Single Event Latch-up and the capability of highly reliable performance in any radiation environment. These devices offer significant power reduction and faster performance when compared to ALSTTL types.

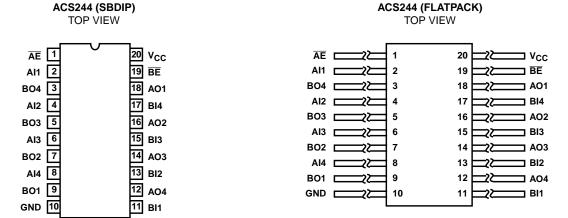
Specifications for Rad Hard QML devices are controlled by the Defense Supply Center in Columbus (DSCC). The SMD numbers listed below must be used when ordering.

Detailed Electrical Specifications for the ACS244 are contained in SMD 5962-98541. A "hot-link" is provided on our homepage with instructions for downloading. http://www.intersil.com/data/sm/index.htm

## **Ordering Information**

SMD PART NUMBER	INTERSIL PART NUMBER	TEMP. RANGE (°C)	PACKAGE	CASE OUTLINE
5962F9854101VRC	ACS244DMSR-02	-55 to 125	20 Ld SBDIP	CDIP2-T20
N/A	ACS244D/Sample-02	25	20 Ld SBDIP	CDIP2-T20
5962F9854101VXC	ACS244KMSR-02	-55 to 125	20 Ld Flatpack	CDFP4-F20
N/A	ACS244K/Sample-02	25	20 Ld Flatpack	CDFP4-F20
N/A	ACS244HMSR-02	25	Die	N/A

### **Pinouts**



#### ACS244MS

#### Die Characteristics

#### **DIE DIMENSIONS:**

Size:  $2540\mu m \times 2540\mu m$  (100 mils x 100mils) Thickness:  $525\mu m \pm 25\mu m$  (20.6 mils  $\pm 1$  mil) Bond Pad:  $110\mu m \times 110\mu m$  (4.3 x 4.3 mils)

METALLIZATION: AI

Metal 1 Thickness:  $0.7\mu m \pm 0.1\mu m$  Metal 2 Thickness:  $1.0\mu m \pm 0.1\mu m$ 

#### **SUBSTRATE POTENTIAL:**

Unbiased Insulator

#### **PASSIVATION**

Type: Phosphorous Silicon Glass (PSG)

Thickness:  $1.30\mu m \pm 0.15\mu m$ 

#### **SPECIAL INSTRUCTIONS:**

Bond V<sub>CC</sub> First

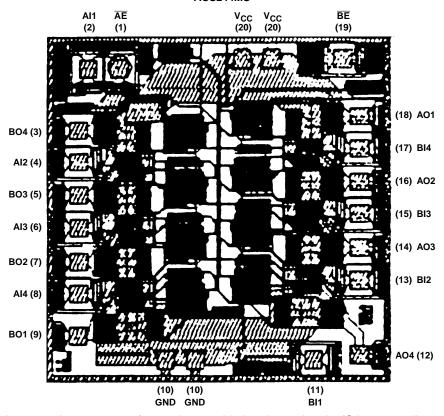
#### ADDITIONAL INFORMATION:

Worst Case Density: <2.0 x 10<sup>5</sup> A/cm<sup>2</sup>

Transistor Count: 214

## Metallization Mask Layout

#### ACS244MS



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## Sales Office Headquarters

#### **NORTH AMERICA**

Intersil Corporation P. O. Box 883, Mail Stop 53-204 Melbourne, FL 32902

TEL: (321) 724-7000 FAX: (321) 724-7240

#### **EUROPE**

Intersil SA Mercure Center 100, Rue de la Fusee 1130 Brussels, Belgium TEL: (32) 2.724.2111 FAX: (32) 2.724.22.05

#### **ASIA**

Intersil (Taiwan) Ltd.
Taiwan Limited
7F-6, No. 101 Fu Hsing North Road
Taipei, Taiwan
Republic of China

TEL: (886) 2 2716 9310 FAX: (886) 2 2715 3029