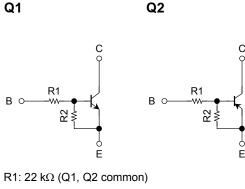
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN47A2

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications.

- Two devices are incorporated into an Ultra-Super-Mini (5 pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

Equivalent Circuit and Bias Resistor Values

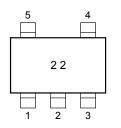


 2.1 ± 0.1 1.25 ± 0.1 0.65 2.0±0.2 1.3±0.1 0.15±0.05 .0±6.0 0~0 1. EMITTER 1 2. BASE 1 3. EMITTER 2 4. COLLECTOR 2 5. COLLECTOR 1 (E1) (B1) (E2) (C2) (C1) USV BASE 2 (B2) JEDEC _ JEITA _ TOSHIBA 2-2L1D

R1: 22 kΩ (Q1, Q2 common) R2: 22 kΩ (Q1, Q2 common) Q1: RN1103F

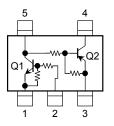
Q2: RN2103F

Marking



Weight:0.0062g (typ.)

Equivalent Circuit (top view)



Unit: mm

Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	10	V
Collector current	Ι _C	100	mA

Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-10	V
Collector current	Ι _C	-100	mA

Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P _C (Note)	200	mW
Junction temperature	Тj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

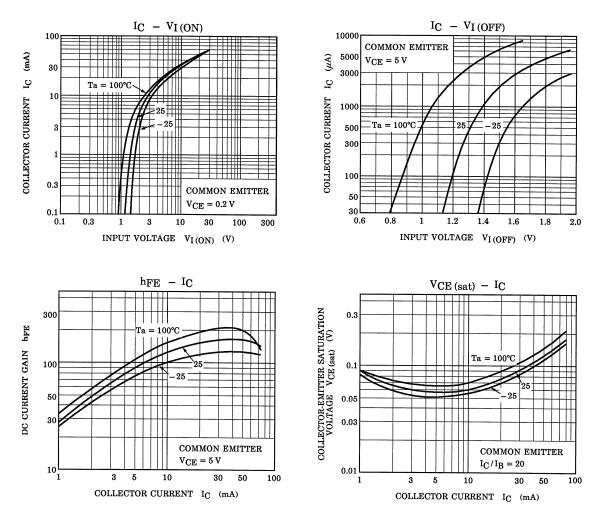
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 50 \text{ V}, \ I_E = 0$	_	_	100	nA
	I _{CEO}	$V_{CE} = 50 \text{ V}, I_B = 0$	_	_	500	11A
Emitter cut-off current	I _{EBO}	$V_{EB}=10~V,~I_C=0$	0.17	_	0.33	mA
DC current gain	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	70	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_{C} = 5 \text{ mA}, I_{B} = 0.25 \text{ mA}$		0.1	0.3	V
Input voltage (ON)	V _{I (ON)}	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.3		3.0	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ mA}$	1.0		1.5	V
Transition frequency	fT	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$	_	250	_	MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$	_	3	_	pF
Input resistor	R1	_	15.4	22	28.6	kΩ
Resistor ratio	R1/R2	—	0.8	1.0	1.2	

Electrical Characteristics (Ta = 25°C) (Q2)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB}=-50~V,~I_{E}=0$	_	_	-100	nA
	I _{CEO}	$V_{CE}=-50~V,~I_B=0$	_	_	-500	
Emitter cut-off current	I _{EBO}	$V_{EB}=-10~V,~I_C=0$	-0.17	_	-0.33	mA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	70	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V _{I (ON)}	$V_{CE}=-0.2 \ V, \ I_C=-5 \ mA$	-1.3	_	-3.0	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	-1.0	_	-1.5	V
Transition frequency	f _T	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$	_	3	_	pF
Input resistor	R1	—	15.4	22	28.6	kΩ
Resistor ratio	R1/R2	_	0.8	1.0	1.2	

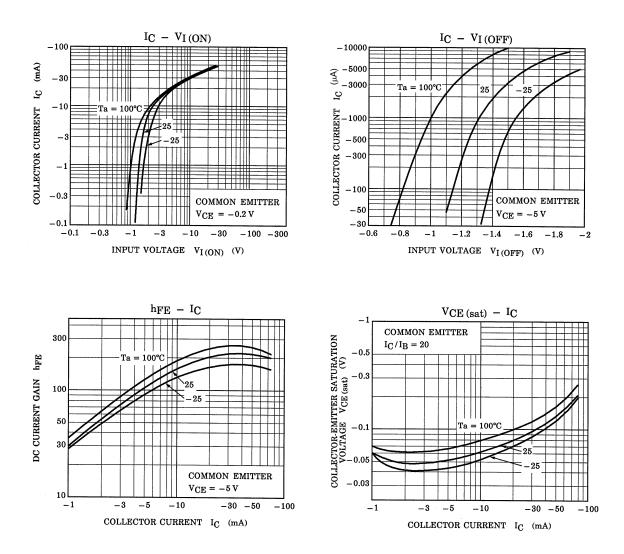
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Q1



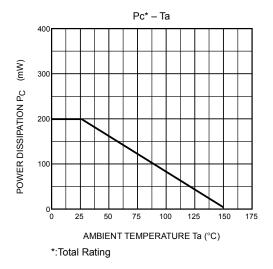
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Q2



TOSHIBA

Q1, Q2 Common



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