

STPS2045C-Y

Automotive power Schottky rectifier

Features

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Avalanche rated
- AEC-Q101 qualified

Description

Dual center tap Schottky rectifier suited for high frequency DC to DC converters.

Packaged in D²PAK, this device is especially intended for use in low voltage, high frequency inverters, free wheeling and polarity protection applications.

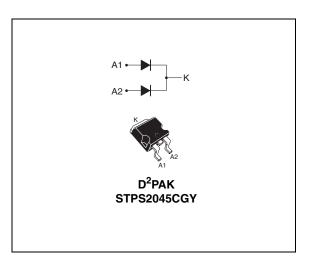


Table 1. Device summary

I _{F(AV)}	2 x 10 A
V _{RRM}	45 V
T _{j(max)}	175 °C
V _{F(typ)}	0.57 V

Characteristics STPS2045C-Y

1 Characteristics

Table 2. Absolute ratings (limiting values, per diode)

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive peak reverse voltage			45	V
I _{F(RMS)}	Forward rms current			30	Α
I _{F(AV)}	Average forward current $\delta = 0.5$	T _c = 155 °C Per diode		10	Α
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$			180	Α
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s$, $T_j = 25 °C$			4000	W
T _{stg}	Storage temperature range			-65 to +175	°C
T _j	Maximum operating junction temperature ⁽¹⁾			-40 to +175	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/µs

^{1.} $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistances parameters

Symbol	Parameter	Value	Unit	
R _{th(j-c)}	Junction to case	Per diode Total	2.2 1.3	°C/W
R _{th(c)}	Coupling		0.3	°C/W

When the diodes 1 and 2 are used simultaneously : $T_j(diode 1) = P(diode 1) \times R_{th(j-c)}(per diode) + P(diode 2) \times R_{th(c)}(per diode) + P(diode 2) \times R_{th(c)}(per diode) + P(diode 3) \times R_{th(c)}(per diode)$

Table 4. Static electrical characteristics (per diode)

Symbol	Test conditions			Min.	Тур.	Max.	Unit
I _B ⁽¹⁾	Reverse leakage current	T _j = 25 °C	$V_R = V_{RRM}$	-	-	100	μΑ
'R`	IR. / Theverse leakage current	T _j = 125 °C		-	7	15	mA
		T _j = 125 °C	I _F = 10 A	-	0.5	0.57	
V _F ⁽¹⁾ Forward voltage dr	Forward voltage drop	T _j = 25 °C	I _F = 20 A	-	-	0.84	V
		T _j = 125 °C		-	0.65	0.72	

^{1.} Pulse test : t_p = 380 μ s, δ < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.42 \times I_{F(AV)} + 0.015 I_{F}^{2}(RMS)$$

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Figure 1. Average forward power dissipation Figure 2. versus average forward current (per diode)

Average forward current versus ambient temperature (δ = 0.5, per diode)

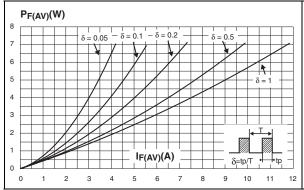
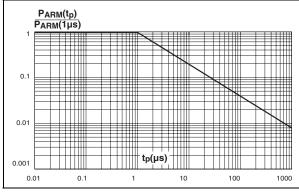


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature



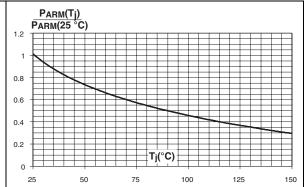
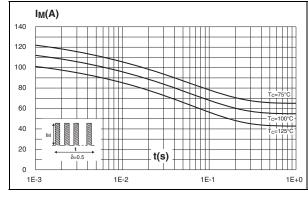
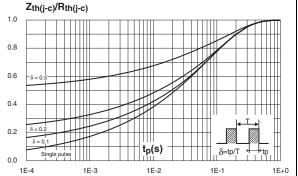


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration

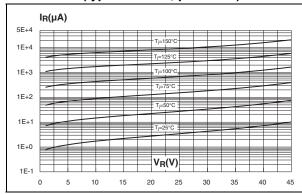




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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)



C(pF)

1000

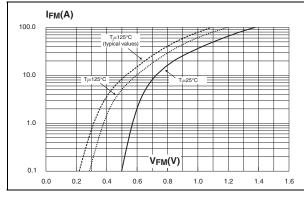
V_R(V)

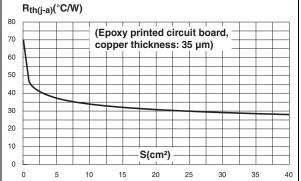
100

1 2 5 10 20 50

Figure 9. Forward voltage drop versus forward current (maximum values, per diode)

Figure 10. Thermal resistance junction to ambient versus copper surface under tab





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2 Package information

- Epoxy meets UL94, V0
- Lead-free package

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Table 5. D²PAK dimensions

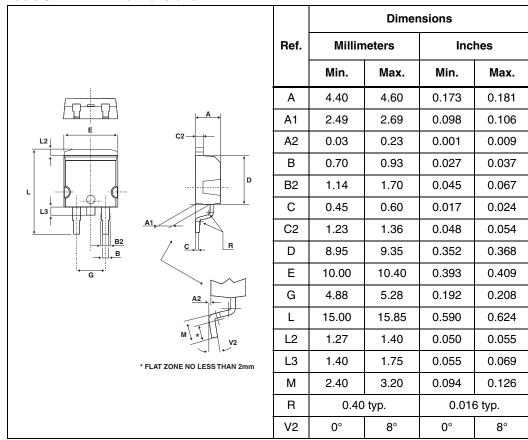
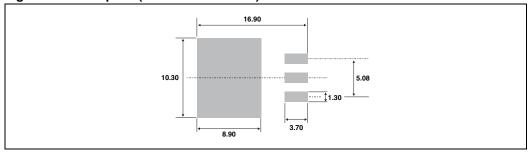


Figure 11. Footprint (dimensions in mm)





Ordering information STPS2045C-Y

3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS2045CGY-TR	STPS2045CGY	D ² PAK	1.48 g	1000	Tape and reel

4 Revision history

Table 7. Document revision history

Date	Revision	Changes
24-May-2011	1	Initial release.

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