New Product

ESH2B, ESH2C & ESH2D

Vishay General Semiconductor

## **Surface Mount Ultrafast Plastic Rectifier**



DO-214AA (SMB)

## FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power loss
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

### **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converter and inverter for both consumer and automotive.

### **MECHANICAL DATA**

Case: DO-214AA (SMB)

Epoxy meets UL 94V-0 flammability rating

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for commercial grade, meets JESD 201 class 1A whisker test, HE3 suffix for high reliability grade (AEC Q101 qualified), meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	ESH2B	ESH2C	ESH2D	UNIT	
Device marking code		EHB	EHC	EHD		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	150	200	V	
Maximum RMS voltage	V <sub>RMS</sub>	70	105	140	V	
Maximum DC blocking voltage	V <sub>DC</sub>	100	150	200	V	
Maximum average forward rectified current (Fig. 1)	I <sub>F(AV)</sub>	2.0			А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60			А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 175			°C	

I <sub>F(AV)</sub> 2 A				
V <sub>RRM</sub>	100 V, 150 V, 200 V			
t <sub>rr</sub>	25 ns			
V <sub>F</sub>	0.93 V			
T <sub>J</sub> max.	175 °C			

DRIMARY CHARACTERISTICS



**RoHS** 



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
Maximum instantaneous forward voltage (1)	I <sub>F</sub> = 2 A	V <sub>F</sub>	0.93	V				
Maximum DC reverse current at rated DC blocking voltage	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	2.0 50	μΑ				
Maximum reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	25	ns			
Typical reverse recovery time	I <sub>F</sub> = 2 A, V <sub>R</sub> = 30 V, dI/dt = 50 A/µs, I <sub>rr</sub> = 10 % I <sub>RM</sub>	T <sub>J</sub> = 25 °C T <sub>J</sub> = 100 °C	t <sub>rr</sub>	35 55	ns			
Typical stored charge	$      I_F = 2 \text{ A},  \text{V}_R = 30 \text{ V}, \\       dl/dt = 50 \text{ A}/\mu \text{s},  \text{I}_{rr} = 10 \text{ \% } \text{I}_{RM} \\       T_J = 100 \text{ °C} $		Q <sub>rr</sub>	20 35	nC			
Typical junction capacitance	4.0 V, 1 MHz		CJ	30	pF			

#### Note:

(1) Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	ESH2B ESH2C ESH2D		ESH2D	UNIT	
Typical thermal resistance <sup>(1)</sup>	$R_{ extsf{ heta}JA}$ $R_{ extsf{ heta}JL}$	65 20			°C/W	

#### Note:

(1) Units mounted on P.C.B. with 8.0 x 8.0 mm land areas.

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
ESH2D-E3/52T	0.096	52T	750	7" diameter plastic tape and reel		
ESH2D-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel		
ESH2DHE3/52T (1)	0.096	52T	750	7" diameter plastic tape and reel		
ESH2DHE3/5BT <sup>(1)</sup>	0.096	5BT	3200	13" diameter plastic tape and reel		

#### Note:

(1) Automotive grade AEC Q101 qualified

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

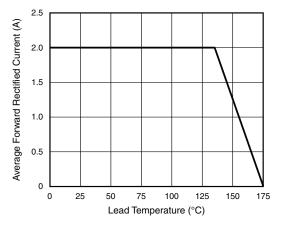


Figure 1. Maximum Forward Current Derating Curve

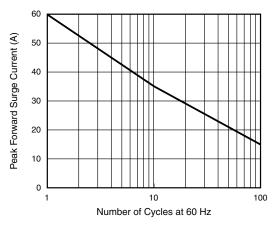


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



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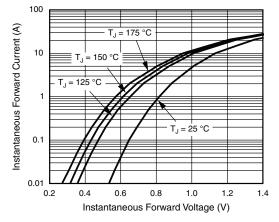


Figure 3. Typical Instantaneous Forward Characteristics

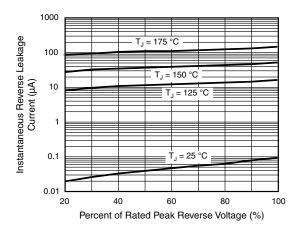


Figure 4. Typical Reverse Leakage Characteristics

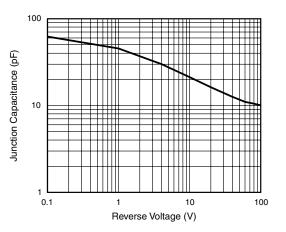


Figure 5. Typical Junction Capacitance

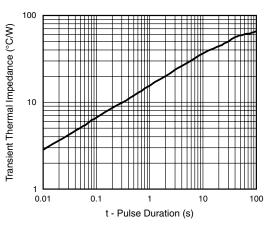
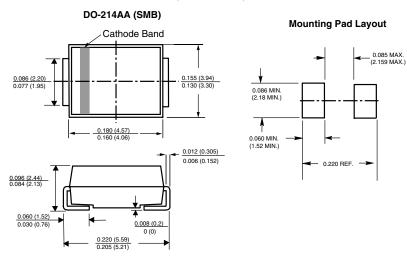


Figure 6. Typical Transient Thermal Impedance

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com



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