

Vishay Semiconductors formerly General Semiconductor



Surface Mount Glass Passivated Junction Rectifiers

Reverse Voltage 50 to 600V Forward Current 0.5A

Patented **DO-213AA** SOLDERABLE ENDS 1st BAND 2nd BAND 0.066 0.060 (1.676) (1.524)0.022 (0.559) 0.016 (0.406) D2 = D1 + 0 - 0.008 (0.20)0.145 (3.683) 0.131(3.327) 1st band denotes type and polarity 2nd band denotes voltage type Dimensions in inches and (millimeters)

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- For surface mount applications
- High temperature metallurgically bonded construction
- · Cavity-free glass passivated junction
- Capable of meeting environmental standards of MIL-S-19500
- Fast switching for high efficiency
- High temperature soldering guaranteed: 450°C/5 seconds at terminals. Complete device submersible temperature of 260°C for 10 seconds in solder bath

Mechanical Data

Case: JEDEC DO-213AA, molded plastic over glass body

Terminals: Plated terminals, solderable per

MIL-STD-750, Method 2026

Polarity: Two bands indicate cathode end – 1st band denotes device type and 2nd band denotes repetitive peak

reverse voltage rating

Mounting Position: Any

Weight: 0.0014 oz., 0.036 g

Maximum Ratings & Thermal Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

| Standard recovery device: first band is white | Symbol | GL34A | GL34B | GL34D | GL34G | GL34J | Unit |
|--|--|-------------|-------|--------|--------|-------|------|
| Polarity color bands (2nd Band) | | Gray | Red | Orange | Yellow | Green | |
| Maximum repetitive peak reverse voltage | VRRM | 50 | 100 | 200 | 400 | 600 | V |
| Maximum RMS voltage | VRMS | 35 | 70 | 140 | 280 | 420 | V |
| Maximum DC blocking voltage | VDC | 50 | 100 | 200 | 400 | 600 | V |
| Maximum average forward rectified current at T _T = 75°C | IF(AV) | 0.5 | | | | А | |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | IFSM | 10 | | | | | А |
| Max. full load reverse current, full cycle average T _A = 55°C | I _{R(AV)} | 30 | | | | μΑ | |
| Maximum thermal resistance (Note 1) (Note 2) | R _θ JA R _θ JT | 150 70 | | | °C/W | | |
| Operating junction and storage temperature range | TJ, TSTG | -65 to +175 | | | | °C | |

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

| Maximum instantaneous forward voltage at 0.5A | VF | 1.2 | 1.3 | V |
|--|-----------------|-----------|-----|----|
| Maximum DC reverse currentT _A = 25°C at rated DC blocking voltageT _A = 125°C | IR | 5.0 50 | | μΑ |
| Typical reverse recovery time at I _F = 0.5A, I _R = 1.0A, I _{rr} = 0.25A | t _{rr} | 1.5 | | μs |
| Typical junction capacitance at 4.0V, 1MHz | CJ | 4.0 | | pF |

Notes

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^{*}Glass-plastic encapsulation is covered by Patent No. 3,996,602 and brazed-lead assembly to Patent No. 3,930,306

⁽¹⁾ Thermal resistance from junction to ambient, 0.2 x 0.2" (5.0 x 5.0mm) copper pads to each terminal

⁽²⁾ Thermal resistance from junction to terminal, 0.2 x 0.2" (5.0 x 5.0mm) copper pads to each terminal

GL34A thru GL34J

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Ratings and Characteristic Curves (TA = 25°C unless otherwise noted)

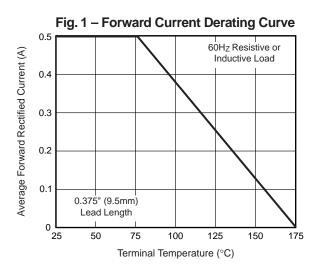
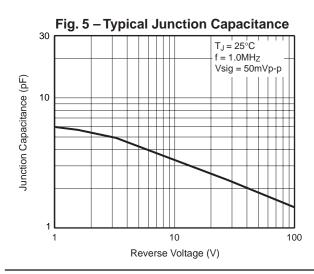


Fig. 3 - Typical Instantaneous Forward Characteristics 10 Instantaneous Forward Current (A) $T_J = 25^{\circ}C$ 0.1 Pulse Width = 300μs 1% Duty Cycle 0.01 0.6 0.4 8.0 1.0 1.6 Instantaneous Forward Voltage (V)





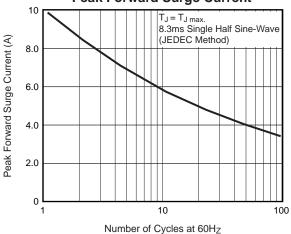
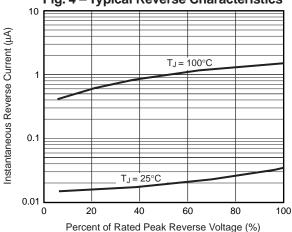


Fig. 4 - Typical Reverse Characteristics



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